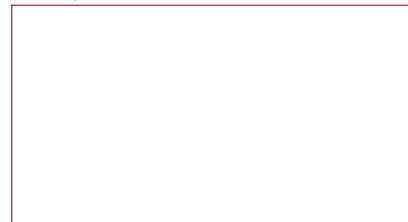


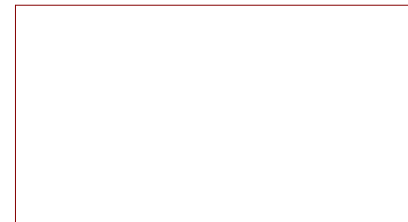
File: pwr.kicad_sch



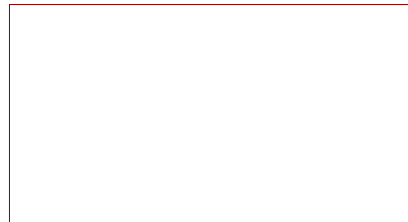
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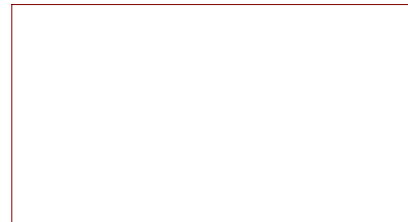
File: memory.kicad_sch



File: coreio.kicad_sch



File: more power.kicad_sch



File: audio.kicad_sch

BerryCore is the SoC and it's additional parts: power supply, PMIC and eMCP. Other BerryFlight designs implement the core as a drop in with standardized BC_ net names.



BerryFlight (Daniel Heymann)

Sheet: /

File: core.kicad_sch

Title: BerryCore

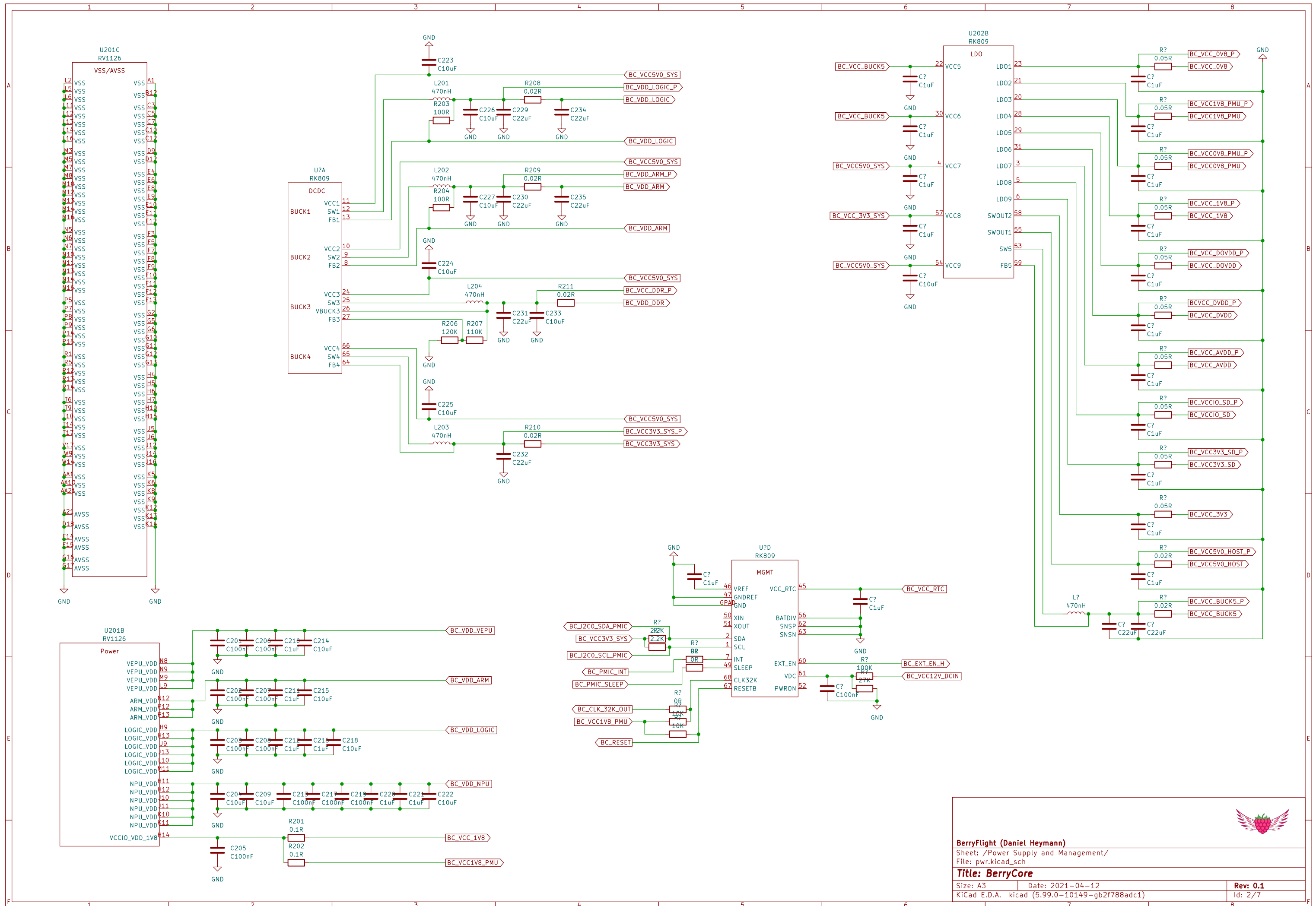
Size: A4

Date: 2021-04-12

Rev: 0.1

Size: 7.1	Date: 2021-07-12
KiCad E.D.A.	kicad (5.99.0-10149-gb2f788adc1)

Id: 1/7



BerryFlight (Daniel Heymann)

Sheet: /Power Supply and Management/
File: pwr.kicad_sch

Title: **BerryCore**

Size: A3 Date: 2021-04-12
KiCad E.D.A. kicad (5.99.0-10149-gb2f788adc1)

Rev: 0.1
Id: 2/7

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D								
E								
F								

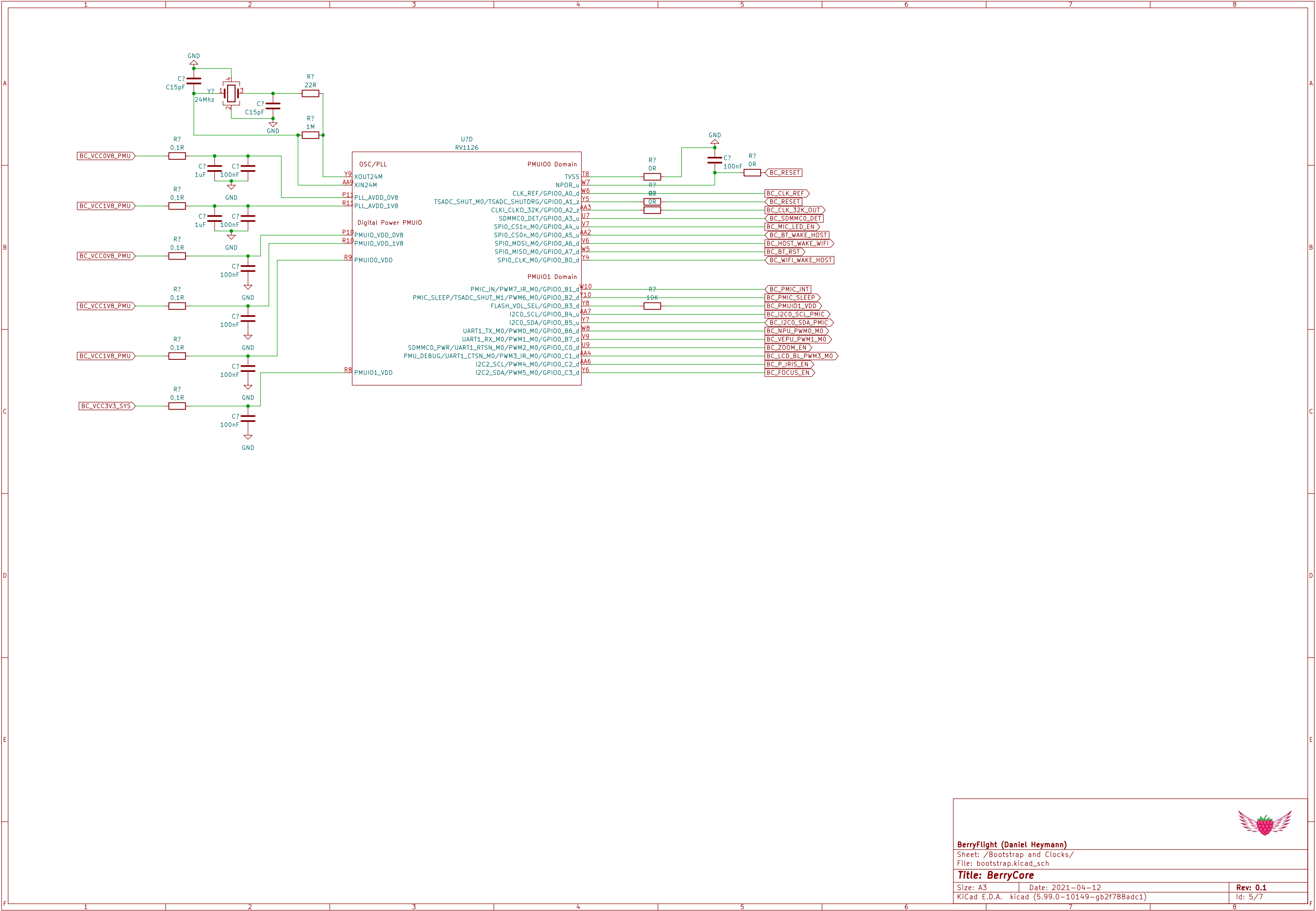


BerryFlight (Daniel Heymann)		
Sheet: /Storage and RAM/ File: memory.kicad_sch		
Title: BerryCore		
Size: A3	Date: 2021-04-12	Rev: 0.1
KiCad E.D.A. kicad (5.99.0-10149-gb2f788adc1)		Id: 3/7

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D								
E								
F								



BerryFlight (Daniel Heymann)		
Sheet: /Core IO/ File: coreio.kicad_sch		
Title: BerryCore		
Size: A3	Date: 2021-04-12	Rev: 0.1
KiCad E.D.A. kicad (5.99.0-10149-gb2f788adc1)	Id: 4/7	



	1	2	3	4	5	6	7	8
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D								
E								
F								



BerryFlight (Daniel Heymann)		
Sheet: /Audio/ File: audio.kicad_sch		
Title: BerryCore		
Size: A3	Date: 2021-04-12	Rev: 0.1
KiCad E.D.A. kicad (5.99.0-10149-gb2f788adc1)	Id: 6/7	

