PROJECT FUNCTION

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	Revision
1	
	Block Diagram (ROOT)
!	HI3518C
	DDR FLASH
;	MISC
	PHY
!	POW
9	CONNECTOR
i	i

SN	Function content	
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PROJECT FILE

SN	FILE							
1	HI3518C-BGA+OV9712_NVT_D11IP-MB_SCH							
2	HI3518C-BGA+OV9712_NVT_D111P-SEN_SCH							
3	HI3518C-BGA+OV9712_NVT_D111P-LED_SCH							

PIN		GPIO2_2		
ADC_CH0		GPIO2_3		
ADC_CH1	PTC	GPIO2_5		
PWM0	FAN ADJ			
PWM1	LED DIM	GPI05_1	LED SW	
GPI00_0	IR_SW	GPIO3_0	day sen	
GPI00_1		GPI03_1	night sen	
GPI00_2	GREEN_LED whith pow			
GPIO0_3	GREEN 2			
GPI00_4	BLUE 2			
GPI00_6	AU_SW			
GPIO4	MOTOR			

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Date	Rev	Description/Change List	Page	Date	Rev	Description/Change List	Page
14.07.03		base on D04IPF	ALL				8, 9
14.07.15	С	CON902 pin 3 change from GND to +3V3, change LED driver methode	9				
14.07.28		add R457 R458 R459 to GPIO0_1 GPIO0_4 C631 change to 0.1uF	9				
14.09.02	D	DC/DC change to MP1470 for no need diode and low price delete AT88SC0104 delete BA10358 for no need for previous version	8 6 6				
14.09.28		GPIO5_2 change to PWM_OUTO GPIO5_3 change to PWM_OUT1 sensor switch to GPIO3_6 7	4 4				
14.10.23		GPIO3_0 GPIO3_1 control fan runing forward or reverse remove state LED control use triode LED driver input change to vin.					

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