PROJECT FUNCTION

PAGE	Content
2 3 4 5 6 7 8	Cover Page Revision Block Diagram (ROOT) HI3518E FLASH MISC PHY POW CONNECTOR

SN	Function content					
1						
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PROJECT FILE

SN	FILE
1	HI3518E+OV9712_NVT_F08IP*-MB_SCH_CADENCE16P5_*
	NVT_F08IP*-IR_SCH_CADENCE16P5_*
۷	NVI_FOOTF -IN_SCH_CADENCETOFS_
1	

					[[
PIN		 	 			
ADC_CH0	CDS_MCU					
GPIO0_0	IR_SW					
GPI00_1	BT_RST					
GPIO0_3	GREEN 2					
GPIO0_4	BLUE 2					
GPI00_6	AU_SW	 	 			
				GPI07_6	IRC1	
				GPI07_7	IRC2	

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Title	HI3518E IPCAM FOR NVT, LOW	COST, USE BOXING	STRUCTURE	
Size A3	Document Number F05IP-MB	Department R & D	Designer Stone Feng	Rev A
Date:	Thursday, July 16, 2015	Sheet 1	of 9	



Date	Rev	Description/Change List	Page	Date	Rev	Description/Change List	Page
15.04.14		base on F05IPA-MB. use boxing structure.	ALL				
	A						
		GPIOO_3 作为模拟SCL					
15.07.15	С	GPI00_4 作为模拟SDA					
		GPIOO_4 作为模拟SDA					













