PROJECT FUNCTION

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PAGE	Content
1	Cover Page
2	Revision
3	Block Diagram (ROOT)
4	HI3518E
5	FLASH
6	MISC
7	PHY
8	POW
9	CONNECTOR

PROJECT FILE

SN	FILE
1	HI3518E_NVT_F05IP*-MB_SCH_CADENCE16P5_*
2	OV9712_NVT_F05IP*-SEN_SCH_CADENCE16P5_*
3	NVT_F05IP*-IR_SCH_CADENCE16P5_*

PIN			GPIO5_4	H_ORG_LIMIT				
ADC_CH0	CDS_MCU		GPIO5_5	H_TML_LIMIT				
			GPIO5_6	V_ORG_LIMIT				
GPIO5_2			GPIO5_7	V_TML_LIMIT				
GPIO0_0	IR_SW							
GPIO0_1	BT_RST		GPIO9_0	H0				
GPIO0_2	GREEN_LED whith pow		GPIO9_1	H1				
GPIO0_3	GREEN 2		GPIO9_2	H2				
GPIO0_4	BLUE 2		GPIO9_3	H3				
GPIO0_5	ETH_RST		GPIO9_4	V0				
GPIO0_6	AU_SW		GPIO9_5	V1				
			GPIO9_6	V2		GPIO7_6	IRC1	
			GPIO9_7	V3		GPIO7_7	IRC2	

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Revision

Date	Rev	Description/Change List	Page
15.04.08	A	base on F02IPA-MB. refer to z-ben BOM and cost down.	ALL
15.04.30 15.05.29	B	add push up resistor for running limit R438-R441. add resister R901-R915 for CCIR656	

Date	Rev	Description/Change List	Page

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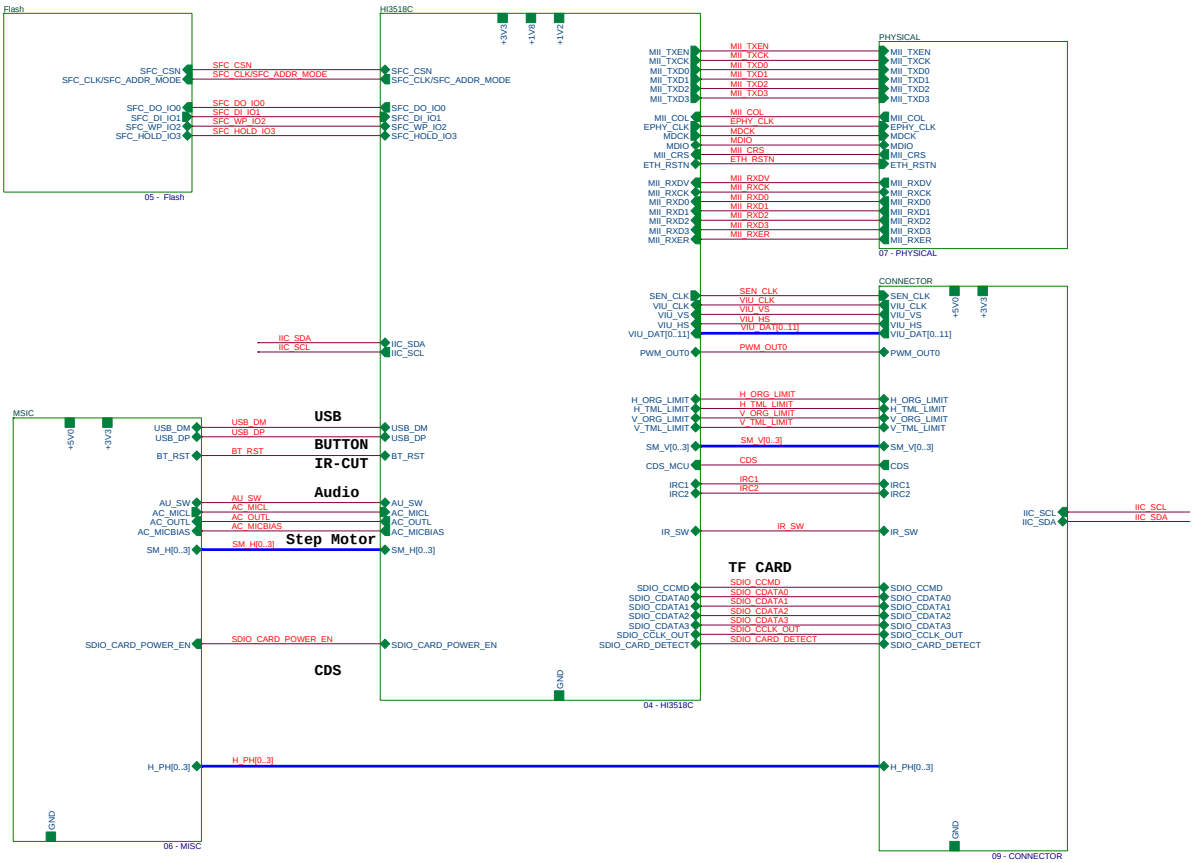

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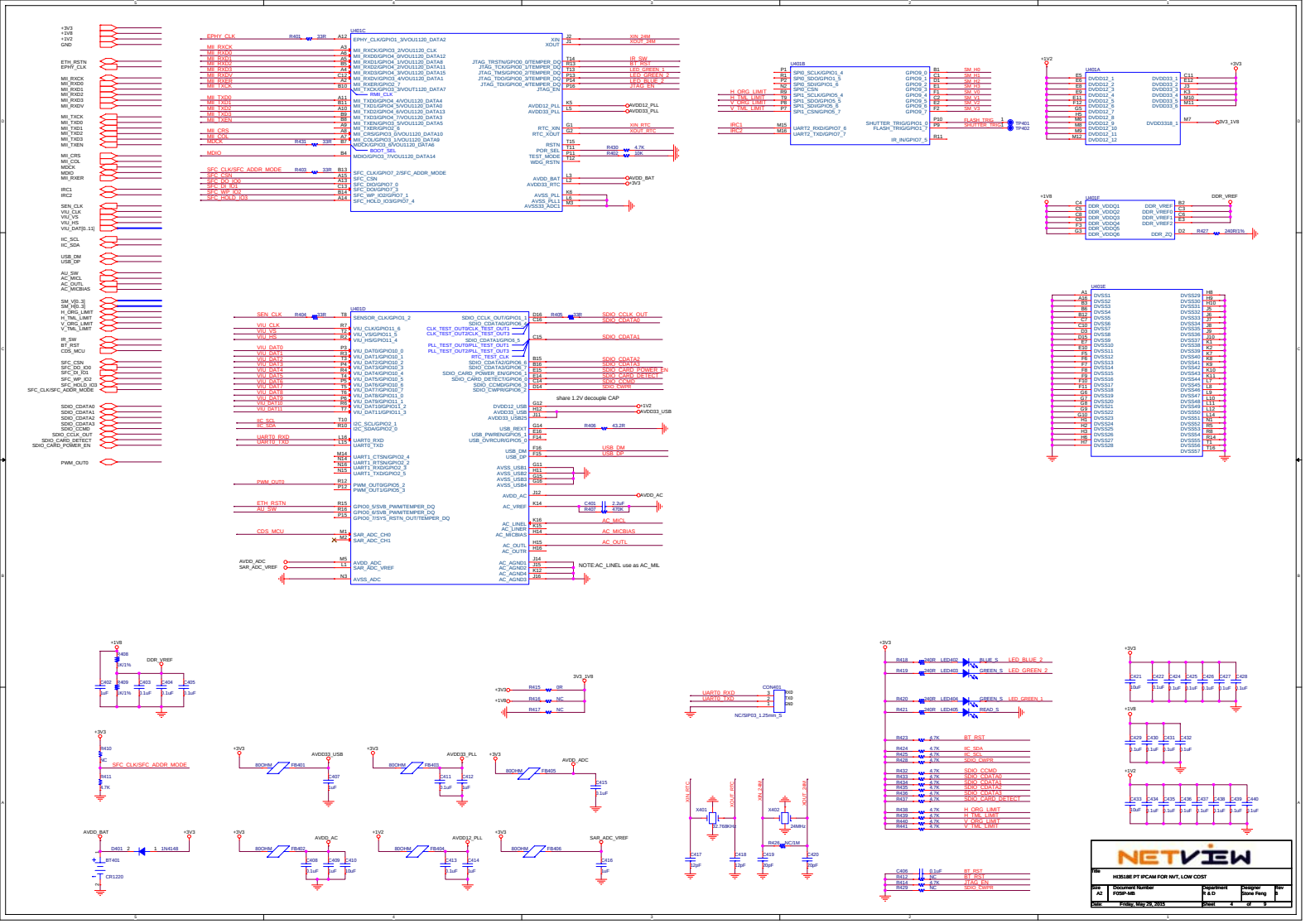
+3V3
+1V8
+1V2
GND
ETH_RSTN
EPHY_CLK
MII_RXCK
MII_RXD0
MII_RXD1
MII_RXD2
MII_RXD3
MII_RXDV
MII_TXCK
MII_TXD0
MII_TXD1
MII_TXD2
MII_TXD3
MII_TXEN
MII_CRS
MII_COL
MDCK
MDIO
IRC1
IRC2
SEN_CLK
VIU_CLK
VIU_VS
VIU_HS
VIU_DAT[0..11]
IIC_SCL
IIC_SDA
USB_DM
USB_DP
AU_SW
AC_MIC1
AC_OUTL
AC_MICBIAS
SM_VID_3[
SM_H[0..3]
H_ORG_LIMIT
H_TML_LIMIT
V_ORG_LIMIT
V_TML_LIMIT
IR_SW
BT_RST
CDS_MCU
SFC_CSN
SFC_DO_IO0
SFC_DI_IO1
SFC_WP_IO2
SFC_HOLD_IO3
SFC_CLK/SFC_ADDR_MODE
SDIO_CDATA0
SDIO_CDATA1
SDIO_CDATA2
SDIO_CDATA3
SDIO_CCLK_OUT
SDIO_CARD_DETECT
SDIO_CARD_POWER_EN
PWM_OUT0
EPHY_CLK
MII_RXCK
MII_RXD0
MII_RXD1
MII_RXD2
MII_RXD3
MII_RXDV
MII_TXCK
MII_TXD0
MII_TXD1
MII_TXD2
MII_TXD3
MII_TXEN
MII_CRS
MII_COL
MDCK
MDIO
SFC_CLK/SFC_ADDR_MODE
SFC_CSN
SFC_DO/IO0
SFC_DI_IO1
SFC_WP_IO2
SFC_HOLD_IO3
U401C
EPHY_CLK/GPIO1_3/VOU1120_DATA2
MII_RXCK/GPIO3_2/VOU1120_CLK
MII_RXD0/GPIO4_3/VOU1120_DATA12
MII_RXD1/GPIO4_1/VOU1120_DATA8
MII_RXD2/GPIO4_2/VOU1120_DATA11
MII_RXD3/GPIO4_3/VOU1120_DATA15
MII_RXDV/GPIO3_4/VOU1120_DATA1
MII_RXER/GPIO2_7
MII_TXCK/GPIO3_3/VOUT1120_DATA7
RMII_CLK
MII_TXD0/GPIO4_4/VOU1120_DATA4
MII_TXD1/GPIO4_5/VOU1120_DATA0
MII_TXD2/GPIO4_6/VOU1120_DATA13
MII_TXD3/GPIO4_7/VOU1120_DATA3
MII_TXEN/GPIO3_5/VOU1120_DATA5
MII_TXER/GPIO2_6
MII_CRS/GPIO3_0/VOU1120_DATA10
MII_COL/GPIO3_1/VOU1120_DATA9
MDCK/GPIO3_6/VOU1120_DATA6
BOOT_SEL
MDIO/GPIO3_7/VOU1120_DATA14
SFC_CLK/GPIO7_2/SFC_ADDR_MODE
SFC_CSN
SFC_DIO/GPIO7_0
SFC_DO/GPIO7_3
SFC_WP_IO2/GPIO7_1
SFC_HOLD_IO3/GPIO7_4
XIN_24M
XOUT_24M
IR_SW
BT_RST
LED_GREEN_1
LED_GREEN_2
JTAG_EN
AVDD12_PLL
AVDD33_PLL
XIN_RTC
XOUT_RTC
AVDD_BAT
3V3
AVSS_PLL
AVSS33_ADC1
JTAG_TRST/GPIO0_0/TEMPER_DQ
JTAG_TCK/GPIO0_1/TEMPER_DQ
JTAG_TMS/GPIO0_2/TEMPER_DQ
JTAG_TDO/GPIO0_3/TEMPER_DQ
JTAG_TDI/GPIO0_4/TEMPER_DQ
U401B
SPI0_SCLK/GPIO1_4
SPI0_SDO/GPIO1_5
SPI0_SDI/GPIO1_6
SPI0_CSN
SPI1_SCLK/GPIO5_4
SPI1_SDO/GPIO5_5
SPI1_SDI/GPIO5_6
SPI1_CSN/GPIO5_7
UART2_RXD/GPIO7_6
UART2_TXD/GPIO7_7
IR_IN/GPIO7_5
SM_H0
SM_H1
SM_H2
SM_H3
SM_V0
SM_V1
SM_V2
SM_V3
FLASH_TRIG_1
SHUTTER_TRIG1
SHUTTER_TRIG2
H_ORG_LIMIT
H_TML_LIMIT
V_ORG_LIMIT
V_TML_LIMIT
IRC1
IRC2
U401A
DVDD12_1
DVDD12_2
DVDD12_3
DVDD12_4
DVDD12_5
DVDD12_6
DVDD12_7
DVDD12_8
DVDD12_9
DVDD12_10
DVDD12_11
DVDD12_12
DVDD33_1
DVDD33_2
DVDD33_3
DVDD33_4
DVDD33_5
DVDD33_6
DVDD33_7
DVDD3318_1
U401F
DDR_VDDQ1
DDR_VDDQ2
DDR_VDDQ3
DDR_VDDQ4
DDR_VDDQ5
DDR_VDDQ6
DDR_VREF1
DDR_VREF2
DDR_ZQ
240R/1%
U401E
DVSS1
DVSS2
DVSS3
DVSS4
DVSS5
DVSS6
DVSS7
DVSS8
DVSS9
DVSS10
DVSS11
DVSS12
DVSS13
DVSS14
DVSS15
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DVSS45
DVSS46
DVSS47
DVSS48
DVSS49
DVSS50
DVSS51
DVSS52
DVSS53
DVSS54
DVSS55
DVSS56
DVSS57
SEN_CLK
VIU_CLK
VIU_VS
VIU_HS
VIU_DAT0
VIU_DAT1
VIU_DAT2
VIU_DAT3
VIU_DAT4
VIU_DAT5
VIU_DAT6
VIU_DAT7
VIU_DAT8
VIU_DAT9
VIU_DAT10
VIU_DAT11
IIC_SCL
IIC_SDA
UART0_RXD
UART0_TXD
PWM_OUT0
ETH_RSTN
AU_SW
CDS_MCU
AVDD_ADC
U401D
SENSOR_CLK/GPIO1_2
VIU_CLK/GPIO11_6
VIU_VS/GPIO11_5
VIU_HS/GPIO11_4
VIU_DAT0/GPIO10_0
VIU_DAT1/GPIO10_1
VIU_DAT2/GPIO10_2
VIU_DAT3/GPIO10_3
VIU_DAT4/GPIO10_4
VIU_DAT5/GPIO10_5
VIU_DAT6/GPIO10_6
VIU_DAT7/GPIO10_7
VIU_DAT8/GPIO11_0
VIU_DAT9/GPIO11_1
VIU_DAT10/GPIO11_2
VIU_DAT11/GPIO11_3
IIC_SCL/GPIO2_1
IIC_SDA/GPIO2_0
UART0_RXD
UART0_TXD
UART1_CTSN/GPIO2_4
UART1_RTSN/GPIO2_2
UART1_RXD/GPIO2_3
UART1_TXD/GPIO2_5
PWM_OUT0/GPIO5_2
PWM_OUT1/GPIO5_3
GPIO9_5/SVB_PWM/TEMPER_DQ
GPIO6_6/SVB_PWM/TEMPER_DQ
GPIO0_7/SYS_RSTN_OUT/TEMPER_DQ
SAR_ADC_CH0
SAR_ADC_CH1
AVDD_ADC
SAR_ADC_VREF
AVSS_ADC
SDIO_CCLK_OUT/GPIO1_1
SDIO_CDATA0/GPIO6_4
CLK_TEST_OUT0/CLK_TEST_OUT1
CLK_TEST_OUT2/CLK_TEST_OUT3
SDIO_CDATA1/GPIO6_5
PLL_TEST_OUT0/PLL_TEST_OUT1
PLL_TEST_OUT2/PLL_TEST_OUT3
RTC_TEST_CLK
SDIO_CDATA2/GPIO6_6
SDIO_CDATA3/GPIO6_7
SDIO_CARD_POWER_EN/GPIO6_1
SDIO_CARD_DETECT/GPIO6_0
SDIO_CCMD/GPIO6_2
SDIO_CWPR/GPIO6_3
SDIO_CCLK_OUT
SDIO_CDATA0
SDIO_CDATA1
SDIO_CDATA2
SDIO_CDATA3
SDIO_CARD_DETECT
SDIO_CCMD
SDIO_CWPR
share 1.2V decouple CAP
DVDD12_USB
AVDD33_USB
AVDD33_USB25
USB_RES1
USB_PWREN/GPIO5_1
USB_OVRCUR/GPIO5_0
USB_DM
USB_DP
AVSS_USB1
AVSS_USB2
AVSS_USB3
AVSS_USB4
AVDD_AC
AC_VREF
AC_LINEL
AC_LINER
AC_MICBIAS
AC_OUTL
AC_OUTR
AC_AGND1
AC_AGND2
AC_AGND3
AC_AGND4
AVDD33_USB
AC_MIC1
AC_MICBIAS
AC_OUTL
NOTE:AC_LINEL use as AC_MIL
AVDD_ADC
SAR_ADC_VREF
AVSS_ADC
3V3_1V8
UART0_RXD
UART0_TXD
CON/001
NC/SIP03_1.25mm_S
LED_BLUE_2
LED_GREEN_2
LED_GREEN_1
READ_S
BT_RST
IIC_SDA
IIC_SCL
SDIO_CWPR
SDIO_CCMD
SDIO_CDATA0
SDIO_CDATA1
SDIO_CDATA2
SDIO_CDATA3
SDIO_CARD_DETECT
H_ORG_LIMIT
H_TML_LIMIT
V_ORG_LIMIT
V_TML_LIMIT
BT_RST
BT_ON
JTAG_EN
SDIO_CWPR
X401
32.768KHz
X402
24MHz
XIN_RTC
XOUT_RTC
XIN_24M
XOUT_24M
AVDD_BAT
D401
1N4148
BT401
CR1220
AVDD33_USB
AVDD33_PLL
AVDD_ADC
AVDD_AC
AVDD12_PLL
SAR_ADC_VREF
SFC_CLK/SFC_ADDR_MODE
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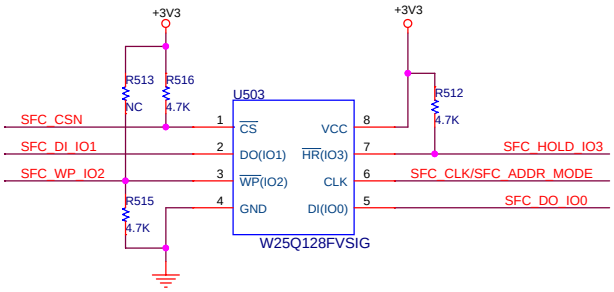
SFC_DO_IO0
SFC_DI_IO1
SFC_WP_IO2
SFC_HOLD_IO3
SFC_CSN
SFC_CLK/SFC_ADDR_MODE



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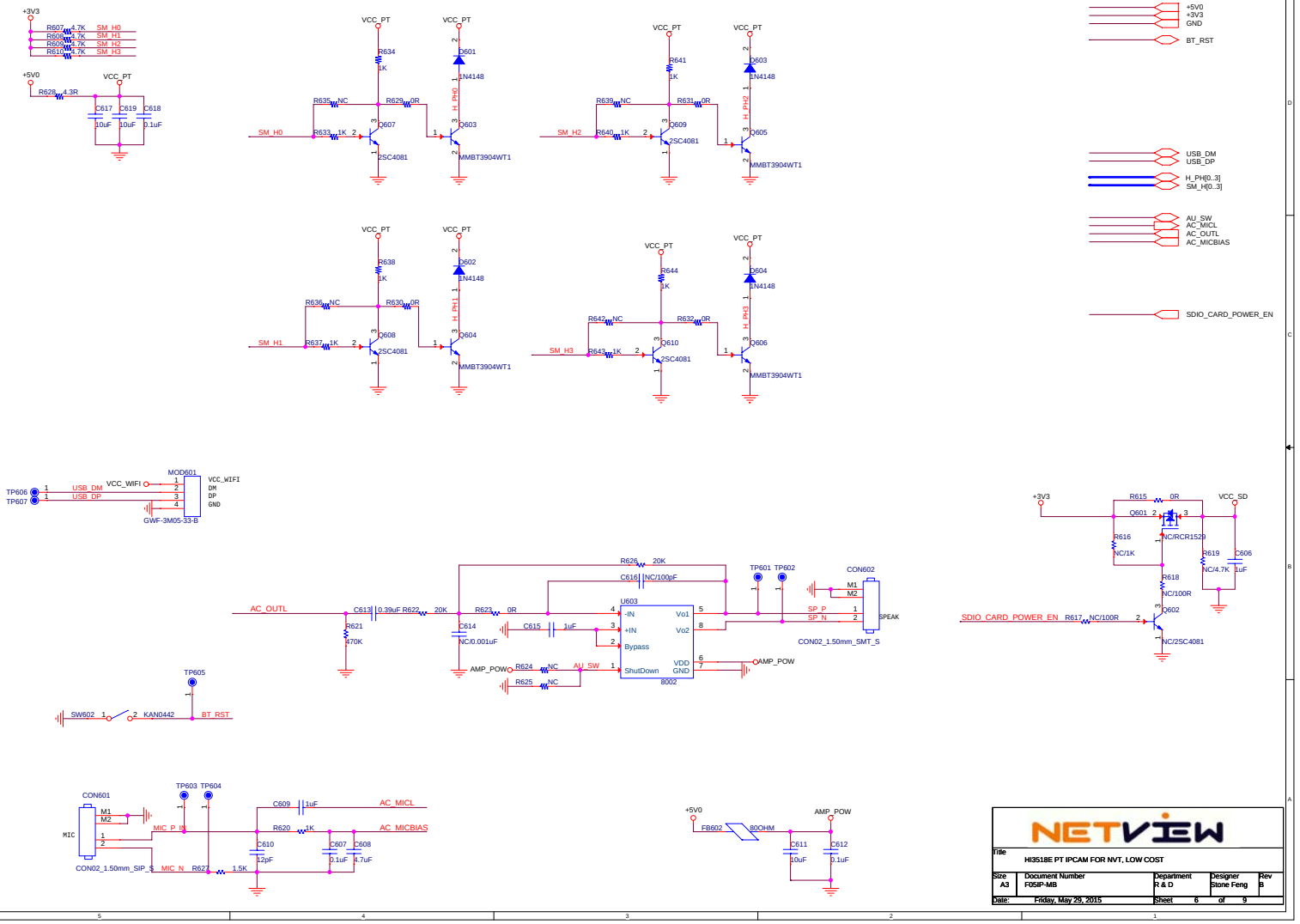

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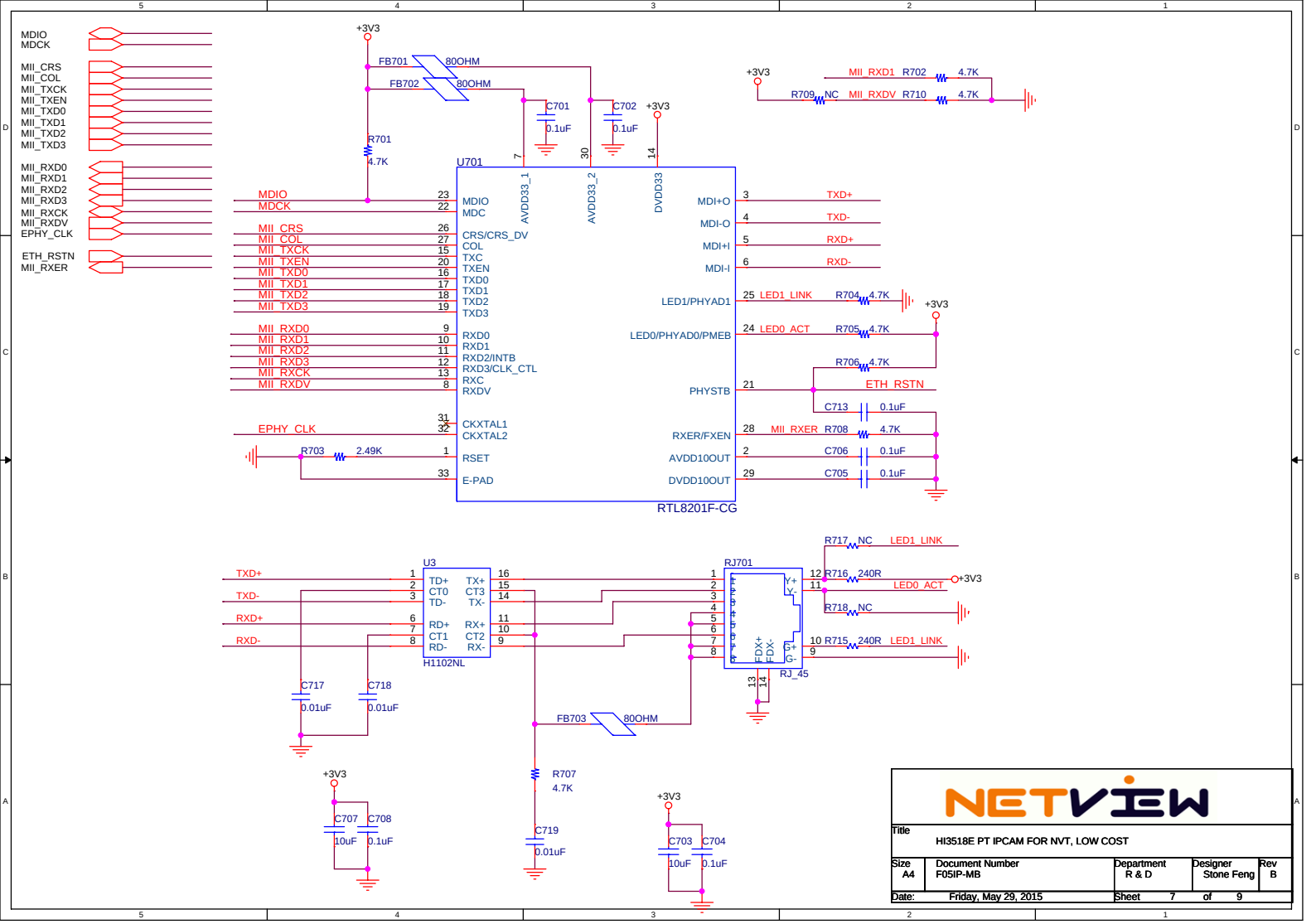

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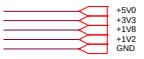

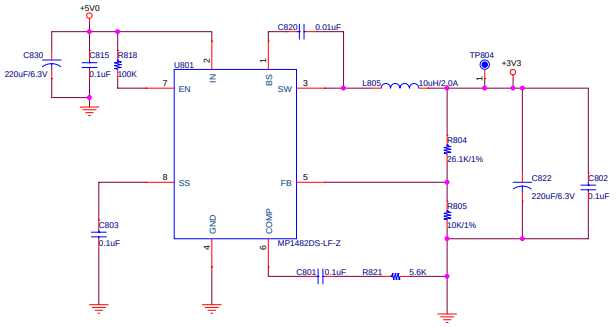

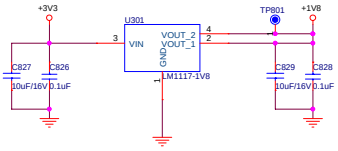

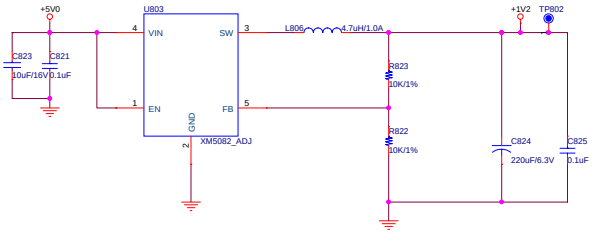

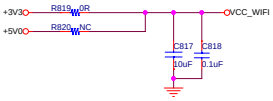

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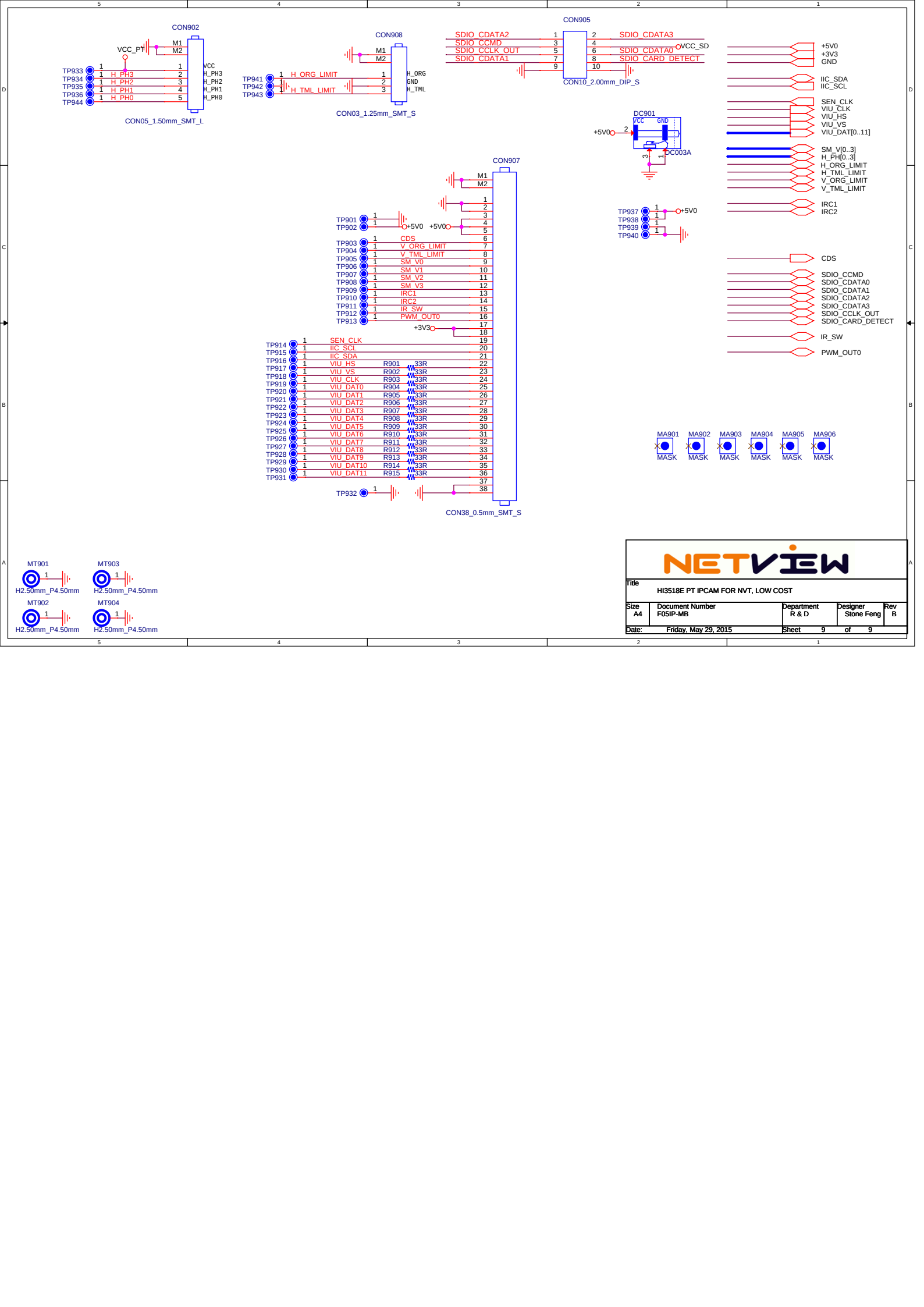
CON902
VCC_PT
M1
M2
TP933 1 H_PH3 2
TP934 1 H_PH2 3
TP935 1 H_PH1 4
TP936 1 H_PH0 5
TP944
1 VCC
H_PH3
H_PH2
H_PH1
H_PH0
CON05_1.50mm_SMT_L
CON908
M1
M2
TP941 1 H_ORG_LIMIT
TP942
TP943 1 H_TML_LIMIT
1 H_ORG
2 GND
3 H_TML
CON03_1.25mm_SMT_S
CON905
SDIO_CDATA2 1
SDIO_CCMD 3
SDIO_CCLK_OUT 5
SDIO_CDATA1 7
9
2 SDIO_CDATA3
4 VCC_SD
6 SDIO_CDATA0
8 SDIO_CARD_DETECT
10
CON10_2.00mm_DIP_S
DC901
VCC
GND
+5V0
2
DC003A
+5V0
+3V3
GND
IIC_SDA
IIC_SCL
SEN_CLK
VIU_CLK
VIU_HS
VIU_VS
VIU_DAT[0..11]
SM_V[0..3]
H_PH[0..3]
H_ORG_LIMIT
H_TML_LIMIT
V_ORG_LIMIT
V_TML_LIMIT
IRC1
IRC2
CDS
SDIO_CCMD
SDIO_CDATA0
SDIO_CDATA1
SDIO_CDATA2
SDIO_CDATA3
SDIO_CCLK_OUT
SDIO_CARD_DETECT
IR_SW
PWM_OUT0
CON907
M1
M2
TP901 1
TP902 1 +5V0
+5V0
TP903 1 CDS 6
TP904 1 V_ORG_LIMIT 7
TP905 1 V_TML_LIMIT 8
TP906 1 SM_V0 9
TP907 1 SM_V1 10
TP908 1 SM_V2 11
TP909 1 SM_V3 12
TP910 1 IRC1 13
TP911 1 IRC2 14
TP912 1 IR_SW 15
TP913 1 PWM_OUT0 16
+3V3 17
18
TP914 1 SEN_CLK 19
TP915 1 IIC_SCL 20
TP916 1 IIC_SDA 21
TP917 1 VIU_HS R901 33R 22
TP918 1 VIU_VS R902 33R 23
TP919 1 VIU_CLK R903 33R 24
TP920 1 VIU_DAT0 R904 33R 25
TP921 1 VIU_DAT1 R905 33R 26
TP922 1 VIU_DAT2 R906 33R 27
TP923 1 VIU_DAT3 R907 33R 28
TP924 1 VIU_DAT4 R908 33R 29
TP925 1 VIU_DAT5 R909 33R 30
TP926 1 VIU_DAT6 R910 33R 31
TP927 1 VIU_DAT7 R911 33R 32
TP928 1 VIU_DAT8 R912 33R 33
TP929 1 VIU_DAT9 R913 33R 34
TP930 1 VIU_DAT10 R914 33R 35
TP931 1 VIU_DAT11 R915 33R 36
37
TP932 1 38
CON38_0.5mm_SMT_S
TP937 1
TP938 1
TP939 1
TP940 1
+5V0
MA901 MASK
MA902 MASK
MA903 MASK
MA904 MASK
MA905 MASK
MA906 MASK
MT901 1 H2.50mm_P4.50mm
MT902 1 H2.50mm_P4.50mm
MT903 1 H2.50mm_P4.50mm
MT904 1 H2.50mm_P4.50mm
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