

		IS_ZERO (p2) CARRY (p1)			-FLAG_SET	-MAR_LOAD	-WE	-MEM_OUT	-IR_OUT	-IR_LOAD	-LOAD_A	-ENABLE_A	HALT	ALU_OUT	SUB/-ADD	-LOAD_B	DISP	PC_INC	-PC_OUT	-PC_LOAD	ADDRESS [X_X_p1_p2   OPCODE   0_t2_t1_t0]	HEXA ADDRESS	EEPROM_1	EEPROM_2
	FETCH	X	X	T0(000)	1	0	1	1	1	1	1	1	0	0	0	1	0	0	0	1	[XXXX XXXX 0000]	XX0	BF	11
		X	X	T1(001)	1	1	1	0	1	0	1	1	0	0	0	1	0	1	1	1	[XXXX XXXX 0001]	XX1	EB	17
1	HALT	X	X	T2(010)	1	1	1	1	1	1	1	1	1	0	0	0	1	0	0	1	[XXXX 0000 0010]	X02	FF	93
	OOO0	X	X	T3(011)	1	1	1	1	1	1	1	1	0	0	0	1	0	0	1	1	[XXXX 0000 0011]	X03	FF	13
		X	X	T4(100)	1	1	1	1	1	1	1	1	0	0	0	1	0	0	1	1	[XXXX 0000 0100]	X04	FF	13
2	LOAD X	X	X	T2(010)	1	0	1	1	0	1	1	1	0	0	0	1	0	0	1	1	[XXXX 0001 0010]	X12	B7	13
	OOO1	X	X	T3(011)	1	1	1	0	1	1	0	1	0	0	0	1	0	0	1	1	[XXXX 0001 0011]	X13	ED	13
		X	X	T4(100)	1	1	1	1	1	1	1	1	0	0	0	1	0	0	1	1	[XXXX 0001 0100]	X14	FF	13
3	ADD X	X	X	T2(010)	1	0	1	1	0	1	1	1	0	0	0	1	0	0	1	1	[XXXX 0010 0010]	X22	B7	13
	OO10	X	X	T3(011)	1	1	1	0	1	1	1	1	0	0	0	0	0	0	1	1	[XXXX 0010 0011]	X23	EF	O3
		X	X	T4(100)	0	1	1	1	1	1	0	1	0	1	0	1	0	0	1	1	[XXXX 0010 0100]	X24	7D	53
4	SUB X	X	X	T2(010)	1	0	1	1	0	1	1	1	0	0	0	1	0	0	1	1	[XXXX 0011 0010]	X32	B7	13
	OO11	X	X	T3(011)	1	1	1	0	1	1	1	1	0	0	0	0	0	0	1	1	[XXXX 0011 0011]	X33	EF	O3
		X	X	T4(100)	0	1	1	1	1	1	0	1	0	1	1	1	0	0	1	1	[XXXX 0011 0100]	X34	7D	73
5	DISP	X	X	T2(010)	1	1	1	1	1	1	1	0	0	0	0	1	1	0	1	1	[XXXX 0100 0010]	X42	FE	1B
	O100	X	X	T3(011)	1	1	1	1	1	1	1	1	0	0	0	1	0	0	1	1	[XXXX 0100 0011]	X43	FF	13
		X	X	T4(100)	1	1	1	1	1	1	1	1	0	0	0	1	0	0	1	1	[XXXX 0100 0100]	X44	FF	13
6	STORE X	X	X	T2(010)	1	0	1	1	0	1	1	1	0	0	0	1	0	0	1	1	[XXXX 0101 0010]	X52	B7	13
	O101	X	X	T3(011)	1	1	0	1	1	1	1	0	0	0	0	1	0	0	1	1	[XXXX 0101 0011]	X53	DE	13
		X	X	T4(100)	1	1	1	1	1	1	1	1	0	0	0	1	0	0	1	1	[XXXX 0101 0100]	X54	FF	13
7	LOAD IMM X	X	X	T2(010)	1	1	1	1	0	1	0	1	0	0	0	1	0	0	1	1	[XXXX 0110 0010]	X62	F5	13
	O110	X	X	T3(011)	1	1	1	1	1	1	1	1	0	0	0	1	0	0	1	1	[XXXX 0110 0011]	X63	FF	13
		X	X	T4(100)	1	1	1	1	1	1	1	1	0	0	0	1	0	0	1	1	[XXXX 0110 0100]	X64	FF	13
8	INC X	X	X	T2(010)	1	1	1	1	0	1	1	1	0	0	0	0	0	0	1	1	[XXXX 0111 0010]	X72	F7	O3
	O111	X	X	T3(011)	0	1	1	1	1	1	0	1	0	1	0	1	0	0	1	1	[XXXX 0111 0011]	X73	7D	53

			IS_ZERO (p2) CARRY (p1)		-FLAG_SET	-MAR_LOAD	-WE	-MEM_OUT	-IR_OUT	-IR_LOAD	-LOAD_A	-ENABLE_A	HALT	ALU_OUT	SUB/ADD	-LOAD_B	DISP	PC_INC	-PC_OUT	-PC_LOAD	ADDRESS [X_X_p1_p2   OPCODE   0_t2_t1_t0]	HEXA ADDRESS	EEPROM_1	EEPROM_2	
	OPCODE + 4BIT NUM	X	X	T4(100)	1	1	1	1	1	1	1	1	0	0	0	1	0	0	1	1	[XXXX 0111 0100]	X74	FF	13	
9	DEC X	X	X	T2(010)	1	1	1	1	0	1	1	1	0	0	0	0	0	0	1	1	[XXXX 1000 0010]	X82	F7	O3	
	1000	X	X	T3(011)	0	1	1	1	1	1	0	1	0	1	1	1	0	0	1	1	[XXXX 1000 0011]	X83	7D	73	
	OPCODE + 4BIT NUM	X	X	T4(100)	1	1	1	1	1	1	1	1	0	0	0	1	0	0	1	1	[XXXX 1000 0100]	X84	FF	13	
10	JUMP X	X	X	T2(010)	1	1	1	1	0	1	1	1	0	0	0	1	0	0	1	0	[XXXX 1001 0010]	X92	F7	12	
	1001	X	X	T3(011)	1	1	1	1	1	1	1	1	0	0	0	1	0	0	1	1	[XXXX 1001 0011]	X93	FF	13	
		X	X	T4(100)	1	1	1	1	1	1	1	1	0	0	0	1	0	0	1	1	[XXXX 1001 0100]	X94	FF	13	
11	JUMP CAR X	1	X	T2(010)	1	1	1	1	0	1	1	1	0	0	0	1	0	0	1	0	[XX1X 1010 0010]	[xx1x]A2	F7	12	2,3,6,7,A,B,E,F
	1010	0	X	T2(010)	1	1	1	1	1	1	1	1	0	0	0	1	0	0	1	1	[XX0X 1010 0010]	[xx0x]A2	FF	13	0,1,4,5,8,9,C,D
		X	X	T3(011)	1	1	1	1	1	1	1	1	0	0	0	1	0	0	1	1	[XXXX 1010 0011]	XA3	FF	13	
		X	X	T4(100)	1	1	1	1	1	1	1	1	0	0	0	1	0	0	1	1	[XXXX 1010 0100]	XA4	FF	13	
12	JUMP IFZ X	X	1	T2(010)	1	1	1	1	0	1	1	1	0	0	0	1	0	0	1	0	[XXX1 1011 0010]	[xxx1]B2	F7	12	1,3,5,7,9,B,D,F
	1011	X	0	T2(010)	1	1	1	1	1	1	1	1	0	0	0	1	0	0	1	1	[XXX0 1011 0010]	[xxx0]B2	FF	13	0,2,4,6,8,A,C,E
		X	X	T3(011)	1	1	1	1	1	1	1	1	0	0	0	1	0	0	1	1	[XXXX 1011 0011]	XB3	FF	13	
		X	X	T4(100)	1	1	1	1	1	1	1	1	0	0	0	1	0	0	1	1	[XXXX 1011 0100]	XB4	FF	13	