INST FETCH	то	PC_OUT	MAR_IN			AVAILABLE CONTROL SIGNALS
	T1	RAM_OUT	IR_IN (INST)	PC_INC		-FLAG_SET
						-MAR_LOAD
HALT [0000]	T2	HALT				-WE (RAM_IN)
	T3					-MEM_OUT
	T4					-IR_OUT
						-IR_LOAD
LOAD X [0001]	T2	IR_OUT	MAR_IN			-LOAD_A
	T3	RAM_OUT	REG_A_IN			-ENABLE_A (REG_A_OUT)
	T4	14.00200	1.22.0			HALT
						ALU_OUT
ADD X [0010]	T2	IR_OUT	MAR_IN			SUB/-ADD
	T3	RAM_OUT	REG_B_IN			-LOAD_B
	T4	ALU_ADD_REQUEST	ALU_OUT	DEC A IN	SET FLAC	DISP
	14	ALO_ADD_REQUEST	ALU_UUI	REG_A_IN	SET_FLAG	PC_INC
SUB X [0011]	T2	ID OUT	MAD IN			-PC_OUT
	_	IR_OUT	MAR_IN			
	T3 T4	RAM_OUT ALU_SUB_REQUEST	REG_B_IN ALU_OUT	REG_A_IN	SET_FLAG	-PC_LOAD
	14	ALU_SUB_REQUEST	ALU_OUT	REG_A_IN	SET_FLAG	
DISPLAY [0100]	T2	REG_A_OUT	DISPLAY			
Did Ext [0100]	T3	1120_7(_001	DIOI EXT			
	T4					
STORE X [0101] LOAD IMD [0110	T2	IR_OUT	MAR_IN			
	T3	REG_A_OUT	RAM_IN			
	T4					
	T0	ID OUT	DEO 4 IN			
	T2 T3	IR_OUT	REG_A_IN			
	T4					
	14					
INC X [0111]	T2	IR OUT	REG_B_IN			
IR = OPCODE + 4bit Num	T3	ALU_ADD_REQUEST	ALU_OUT	REG_A_IN	SET_FLAG	
	T4				_	
DEC X [1000]	T2	IR_OUT	REG_B_IN			
IR = OPCODE + 4bit Num	T3	ALU_SUB_REQUEST	ALU_OUT	REG_A_IN	SET_FLAG	
	T4					
JUMP X [1001]	T2	IR_OUT	PC_IN			
	T3	IR_OUT	PC_IN			
	T4					
JUMP IF_CAR X [1010] JUMP IFZ X [1011]	T2	IR_OUT	PC_IN			
	T2					
	T3					
	T4					
	T2	IR_OUT	PC_IN			
	T2					
	T3 T4					
	14					