

Imperial College London

Department of Electrical and Electronic Engineering

Final Year Project Report (DRAFT)



Project Title: **A High-radix Online Arithmetic Verification System**

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Course: **EEE4**

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Abstract

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- 1 Introduction**
- 2 Background**
- 3 Requirements Capture**
- 4 Analysis and Design**

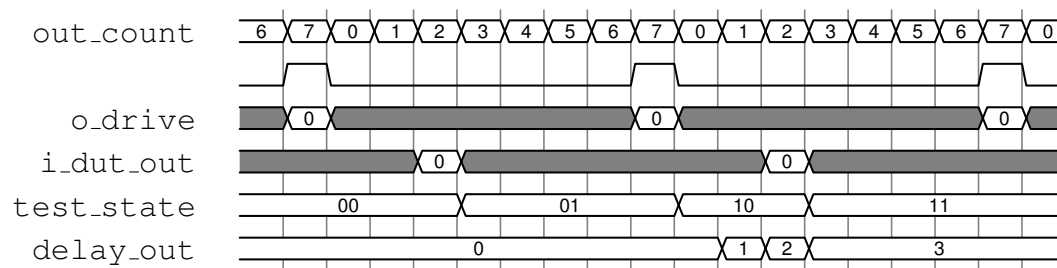


Figure 1: 3-bit Delay Tester FSM

- 5 Implementation**
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- 8 Evaluation**
- 9 Conclusion**
- 10 Further Work**
- 11 User Guide**

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