

Version	Date	Change Description
R0A	2013/08/15	This schematic is based on bcm943460MC5 P355 design
R01	2014/06/25	Chnage discription from R0A to R01

# 11ac 3X3 Mini PCIe Card

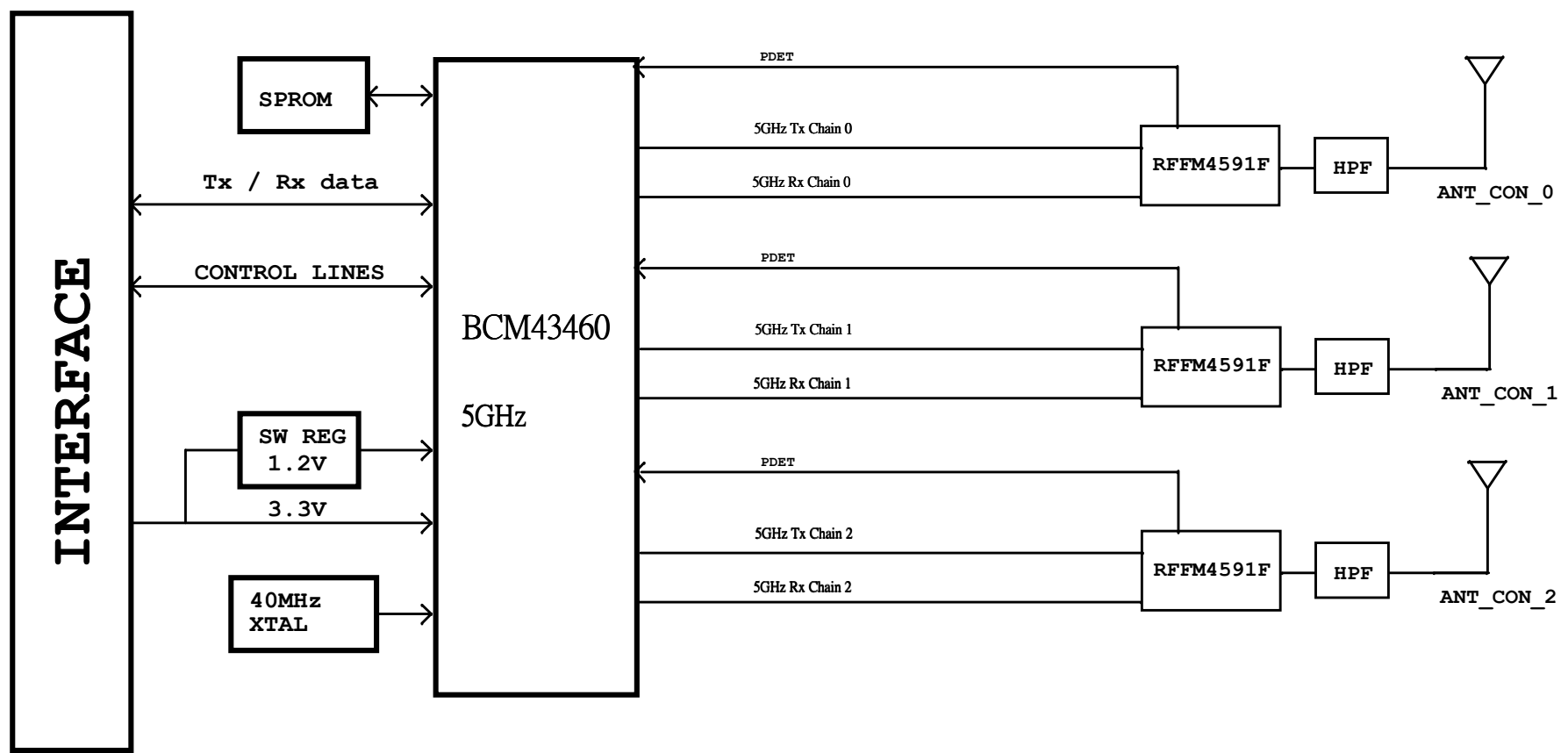
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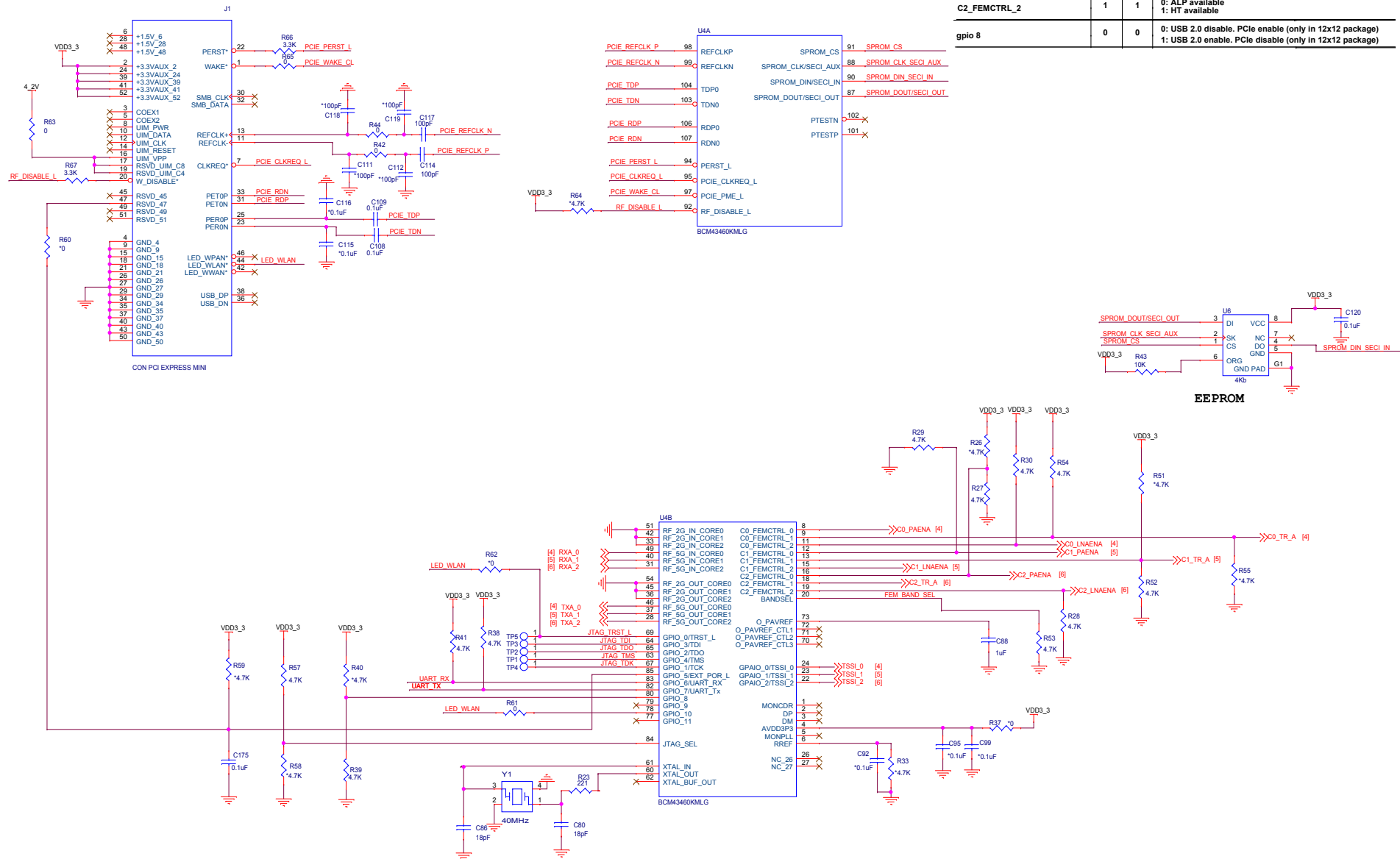
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HPF : HIGH PASS FILTER  
PDET : POWER DETECTOR  
ANT\_CON : ANTENNA CONNECTOR



## B0 strapping option

Pin	chip default	board default	Description
C0_FEMCTRL_2	1	1	0: SPROM not present(ATMEL if SFLASH is present) 1: SPROM present(ST if SFLASH is present)
C2_FEMCTRL_2	1	1	0: ALP available 1: HT available
gpio 8	0	0	0: USB 2.0 disable, PCIe enable (only in 12x12 package) 1: USB 2.0 enable, PCIe disable (only in 12x12 package)



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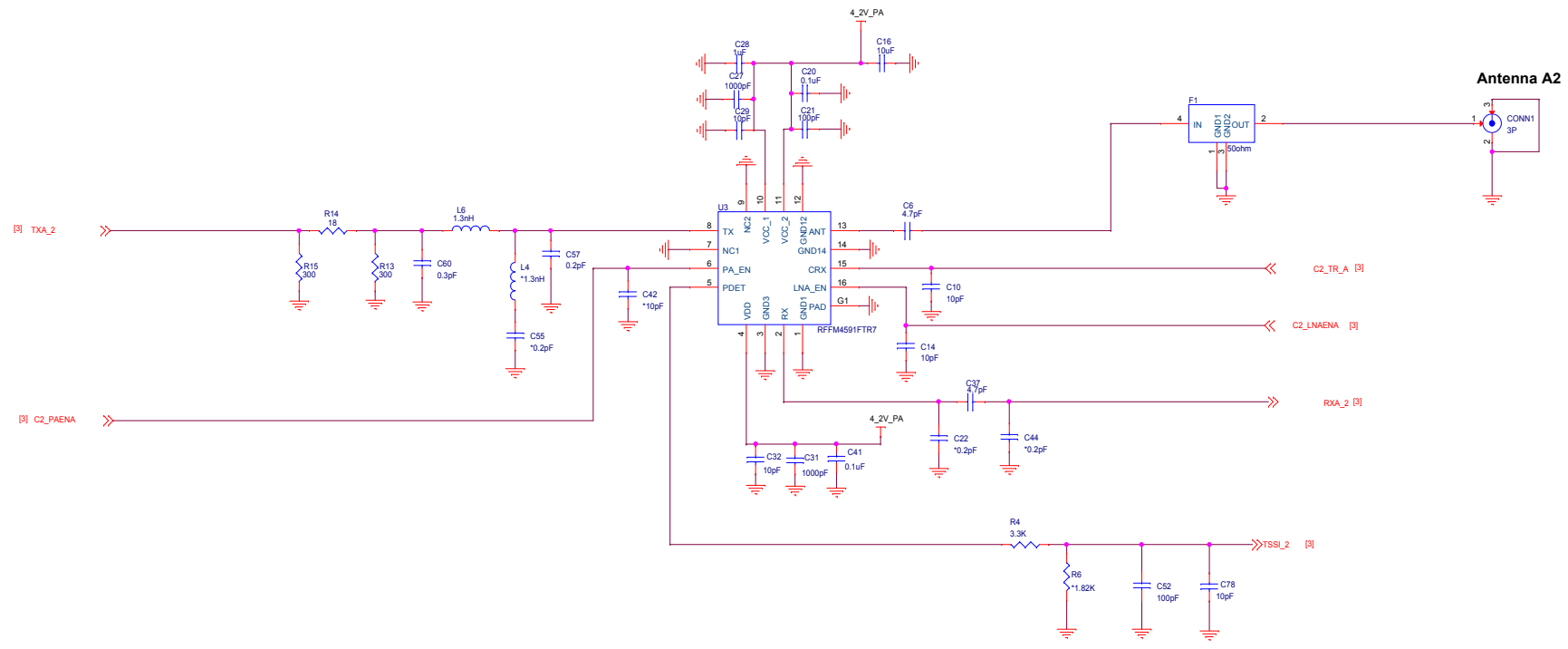


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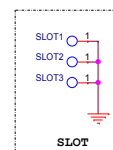
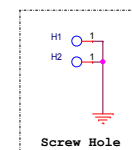
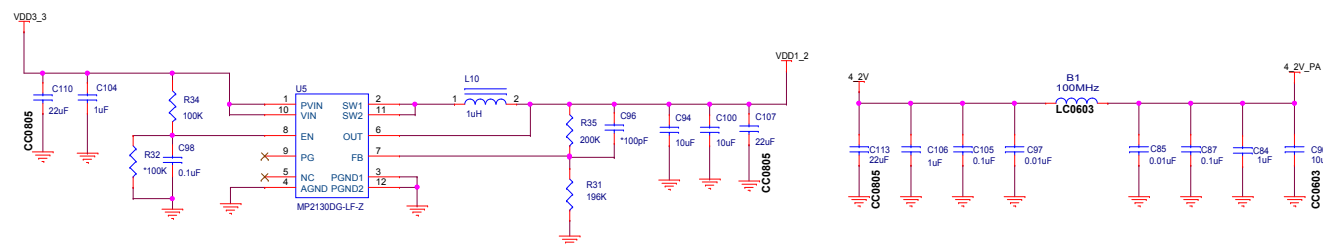
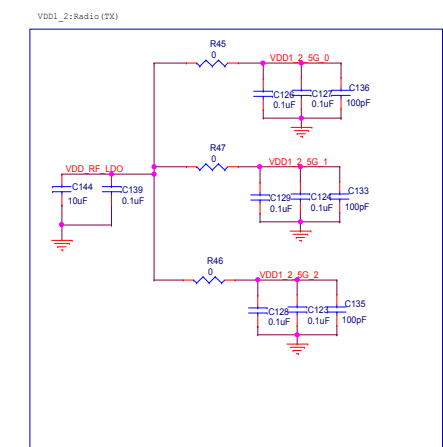
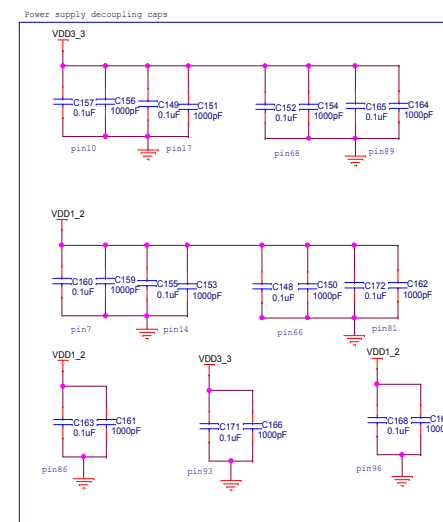
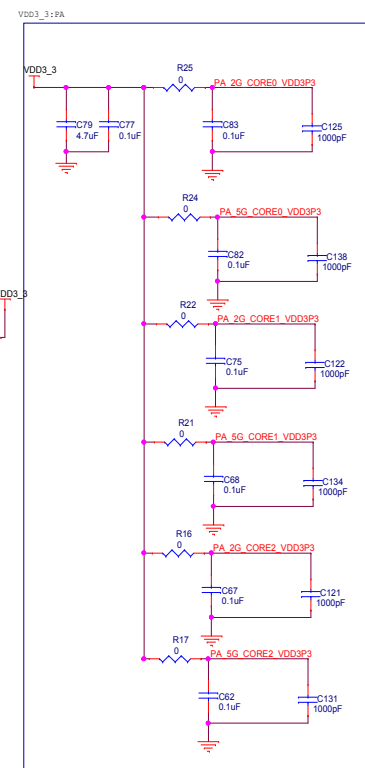
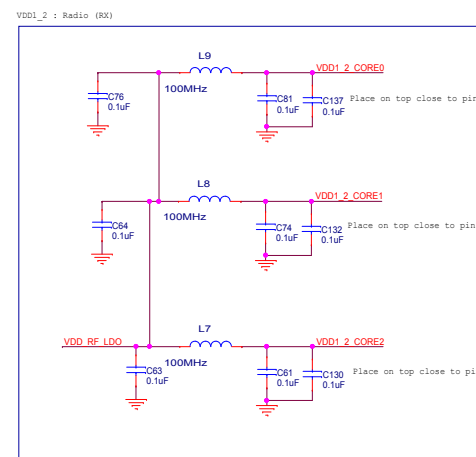
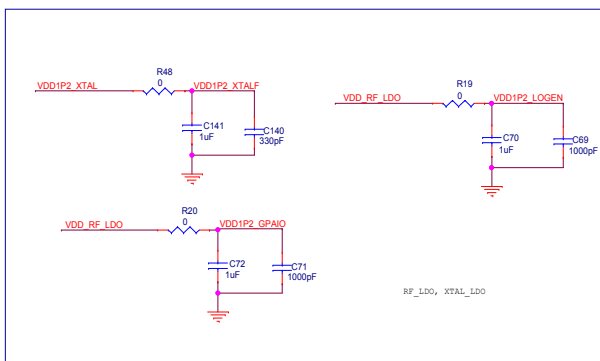
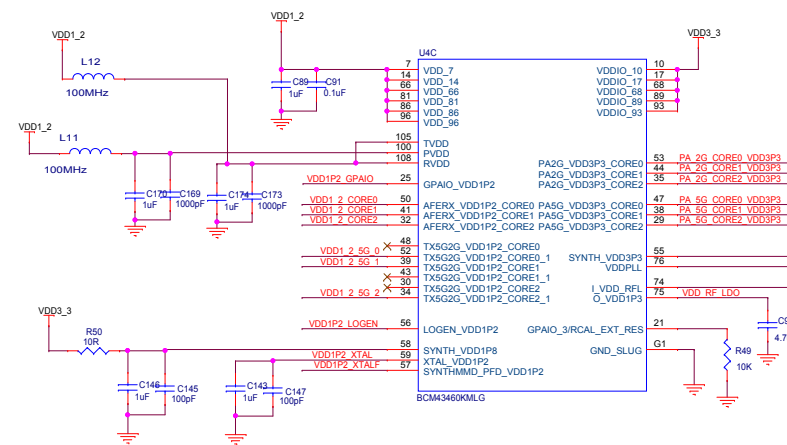
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