	Açıklama(lar): Instructions	Öğrenci İmzası Student Signature	Ön Sayfa (lar) Puanı Front Page (s) Grade
BEYKOZ ÜNİVERSİTESİ	Use the space provided		Arka Sayfa (lar) Puanı Back Page(s) Grade
	GOOD LUCK!		Toplam Puan Total Exam Grade
Öğrenci Adı ve Soyadı Student Name and Surname		Sınav Tarihi Exam Date	27.03.2024
Öğrenci Numarası Student Number		Sınav Saat Aralığı Exam Time Interval	
Programi Program	Computer Engineering	Sınav Süresi (Dakika) Exam Duration (Minutes)	60
Ders Kodu Course Code	60714MEEOZ-ELE3144	Sınav Not Katkı Yüzdesi (%) Exam Percentage in Overall Grade (%)	%8
Ders Adı/Sınav Course Name/Exam	Digital System Laboratory/ MIDTERM		

QUESTIONS

Q1 (30pt)-

Course Name/Exam

M is a decimal number which is M=169

- a) (5pt) Convert M into a 8 bit binary number. Show your steps.
- b) (10pt) Convert the binary number found in part (a) to octal. Use direct conversion.
- c) (10pt) Convert the binary number found in part (a) to hexadecimal. Use direct conversion.
- d) (5pt) Obtain 1's complement of the binary number found in (a).

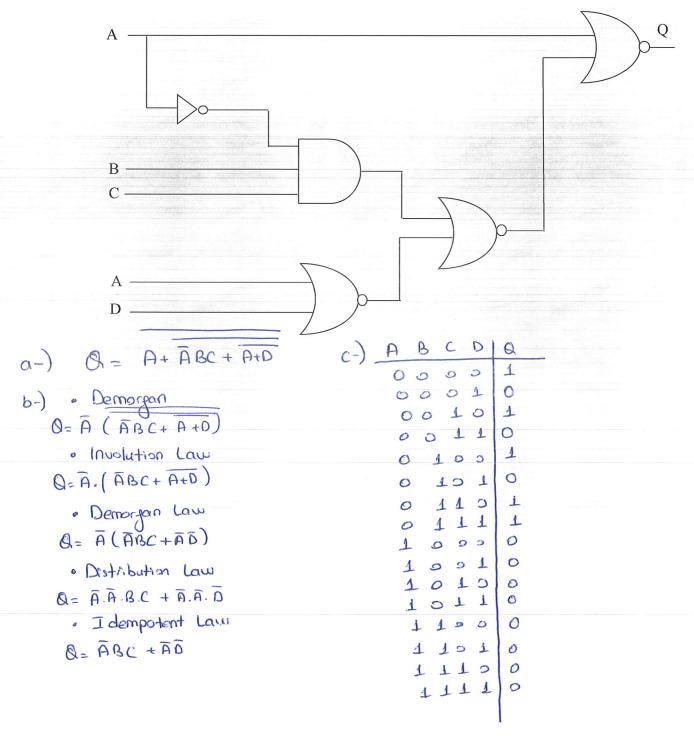
a.)
$$1010\ 1001$$
b.) $(251)_8$
c.) $(A9)_{16}$
d.) $0101\ 0110$

Form No	Revizyon Tarihi	Revizyon No	Basım Tarihi	Sayfa
GS.OIM.F.34	-	-	18.04.2024 **	1/1

Bu dokümanın güncelliği sadece "BASIM TARİHİNDE" geçerlidir.
** GÜNCEL DOKÜMAN İÇİN AĞA BAKINIZ **

Q2 (40pt) -

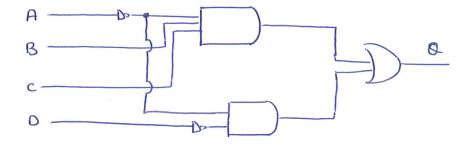
- a) (5pt) Derive the Boolean expression (use Boole operators only, + . ') for the output of the following logic system. (Do not simplify at this step!)
- b) (15pt) Simplify the expression. List the general boolean algebra theorems that are used (just explain what you are doing step by step).
- c) (10pt) Create the corresponding truth table.
- d) (10pt) Draw the simplified circuit.



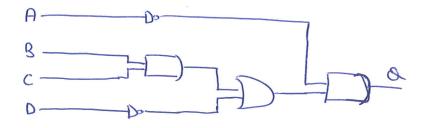
Form No	Revizyon Tarihi	Revizyon No	Basım Tarihi	Sayfa
GS.OIM.F.34	-	-	18.04.2024 **	1/1

Bu dokümanın güncelliği sadece "BASIM TARİHİNDE" geçerlidir.
** GÜNCEL DOKÜMAN İÇİN AĞA BAKINIZ **

d-) For Answer = ABC + AD



For Answer = A (BC+D)

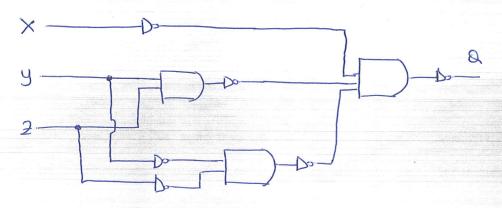


Q3 (30pt)-

$$Q = X + YZ + \bar{Y}\bar{Z}$$

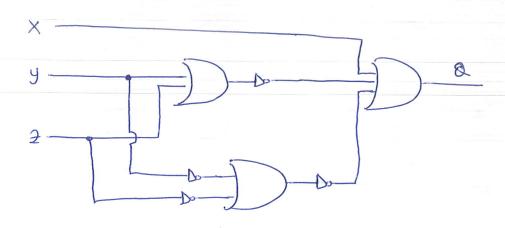
- a) (15pt) Draw the circuit using only AND and NOT gates.
- b) (15pt) Draw the circuit using only OR and NOT gates.

$$Q = \overline{Q} = \overline{X + y_2 + \overline{y_2}} = \overline{X} \cdot \overline{y_2} \cdot \overline{\overline{y_2}}$$



6-)
$$\overline{y}_{2} = y_{2}$$
 $\overline{y}_{2} = \overline{y}_{2}$

$$0 = x + \overline{y}_{2} + \overline{y}_{2} = x + \overline{y}_{1} + \overline{y}_{2} + \overline{y}_{1} + \overline{z}_{2} = x + \overline{y}_{1} + \overline{z}_{2} + \overline{y}_{2} + \overline{z}_{2} = x + \overline{y}_{1} + \overline{z}_{2} + \overline{y}_{2} + \overline{z}_{2} = x + \overline{y}_{1} + \overline{z}_{2} + \overline{y}_{2} + \overline{z}_{2} = x + \overline{y}_{1} + \overline{z}_{2} + \overline{y}_{2} + \overline{z}_{2} = x + \overline{y}_{1} + \overline{z}_{2} + \overline{y}_{2} + \overline{z}_{2} = x + \overline{y}_{1} + \overline{z}_{2} + \overline{y}_{2} + \overline{z}_{2} = x + \overline{y}_{1} + \overline{z}_{2} + \overline{y}_{2} + \overline{z}_{2} + \overline{y}_{2} + \overline{z}_{2} + \overline{y}_{2} + \overline{z}_{2} + \overline{y}_{2} + \overline{z}_{2} + \overline{y}_{2} + \overline{z}_{2} + \overline{y}_{2} + \overline{z}_{2} + \overline{y}_{2} + \overline{z}_{2} + \overline{y}_{2} + \overline{z}_{2} + \overline{y}_{2} + \overline$$



Form No	Revizyon Tarihi	Revizyon No	Basım Tarihi	Sayfa
GS.OIM.F.34	-	-	18.04.2024 **	1/1
	D 1 1 1 1 11 11 11 11 11 11 11 11 11 11			

Bu dokümanın güncelliği sadece **"BASIM TARİHİNDE"** geçerlidir. ** **GÜNCEL DOKÜMAN İÇİN AĞA BAKINIZ** **