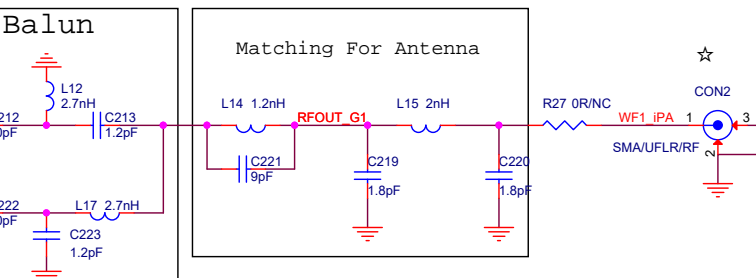
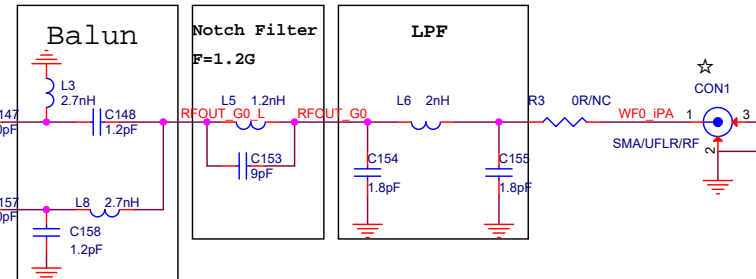
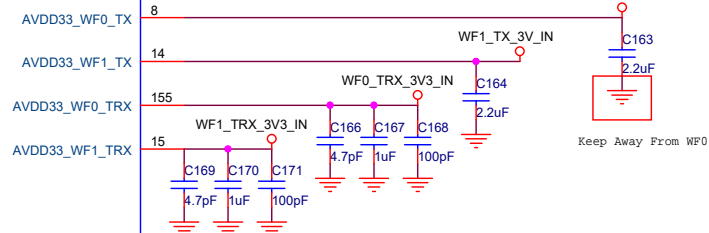
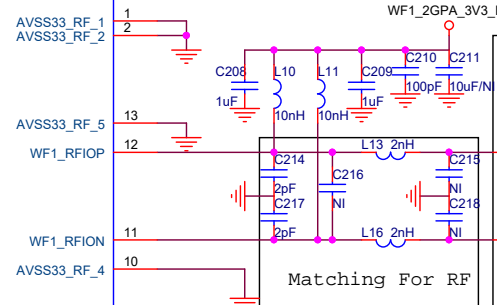
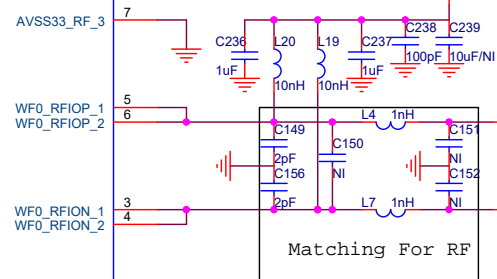
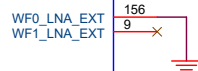


operation temp : -40~+85  
40MHz, +/-15ppm, CL=13pF

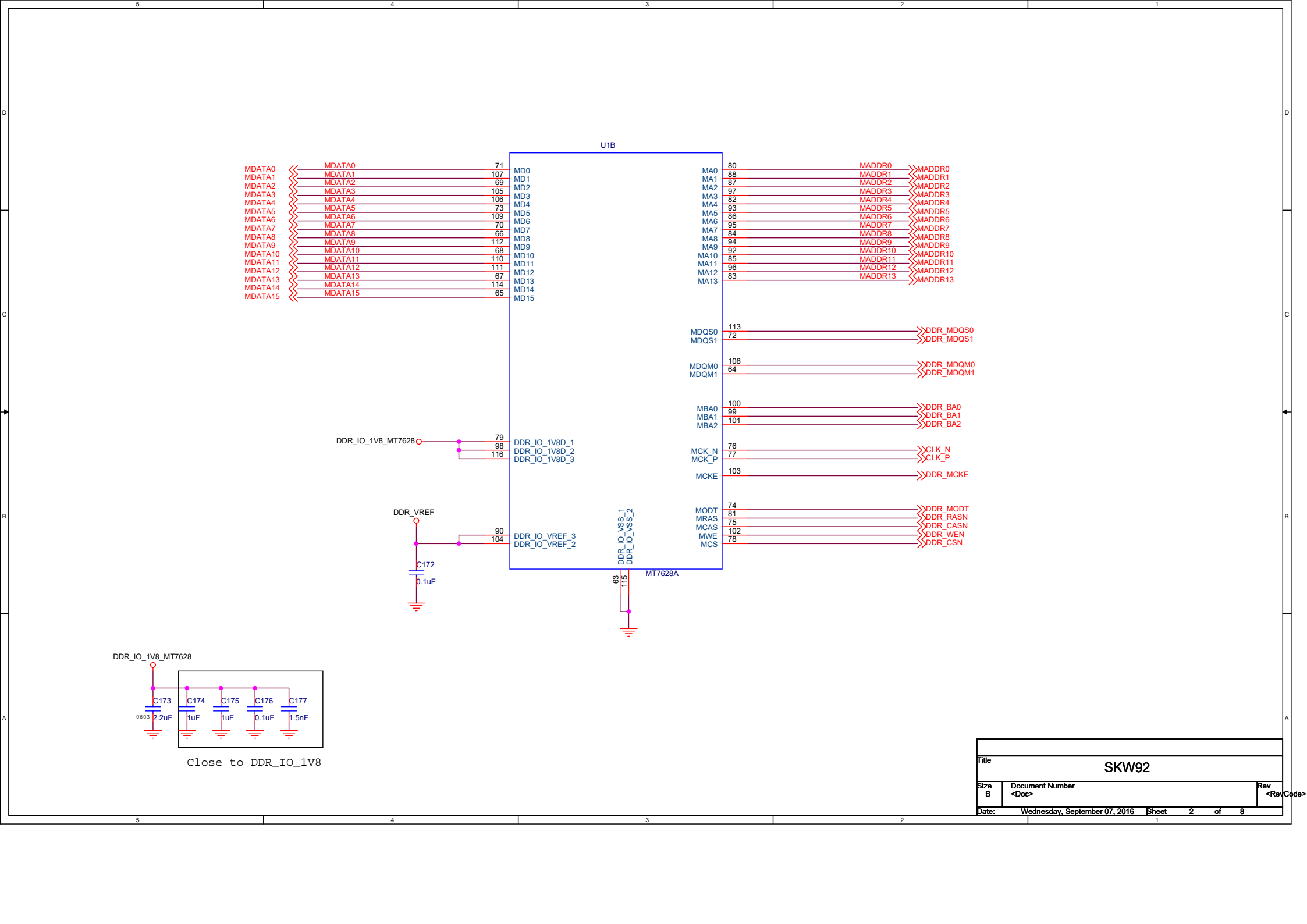
☆

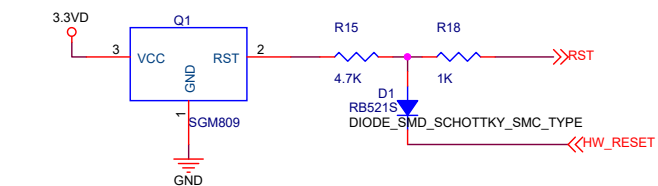
MT7628A



☆ ☆ 特殊特性

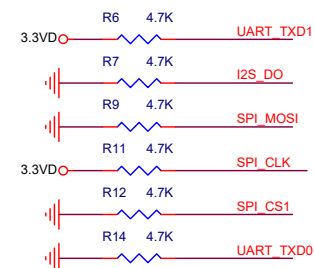
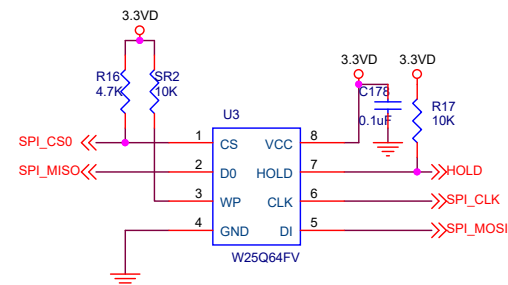
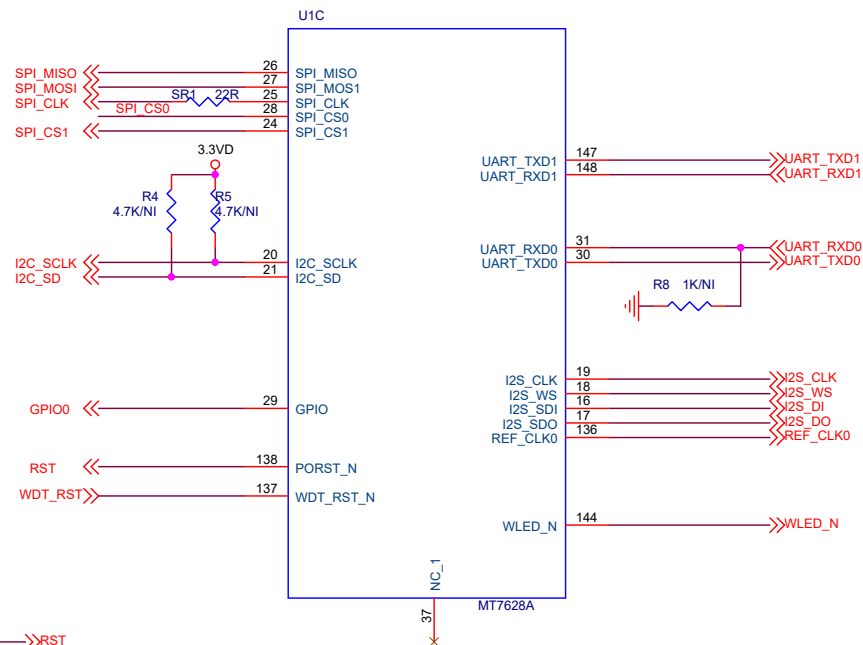
Title				SKW92	
Size	B	Document Number	<Doc>	Rev	<RevCode>
Date:	Wednesday, September 07, 2016			Sheet	1 of 8



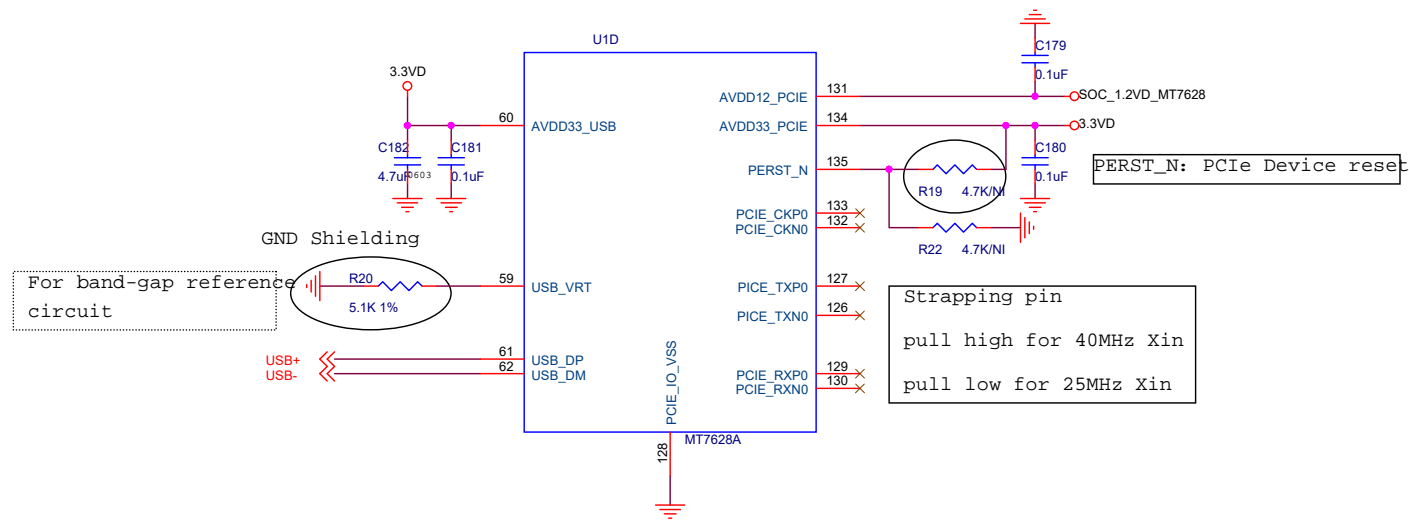


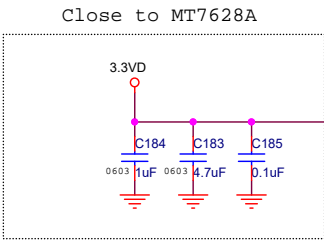
	0	1
UART_TXD1	JTAG_MODE	EPHY_LED(default)
PERST_N	25MHz	40MHz
I2S_SDO	DDR2	DDR1
UART_TXD0	Normal(default)	Test Mode

SPI_MOSI	SPI_CLK	SPI_CS1
000:Boot from PLL(3-Byte Addr)		
001:Boot from PLL(4-Byte Addr)		
010:Boot from XTAL(3-Byte Addr)		
011:Boot from XTAL(4-Byte Addr)		

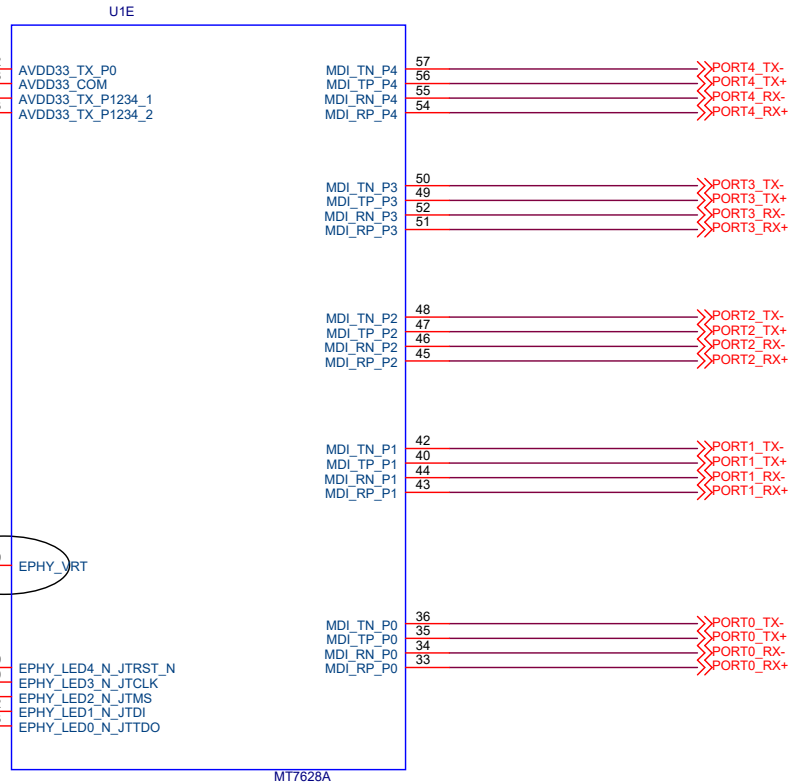
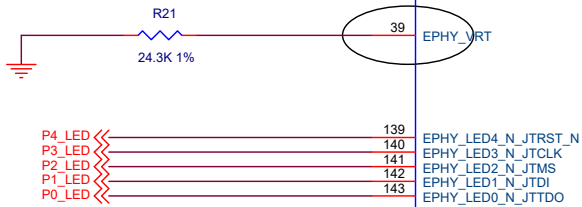


Title			SKW92		
Size	Document Number			Rev	<RevCode>
B	<Doc>				
Date:	Wednesday, September 07, 2016	Sheet	3	of	8

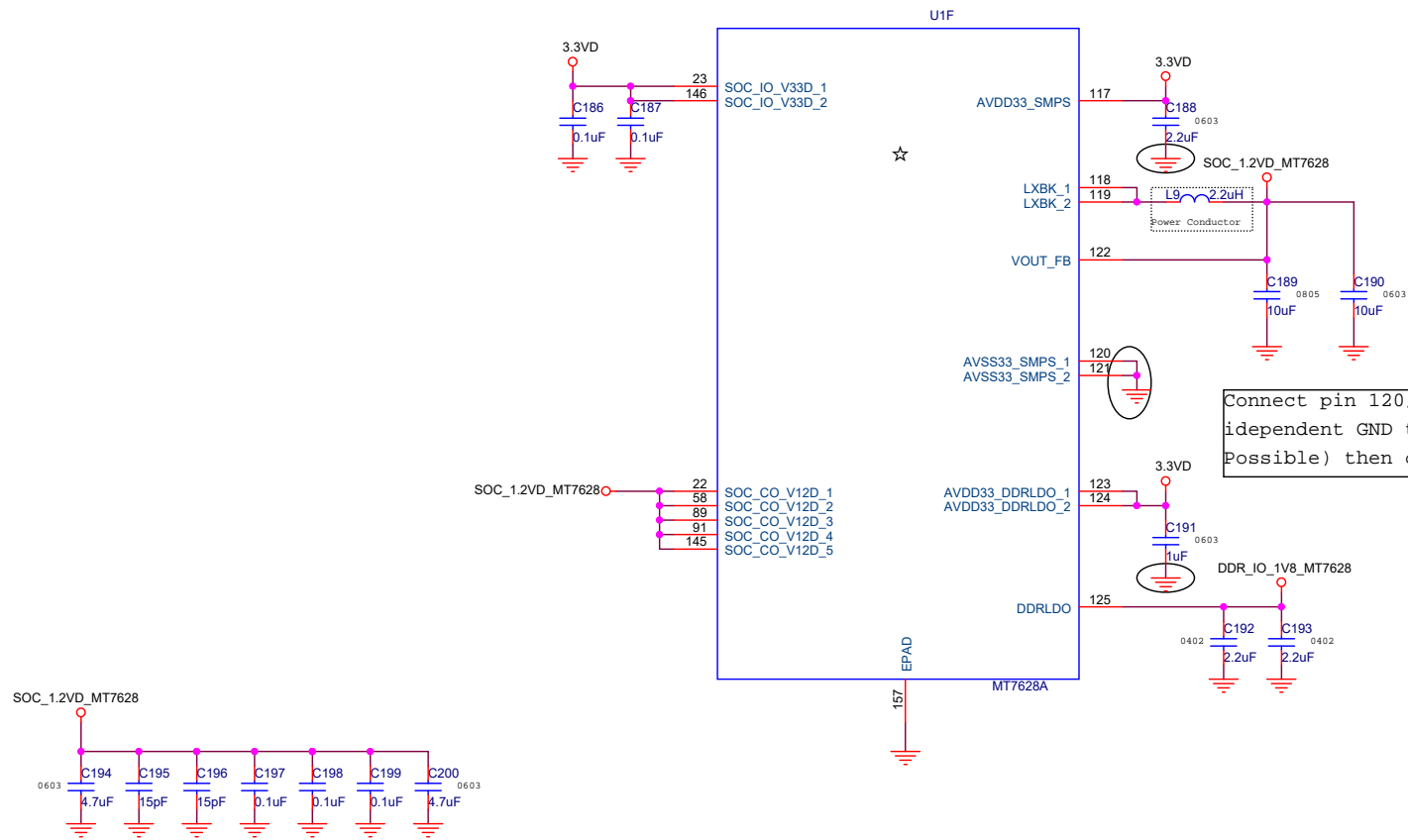




To provide accurate bias current

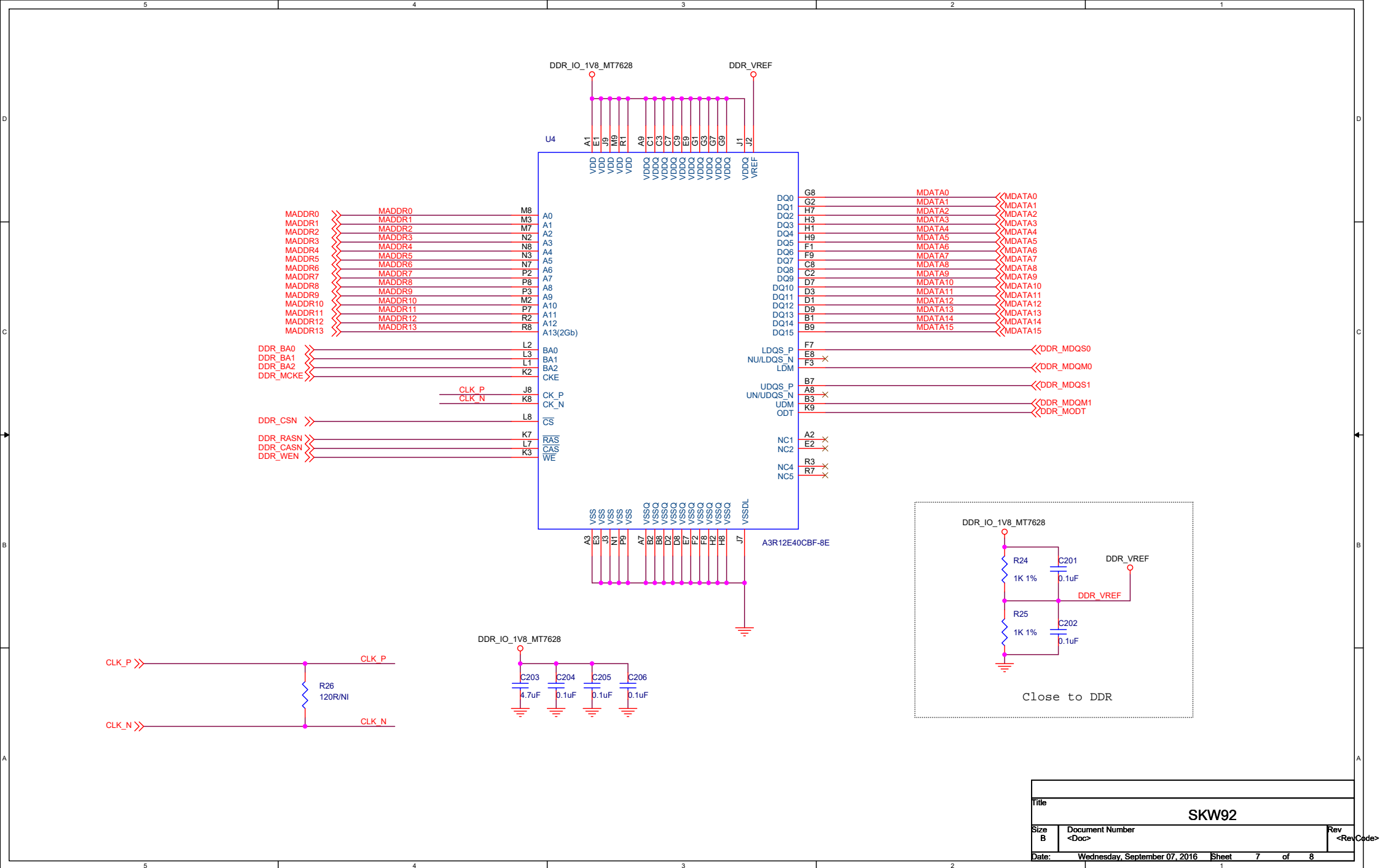


Title			SKW92
Size	Document Number	Rev	<RevCode>
B	<Doc>		
Date:	Wednesday, September 07, 2016	Sheet	5 of 6

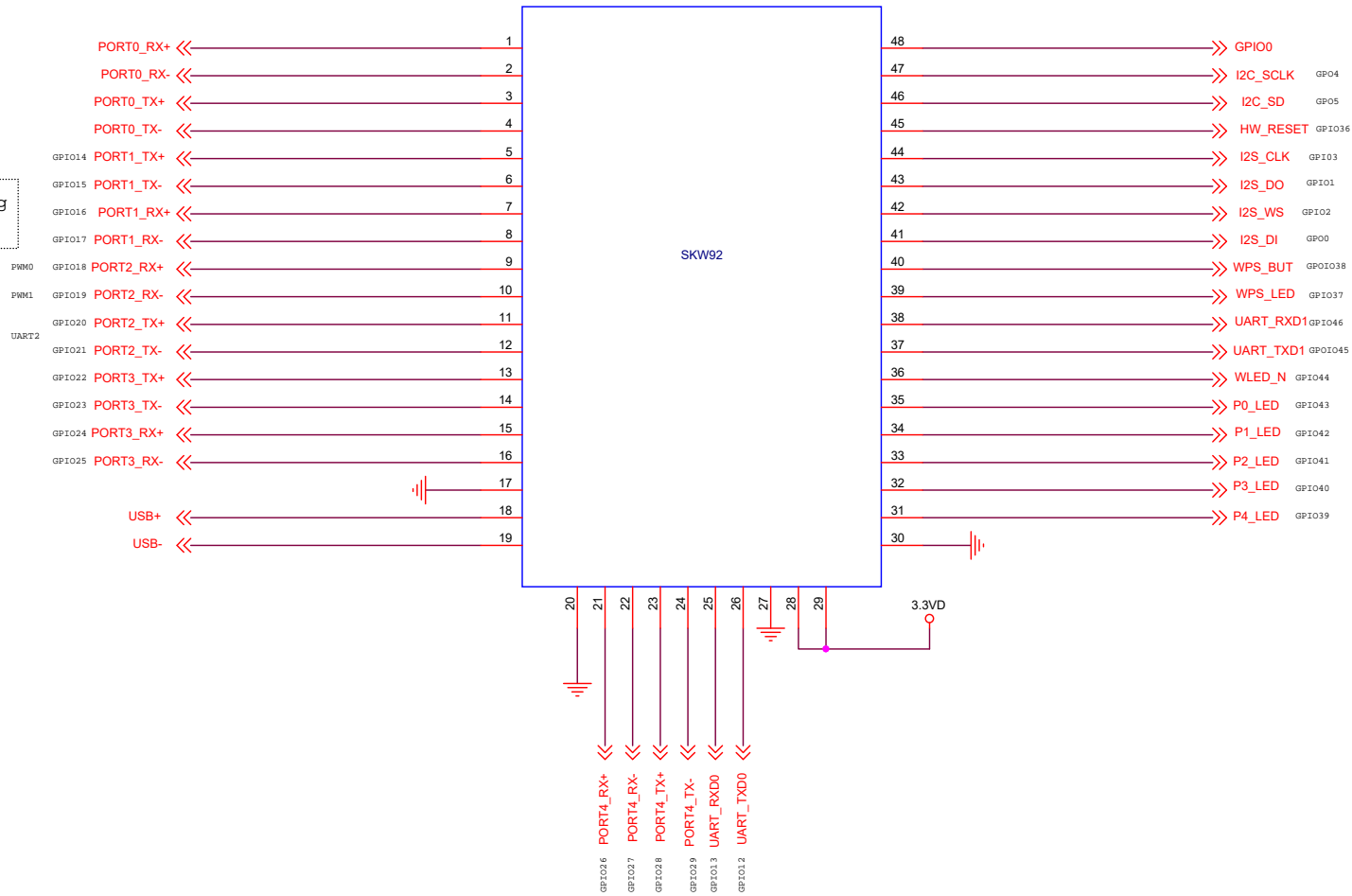


☆：特殊特性

Title			SKW92	
Size	Document Number			Rev
B	<Doc>			<RevCode>
Date:	Wednesday, September 07, 2016	Sheet	6	of 8



Port3 and port4 multiplexing  
for SD Interface



Title			SKW92
Size B	Document Number <Doc>		Rev <RevCode>
Date:	Wednesday, September 07, 2016	Sheet 8 of 8	1