

32.768 kHz CMOS Crystal Oscillator

Features

- · CMOS Output XO
- 32.768 kHz
- 3.3V, 2.5V, and 1.8V Operation
- 18 µA Maximum Current
- · Output Disable Feature
- -40°C to +125°C Operating Temperature Range
- Small, Industry Standard 3.2 mm x 2.5 mm Package
- Product is Compliant to RoHS Directive and Fully Compatible with Lead-Free Assembly

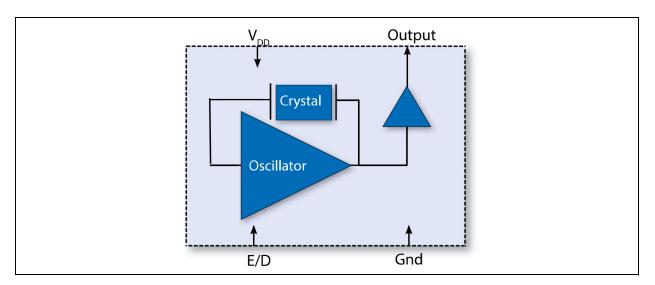
Applications

- RTC IC
- · Smartphones
- IoT
- Wearables

Block Diagram

General Description

Microchip's VC-829 crystal oscillator is a quartz-stabilized, square wave generator with a CMOS output. The VC-829 uses a fundamental crystal that results in very low jitter performance and a monolithic IC that improves reliability and reduces cost.



1.0 ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings †

ESD Rating, Human Body Model (MIL-STD-883, Method 3015, Note 1)	1.5 kV
ESD Rating, Charged Device Model (JESD22-C101, Note 1)	1 kV
Storage Temperature (T _S)	–55°C to +125°C
Soldering Temperature (T _{LS})	+260°C for 30 seconds

† Notice: Stresses in excess of the Absolute Maximum Ratings can permanently damage the device. Functional operation is not implied at these or any other conditions in excess of conditions represented in the operational sections of this data sheet. Exposure to Absolute Maximum Ratings for extended periods may adversely affect device reliability.

Note 1: Although ESD protection circuitry has been designed into the VC-829, proper precautions should be taken when handling and mounting. Microchip employs a Human Body Model (HBM) and a Charged Device Model (CDM) for ESD susceptibility testing and design protection evaluation.

ELECTRICAL CHARACTERISTICS

Parameter	Sym.	Min.	Тур.	Max.	Units	Conditions	
		3.15	3.3	3.45			
Voltage (Note 1)	V_{DD}	2.25	2.5	2.75	V	_	
		1.62	1.8	1.98			
Maximum Voltage	_	-0.5	_	5	V	_	
Current (Note 2)	I _{DD}	_	_	18	μΑ	_	
Current, Output Disabled	_	_	_	3	μA	_	
Frequency							
Nominal Frequency	f _N	_	32.768	_	kHz	_	
		_	_	±20			
Stability (Note 3)	_	_	_	±25	ppm	Ordering Option	
		_	_	±50			
Outputs							
Output Logic Level High	V _{OH}	0.9 * V _{DD}	_	_	٧	3.3V, 2.5V, and 1.8V	
Output Logic Level Low	V_{OL}	_	_	0.1 * V _{DD}	٧	operation, Note 2	
Load	_	_	_	15	pF	_	
Output Rise/Fall Time (Note 2)	t _r /t _f	_	_	15	ns	_	
Duty Cycle (Note 2, Note 4)	_	45	50	55	%	_	
Enable/Disable							
Output Enable (Note 5)	V _{IH}	0.7 * V _{DD}			V		
Output Disable (Note 5)	V _{IL}			0.3 * V _{DD}	V	_	
Disable Time	t _D	_	_	150	ns		
Start-Up Time	t _{SU}			20	ms	_	

- **Note 1:** The VC-829 power supply should have bypass capacitors as close to the supply and to ground as possible. For example, a 0.1 μF and 0.01 μF capacitor.
 - 2: Parameters are tested with the test circuit shown in Figure 1-1.
 - **3:** Includes initial accuracy, operating temperature, supply voltage, shock and vibration (not under operation) and 1 year aging.
 - 4: Duty Cycle is measured as On/Time Period, see Figure 1-2.
 - 5: The output is enabled if the Enable/Disable pin is left open.

ELECTRICAL CHARACTERISTICS (CONTINUED)

Parameter	Sym.	Min.	Тур.	Max.	Units	Conditions
Operating Temperature	T _{OP}	-10	_	+70		Ordering Option
		-40	_	+85	°C	
		-40	_	+105		
		-40	_	+125		

- **Note 1:** The VC-829 power supply should have bypass capacitors as close to the supply and to ground as possible. For example, a $0.1 \, \mu F$ and $0.01 \, \mu F$ capacitor.
 - 2: Parameters are tested with the test circuit shown in Figure 1-1.
 - **3:** Includes initial accuracy, operating temperature, supply voltage, shock and vibration (not under operation) and 1 year aging.
 - 4: Duty Cycle is measured as On/Time Period, see Figure 1-2.
 - **5**: The output is enabled if the Enable/Disable pin is left open.

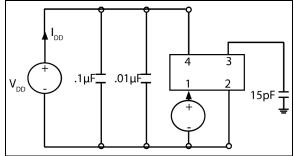


FIGURE 1-1: Test Circuit.

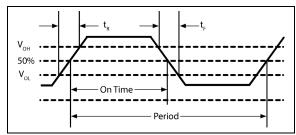


FIGURE 1-2: Output Rise/Fall Time.

2.0 PIN DESCRIPTIONS

The descriptions of the pins are listed in Table 2-1.

TABLE 2-1: PIN FUNCTION TABLE

Pin Number	Pin Name	Description
1	E/D	Enable/Disable.
2	GND	Case and electrical ground.
3	Output	Output.
4	V_{DD}	Power supply voltage.

TABLE 2-2: ENABLE/DISABLE FUNCTION

E/D Pin	Output			
High	Clock Output			
Open	Clock Output			
Low	High Impedance			

3.0 RELIABILITY

Microchip qualification will include aging at various extreme temperatures, shock and vibration, temperature cycling, and IR reflow simulation. The VC-829 family is capable of meeting the following qualification tests.

TABLE 3-1: ENVIRONMENTAL COMPLIANCE

Parameter	Conditions
Mechanical Shock	MIL-STD-883, Method 2002
Mechanical Vibration	MIL-STD-883, Method 2007
Solderability	MIL-STD-883, Method 2003
Gross and Fine Leak	MIL-STD-883, Method 1014
Resistance to Solvents	MIL-STD-883, Method 2015
Moisture Sensitivity Level	MSL 1
Contact Pads	Gold (0.3 µm min., 1.0 µm max.) over Nickel
Weight	26 mg

4.0 IR REFLOW

The VC-829 is qualified to meet the JEDEC standard for Pb-Free assembly. The temperatures and time intervals listed are based on the Pb-Free small body requirements. The VC-829 device is hermetically sealed, so an aqueous was is not an issue.

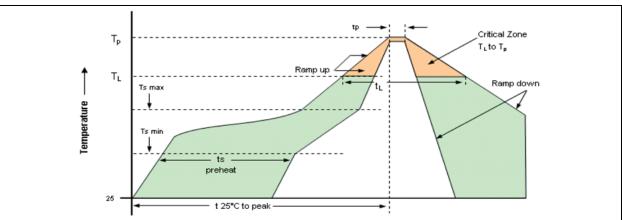


FIGURE 4-1: Solder Profile.

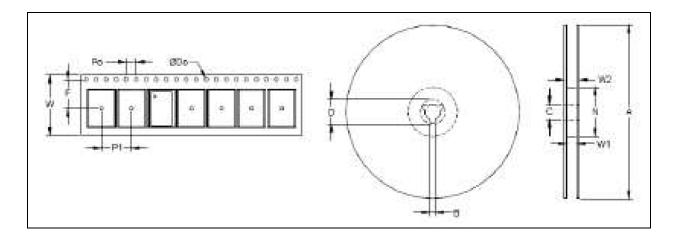
TABLE 4-1: REFLOW PROFILE

Parameter	Symbol	Value
Pre-Heat Time		60 sec. minimum, 260 sec. maximum
T _{S(MIN)}	t _S	150°C
T _{S(MAX)}		200°C
Ramp Up	R_{UP}	3°C/sec. maximum
Time above 217°C	t _L	60 sec. minimum, 150 sec. maximum
Time to Peak Temperature	T _{AMB-P}	480 seconds maximum
Time at 260°C	t _P	30 seconds maximum
Ramp Down	R _{DN}	6°C/sec. maximum

5.0 TAPE AND REEL

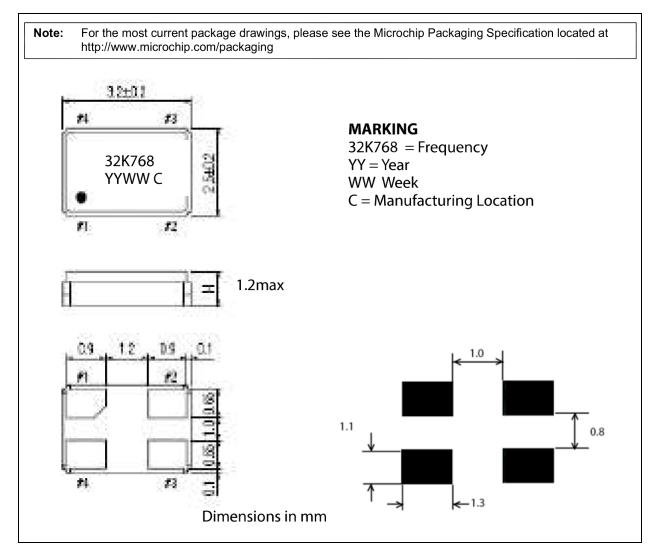
TABLE 5-1: TAPE AND REEL DIMENSIONS

	Tape D	imensi	ons (mr	n)				Ree	I Dimen	sions (ı	nm)		
Dimension	W	F	Do	Ро	P1	Α	В	С	D	N	W1	W2	# per
Tolerance	Тур	Тур	Тур	Тур	Тур	Тур	Min	Тур	Min	Min	Тур	Max	Reel
VC-829	8	3.5	1.5	4	4	178	2	13	21	60	10	14	1000



6.0 PACKAGING INFORMATION

4-Lead 3.2 mm x 2.5 mm Ceramic Package Outline and Recommended Land Pattern



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NOTES:

APPENDIX A: REVISION HISTORY

Revision A (April 2021)

- Converted Vectron document VC-829 to Microchip data sheet template DS20006515A.
- Minor grammatical text changes throughout.

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NOTES:

PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, contact your local Microchip representative or sales office.

<u>X</u> **Device** <u>X</u> -<u>X</u> X <u>X</u> -XXXXXXXXX <u>XX</u> -<u>X</u> <u>X</u> Part No. Power Output Temp. Stability Enable/ Load Custom Frequency Packaging Supply Range Disable Options **Examples:** VC-829: 32.768 kHz CMOS Crystal Oscillator in Device: a) VC-829-EAE-KAAN-32K768000TR: 4-Lead 3.2 mm x 2.5 mm Package VC-829, 3.3VDC, CMOS Output, -40°C to +85°C Temp Range, ±50 ppm Stability, 3.3VDC Ε = Enable High, 15 pF Load, Н = 2.5VDC **Power Supply:** Standard Option, 32.768 kHz, 1000/Reel J = 1.8VDC 1.61VDC to 3.63VDC (±50 ppm option only) b) VC-829-HAW-EAAN-32K768000TR: VC-829, 2.5VDC, CMOS Output, -10°C to +70°C Temp Range, ±20 ppm Stability, Output: **CMOS** Enable High, 15 pF Load, Standard Option, 32.768 kHz, 1000/Reel -40°C to +85°C -40°C to +105°C (±50 ppm option only) Temperature -40°C to +125°C (±50 ppm option only) Range: Tape and Reel identifier only appears in the Note 1: -10°C to +70°C catalog part number description. This identifier is used for ordering purposes and is not printed on the device package. Check with your Microchip ±20 ppm Sales Office for package availability with the Stability: ±25 ppm Tape and Reel option. ±50 ppm Enable/Disable: Enable/Disable, Enable High Α Load: 15 pF Custom Standard option Options: Frequency: 32K768000=32.768 kHz TR = 1,000/Reel

dlank>= Out Tape/ non-TR quantities Packaging: _SNPB= Tin lead solder dipped

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NOTES:

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- Microchip products meet the specifications contained in their particular Microchip Data Sheet.
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