

Date :- 25/03/2022

Examination Roll No. :- 21312915017

Name of Program :- B.Tech. (Information Tech.  
and Mathematical Innovation)

Semester :- I<sup>st</sup> Sem.

Unique Paper Code :- 32861109

Title of the Paper :- Understanding computer  
system Architecture.

Solution 3

x	y	z	A	B	C
0	0	0	0	0	1
0	0	1	0	0	0
0	1	0	0	1	1
0	1	1	0	1	0
1	0	0	1	0	1
1	0	1	1	0	0
1	1	0	1	1	1
1	1	1	1	1	0

for A

	$y'z'$	$y'z$	$yz$	$yz'$
$x'$	0	0	0	0
$x$	1	1	1	1

$$A = x$$

B

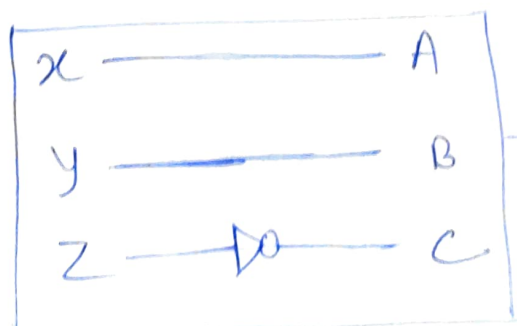
	$y'z'$	$y'z$	$yz$	$yz'$
$x'$	0	0	1	1
$x$	0	0	1	1

$$B = y$$

C

	$y'z'$	$y'z$	$yz$	$yz'$
$x'$	1	0	0	1
$x$	1	0	0	1

$$C = z'$$



→ Combinational  
Circuit.

Roll No. :- 21312915017  
UPC :- 32861104

②

→ 3 input NAND gate using 2 input NAND gate

