Tasks

	Task	Time
✔	▼ ▶ Compile Design	00:07:08
✔	▶ ▶ Analysis & Synthesis	00:01:05
✔	▶ ▶ Fitter (Place & Route)	00:05:25
✔	▶ ▶ Assembler (Generate programming files)	00:00:14
✔	▶ ▶ Timing Analysis	00:00:16
✔	▶ ▶ EDA Netlist Writer	00:00:08
	📁 Edit Settings	

Compilation

Power Analyzer Summary

🔍 <<Filter>>

Power Analyzer Status	Successful - Thu Dec 4 12:51:46 2025
Quartus Prime Version	25.1std.0 Build 1129 10/21/2025 SC Lite Edition
Revision Name	corelet
Top-level Entity Name	corelet
Family	Cyclone IV GX
Device	EP4CGX150DF31I7AD
Power Models	Final
Total Thermal Power Dissipation	245.40 mW
Core Dynamic Thermal Power Dissipation	24.16 mW
Core Static Thermal Power Dissipation	119.39 mW
I/O Thermal Power Dissipation	101.85 mW
Power Estimation Confidence	Low: user provided insufficient toggle rate data

Analysis & Synthesis Summary

🔍 <<Filter>>

Analysis & Synthesis Status	Successful - Thu Dec 4 12:43:13 2025
Quartus Prime Version	25.1std.0 Build 1129 10/21/2025 SC Lite Edition
Revision Name	corelet
Top-level Entity Name	corelet
Family	Cyclone IV GX
Total logic elements	22,845
Total registers	11970
Total pins	227
Total virtual pins	0
Total memory bits	0
Embedded Multiplier 9-bit elements	0
Total GXB Receiver Channel PCS	0
Total GXB Receiver Channel PMA	0
Total GXB Transmitter Channel PCS	0
Total GXB Transmitter Channel PMA	0
Total PLLs	0

Slow 1200mV 100C Model Fmax Summary

🔍 <<Filter>>

	Fmax	Restricted Fmax	Clock Name
1	129.02 MHz	129.02 MHz	clk