Gao Shengqi

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Personal Summary

- I am optimistic and cheerful, with excellent academic performance, strong self-motivation, good communication skills, and team spirit, capable of working communication in English (CET-6 score 510).
- I have six years of Linux experience, rich experience in software development, open source project contributions, and maintenance. I am skilled in technical writing and continuously follow the development of internet technology.
- Job intention: Software and hardware development and related internships.

Educational Experience

ShanghaiTech University |Information and Communication Engineering, School of Information | *Academic Master's Degree* 2023.09—2026.06(Expected)

My main research direction is **Low Power Internet of Things**, with some research and engineering experience in embedded development and system design. **Graduating in 2026**.

Central South University |Resource Exploration Engineering, Information and Communication Engineering, School of Geosciences and Information Engineering | *Bachelor of Engineering* 2019.09—2023.06

GPA: 3.0/4.0(Top 40% in the major),

Technical Skills[1]

- **Embedded Development**: Keil, C++, Microcontroller Debugging —ARM, ESP32, 51
- **Front and Back Ends**: Rust, Golang, Python, MySQL, React, Vue.js, JavaScript/TypeScript, HTML/CSS
- **Operations and Maintenance**: Linux, Docker, Shell, Tomcat, Nginx
- **FPGA Development**: Experience in developing products under Altera
- **Paper Production Ability**: Backed by the laboratory, with experimental equipment and an academic environment

Project Experience

Development of an Embedded System with Specific Timing Functions |*Finite State Machine and Low Power Programming* 2024.03—2024.04

- Based on the architecture of ZYX (an open-source blockchain framework implemented in Rust), **modified and implemented a consensus algorithm**.
- Conducted performance testing on the system, analyzed bottlenecks, and optimized throughput; TPS increased from 1K to 6K.
- This project is part of a laboratory research project.

MINIST Handwritten Digit Dataset Recognition |*FPGA Development* 2023.05—2023.09

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Front and Back End Separated Program Development |*Program Development* 2023.05—2023.09

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[1] Skills unrelated to the job position are omitted or represented in gray.