

# Analog Electronics

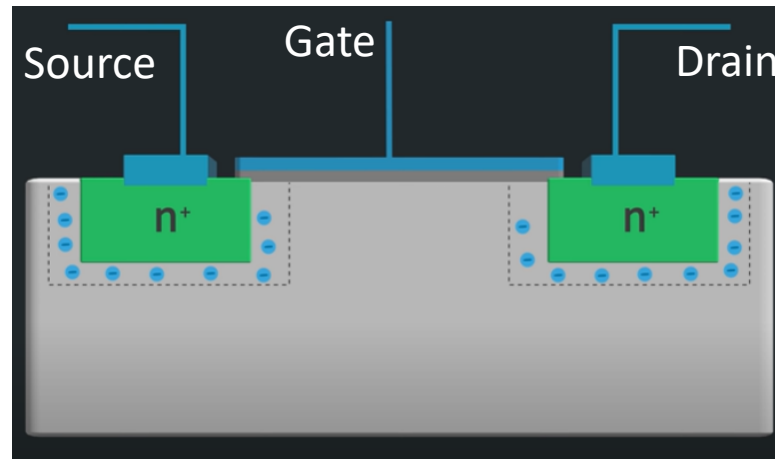
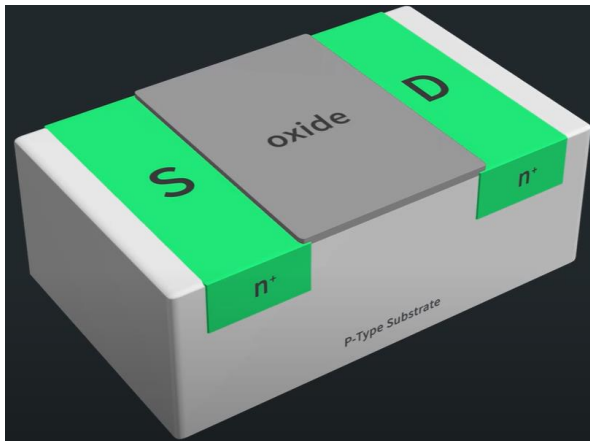
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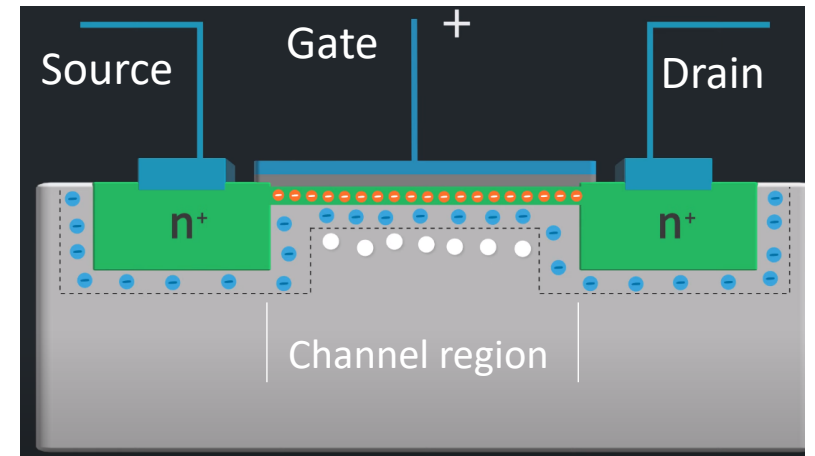
# Agenda

- Introduction to enhancement MOSFET
  - Structure and operation regions
  - Large & small signal model
- MOSFET VS. BJT
- Frequency response of circuit

# Metal-Oxide-Semiconductor Field-Effect Transistor (MOSFET)

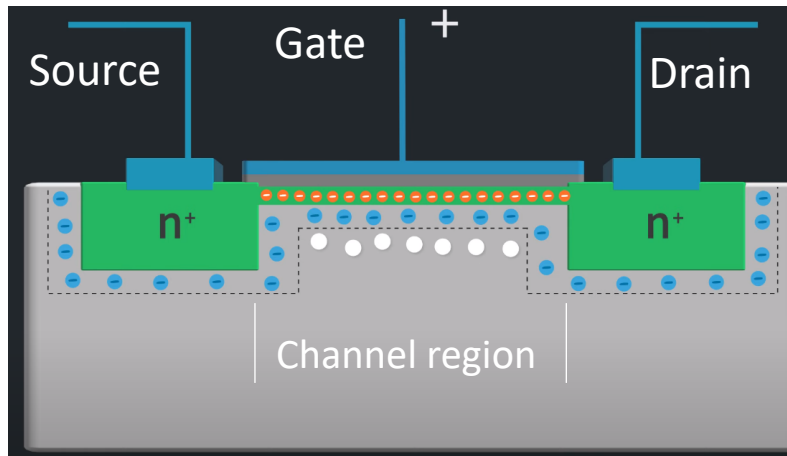


$V_G < V_{TH} \rightarrow$  'cut-off' region

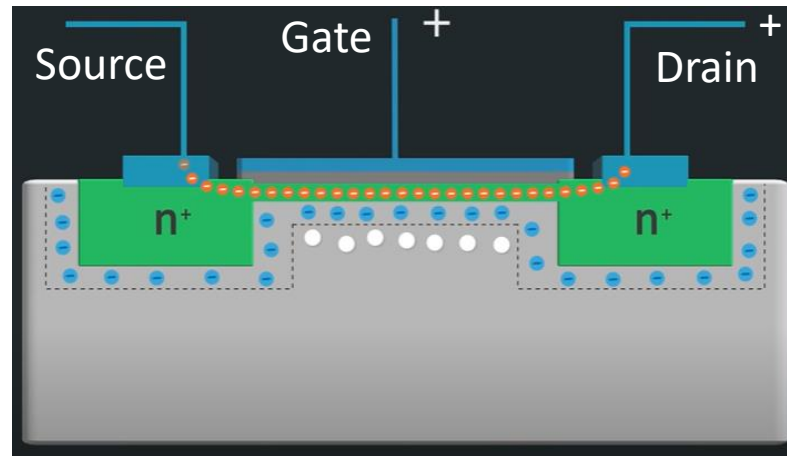


$V_G > V_{TH} \rightarrow$  A channel

# Metal-Oxide-Semiconductor Field-Effect Transistor (MOSFET)



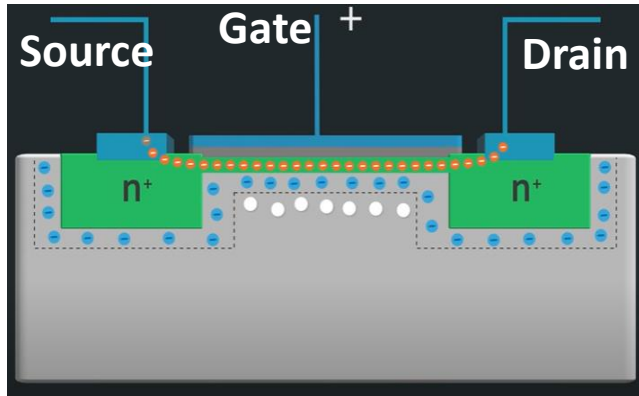
$V_G > V_{TH} \& V_{DS} = 0$   
Turn on but no current



$V_G > V_{TH} \& V_{DS} > 0$   
The current flows from D to S.

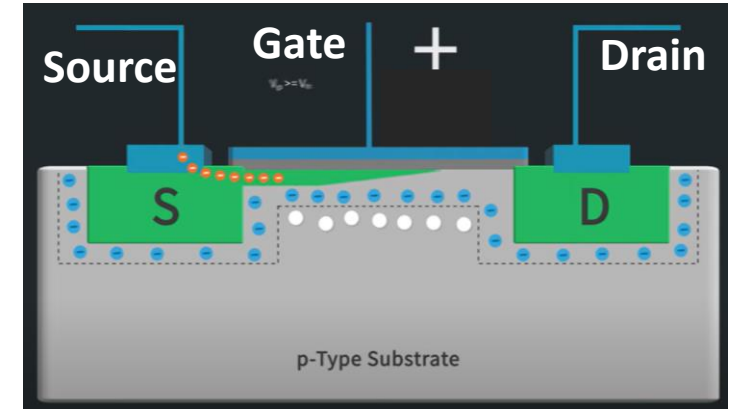
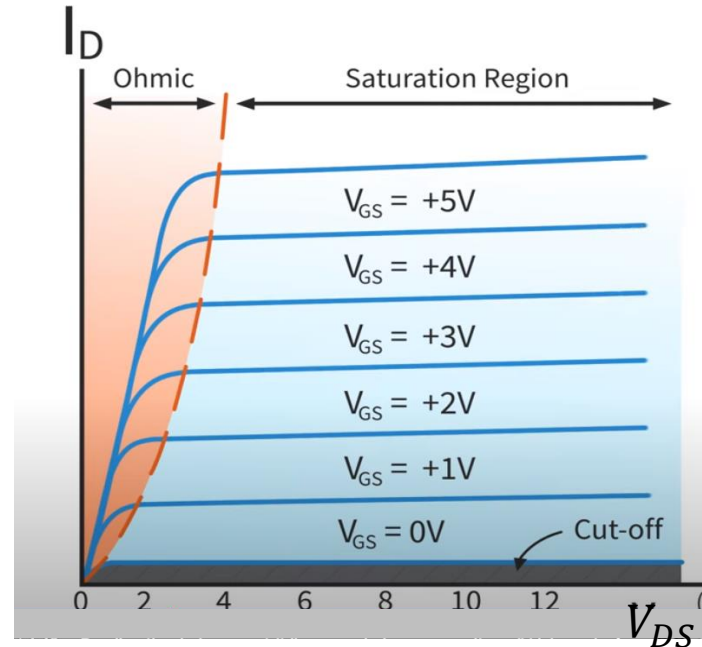
Enhancement MOSFET: increasing gate voltage 'enhances' the channel conductivity.  
As  $V_G$  increases, electron density in channel increases → resistance between S and D decreases.  
A voltage controlled current source.

# Operation regions of MOSFET



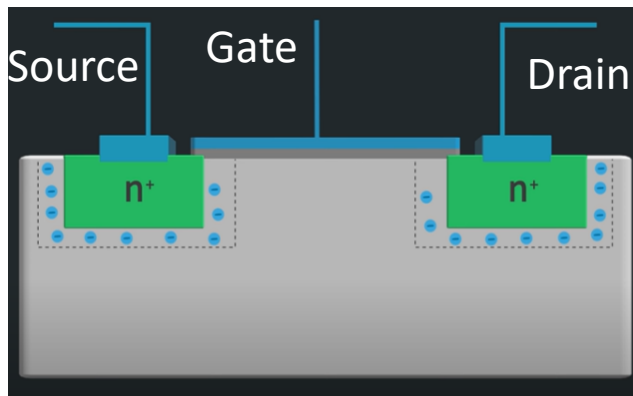
Triode or ohmic region:  $V_{GD} > V_{TH}$

$$I_D = k_n [(V_{GS} - V_{TH})V_{DS} - \frac{1}{2}V_{DS}^2]$$



Saturation region:  $V_{GD} < V_{TH}$   
 $I_D = \frac{1}{2}k_n (V_{GS} - V_{TH})^2 (1 + \lambda V_{DS})$

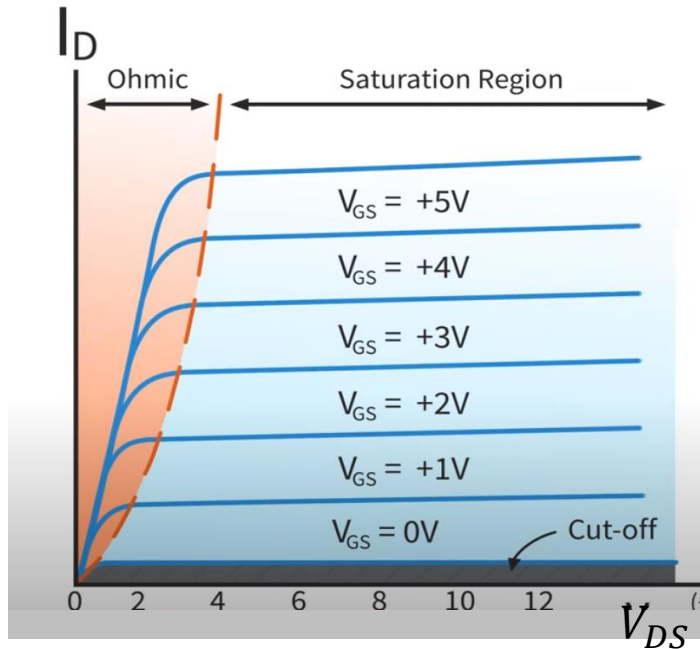
- $\lambda = \frac{L - L'}{V_{DSL}}$ : channel length modulation coefficient
- $L'$ : actual channel length



Cut-off region:  $V_{GS} < V_{TH}$   
 $I_D = 0$

Triode and cut-off region: switching devices  
 Saturation region: amplifier

# Operation regions of MOSFET



$$I_D = \begin{cases} 0, \\ k_n[(V_{GS} - V_{TH})V_{DS} - \frac{1}{2}V_{DS}^2], \\ \frac{1}{2}k_n(V_{GS} - V_{TH})^2(1 + \lambda V_{DS}), \end{cases}$$

**MOSFET transconductance parameter:**

$$k_n = \mu_n C_{ox} W / L$$

- $\mu_n$ : mobility of the electrons at the surface of the channel
- $C_{ox}$ : oxide capacitance
- $W$  &  $L$ : width & length of the channel

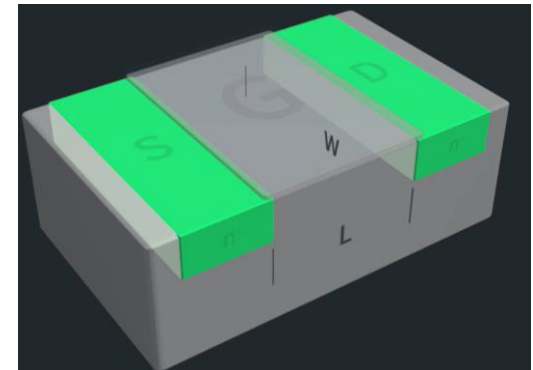
**Overdrive voltage:**  $V_{ov} = V_{GS} - V_{TH}$

**Threshold voltage:**  $V_{TH}$  ---- 0.3 V ~ 1V

cut-off:  $V_{GS} < V_{TH}$

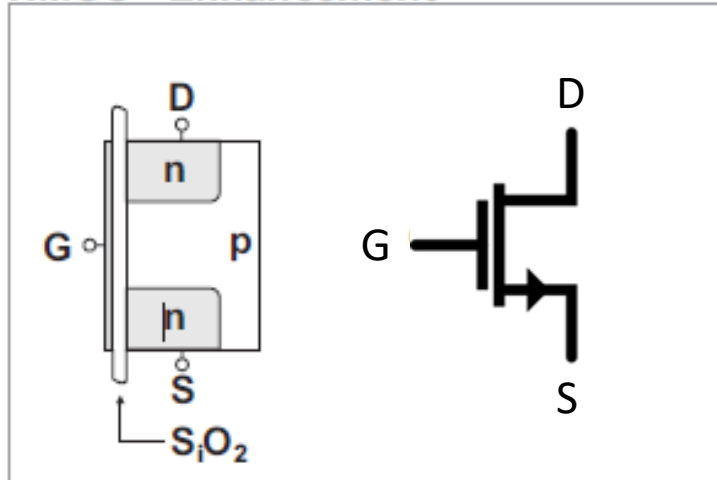
triode:  $V_{GD} > V_{TH}$

saturation:  $V_{GD} < V_{TH}$

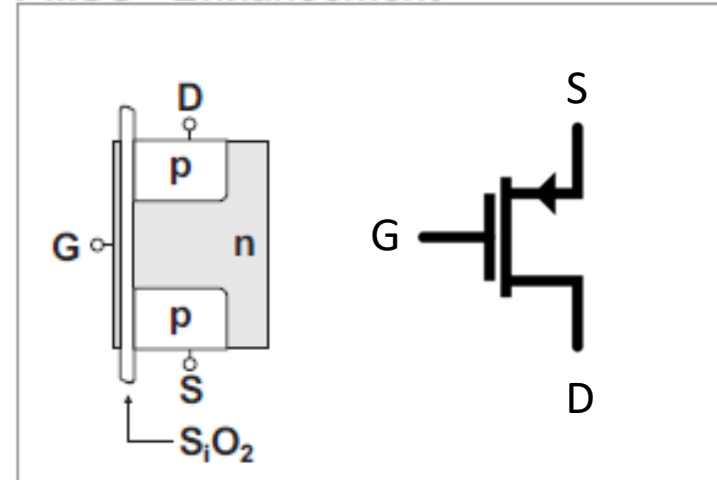


# MOSFET-- symbol

NMOS - Enhancement



PMOS - Enhancement

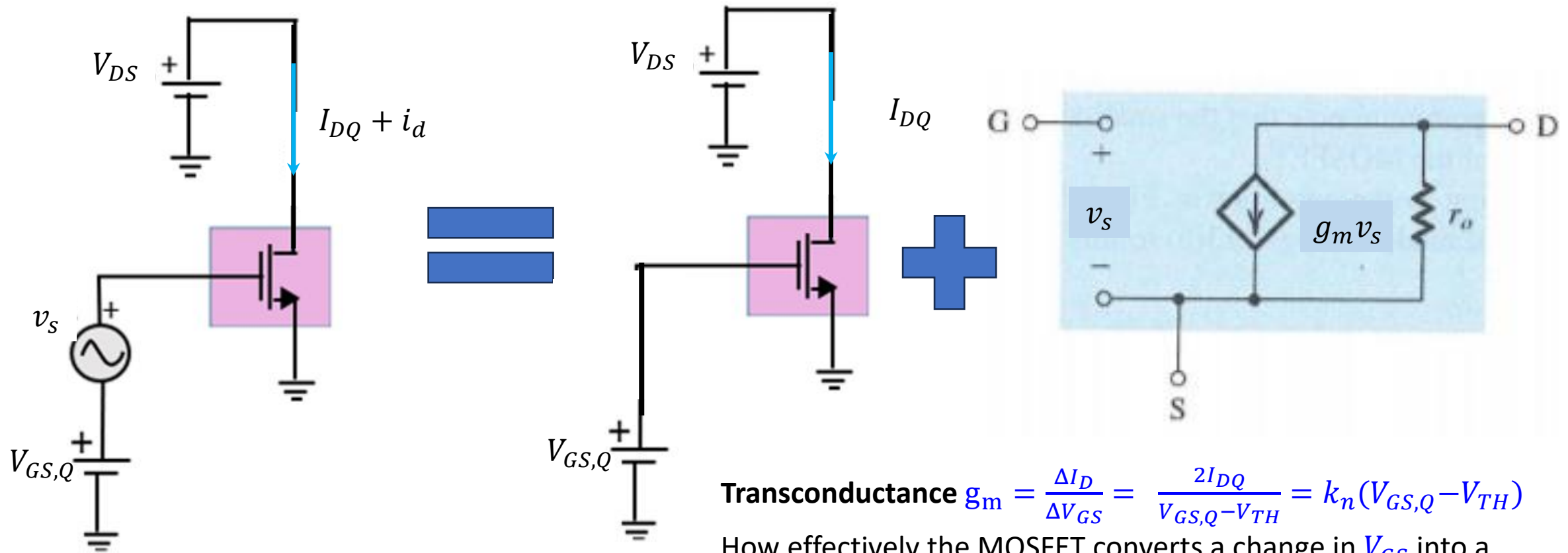


# Agenda

- Introduction to enhancement MOSFET
  - Structure and operation regions
  - Large & small signal model
- MOSFET VS. BJT
- Frequency response of circuit



# MOSFET—small-signal model



$V_{GS,Q} + v_s - V_{DS} \leq V_{TH}$   
In saturation region

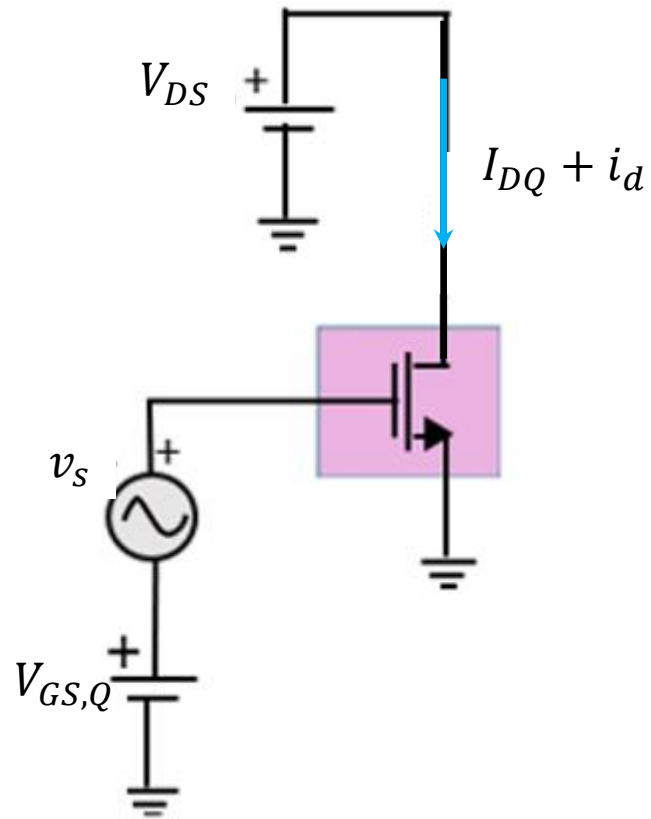
**Transconductance**  $g_m = \frac{\Delta I_D}{\Delta V_{GS}} = \frac{2I_{DQ}}{V_{GS,Q} - V_{TH}} = k_n(V_{GS,Q} - V_{TH})$

How effectively the MOSFET converts a change in  $V_{GS}$  into a change in  $I_D$ .

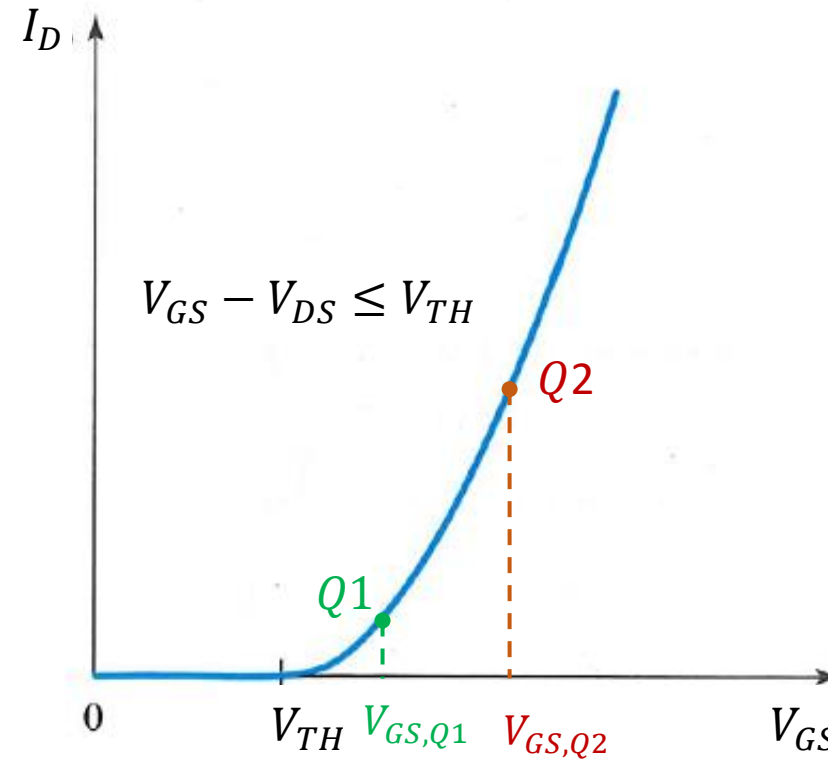
**Output resistance:**  $r_o = \frac{\Delta V_{DS}}{\Delta I_D} = \frac{v_s}{i_d} = \frac{1}{I_{DQ}\lambda}$

# Quiz

Which operating point is preferred, **Q1** or **Q2**?



$V_{GS,Q} + v_s - V_{DS} \leq V_{TH}$   
In saturation region



# Agenda

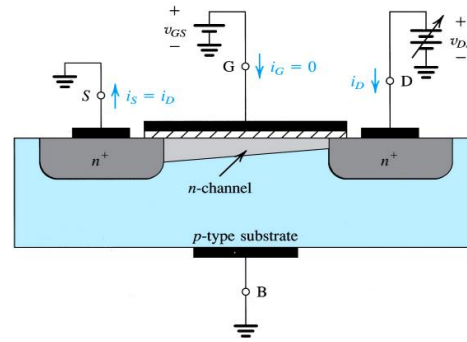
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- **MOSFET VS. BJT**
- Frequency response of circuit

# MOSFT

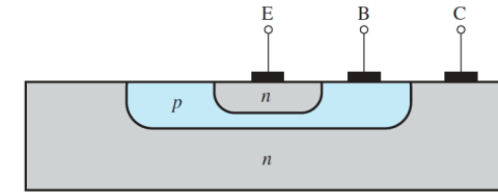
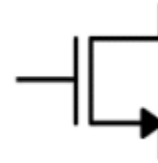
VS.

# BJT

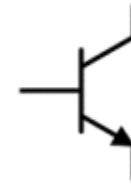
## Structure



N-type or P-type



NPN or PNP



## Symmetric fabrication

Yes

No

## Dominant I-V characteristics

$$I_D = \frac{1}{2} k_n (V_{GS} - V_{TH})^2 (1 + \lambda V_{DS}) - \text{Saturation}$$

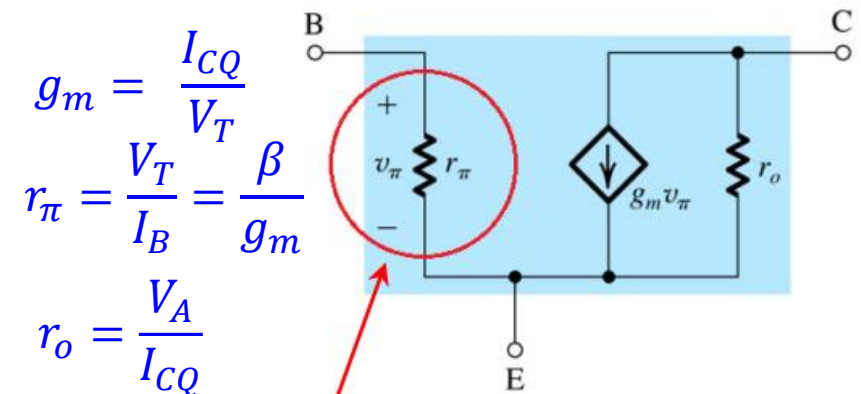
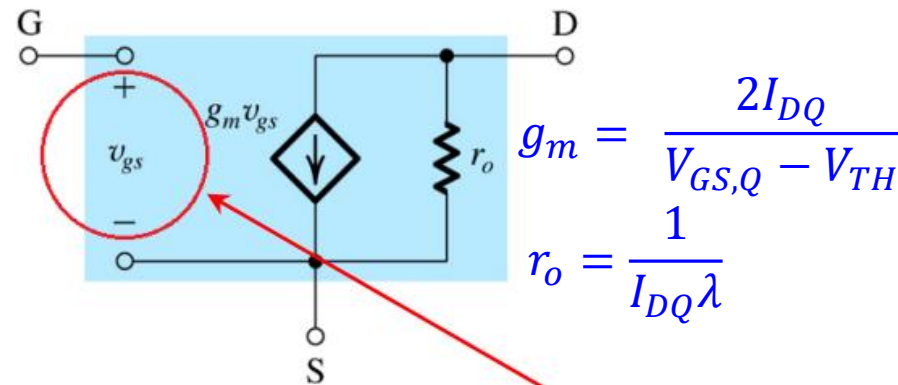
$$I_C = I_s (e^{\frac{V_{BE}}{V_T}} - 1) - \text{forward active}$$

## Current control

**No** current flowing through G and S. The voltage of GS that controls the current through D and S.

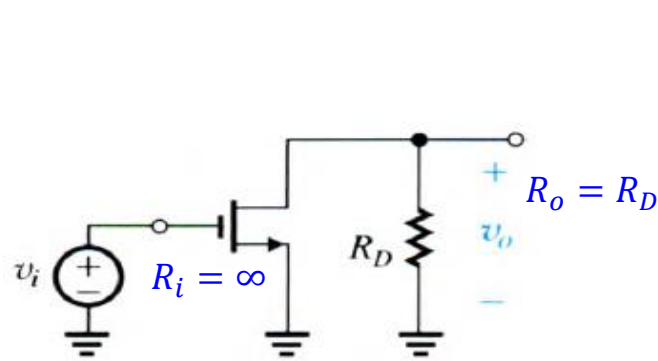
A **small** current flowing through B and E controls a much larger current through C and E.

## Small-signal model



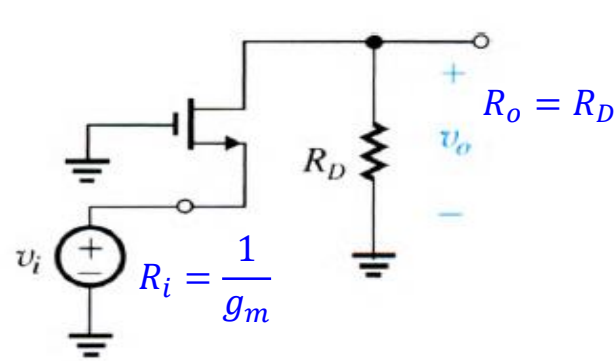
# Three basic configurations: MOSFT **VS.** BJT

MOSFT



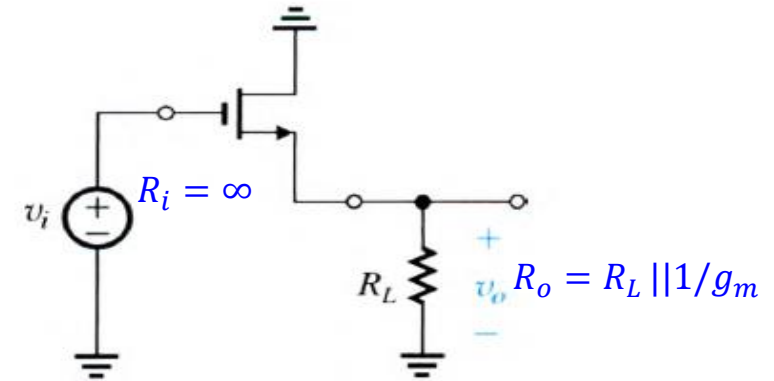
(a) Common-Source (CS)

$$A_v = -g_m R_D$$



(b) Common-Gate (CG)

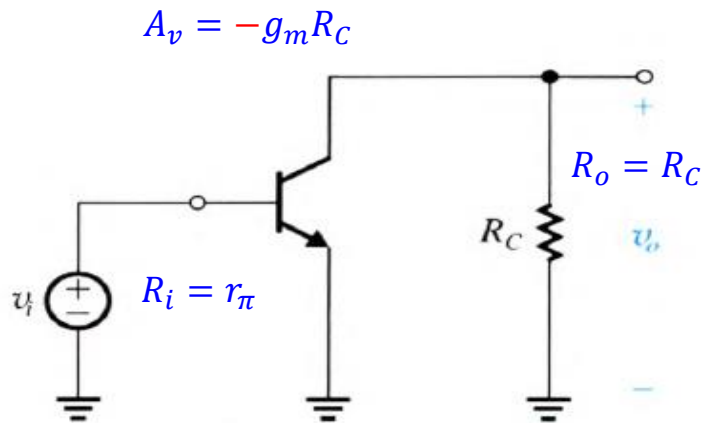
$$A_v = g_m R_D$$



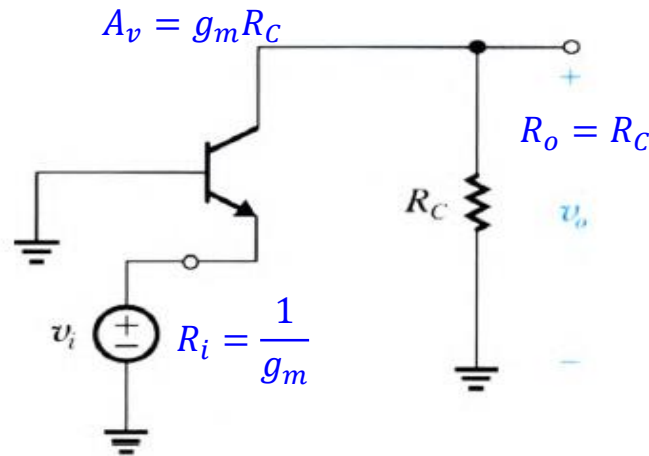
(c) Common-Drain (CD)  
or Source Follower

$$A_v = \frac{R_L}{R_L + 1/g_m}$$

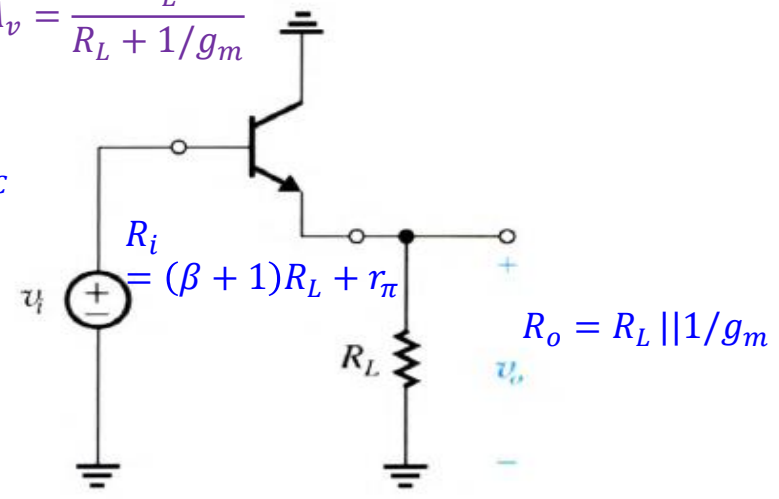
BJT



(d) Common-Emitter (CE)



(e) Common-Base (CB)

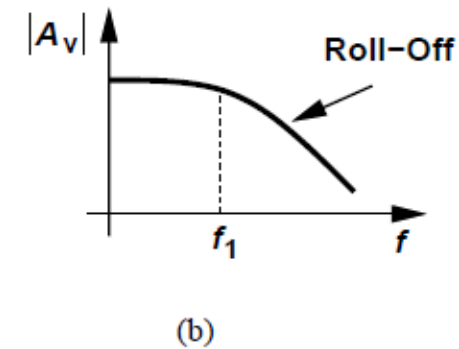
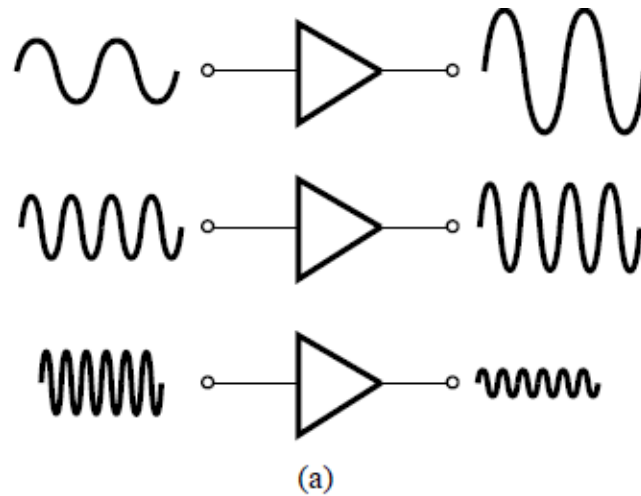
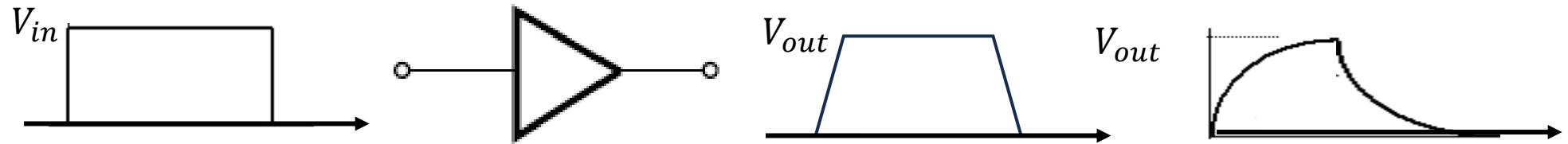


(f) Common-Collector (CC)  
or Emitter Follower

# Agenda

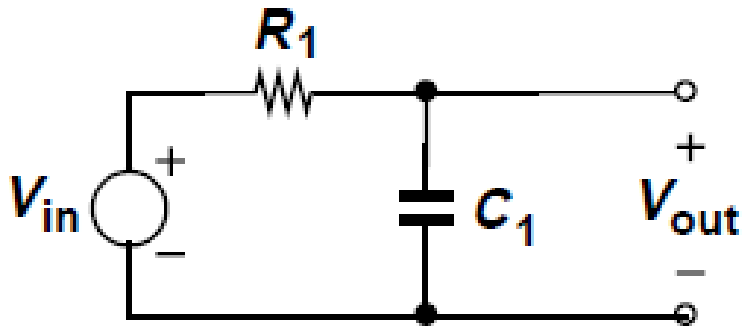
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# Frequency response — 'speed limitation'



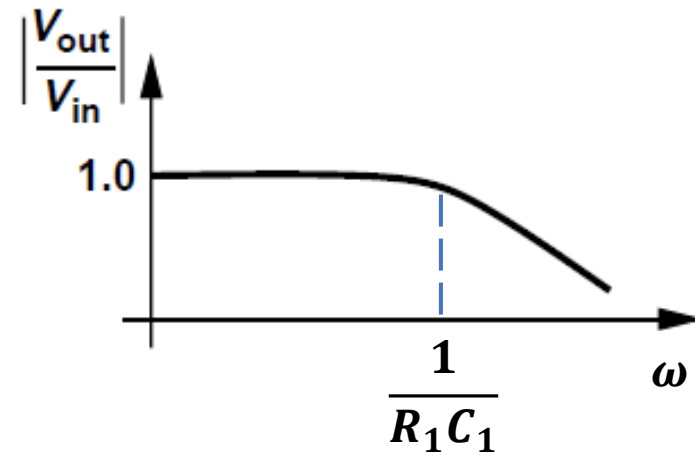
Gain of transistors is not constant over frequencies.

# Frequency response



Time-domain analysis

$$\begin{aligned} V_{out}(t) &= V_{in}(t) - I_1(t)R_1 \\ &= V_{in}(t) - R_1 C_1 \frac{dV_{out}(t)}{dt} \end{aligned}$$



Frequency-domain analysis

$$V_{out}(s) = \left[ \frac{\frac{1}{C_1 s}}{R_1 + \frac{1}{C_1 s}} \right] V_{in}(s)$$

Transfer function of the circuit

$$H(s) = \frac{1}{1 + sR_1 C_1} \rightarrow H(j\omega) = \frac{1}{1 + j\omega R_1 C_1}$$



# Transfer function and frequency response

- The transfer function of a circuit:

$$H(s) = \frac{Y(s)}{X(s)} = A_0 \frac{(s - z_1)(s - z_2) \dots (s - z_M)}{(s - p_1)(s - p_2) \dots (s - p_N)}$$

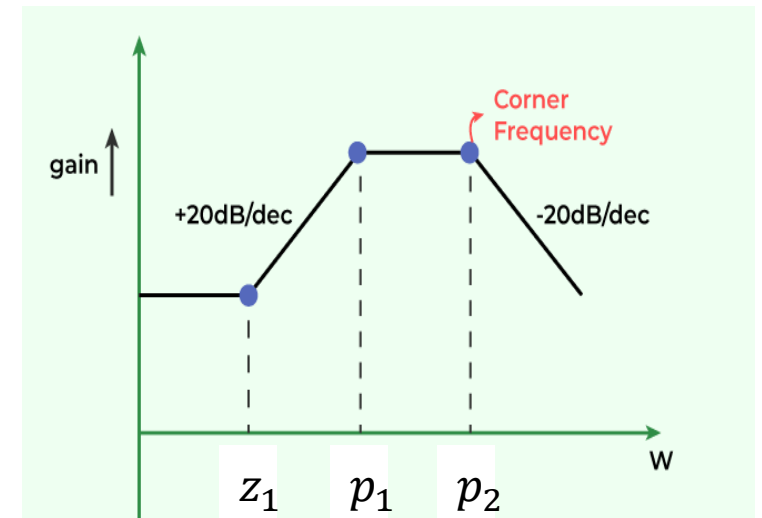
$$A(\omega) = |H(j\omega)| = |A_0 \frac{(j\omega - z_1)(j\omega - z_2) \dots (j\omega - z_M)}{(j\omega - p_1)(j\omega - p_2) \dots (j\omega - p_N)}|$$

$$\omega = 2\pi f$$

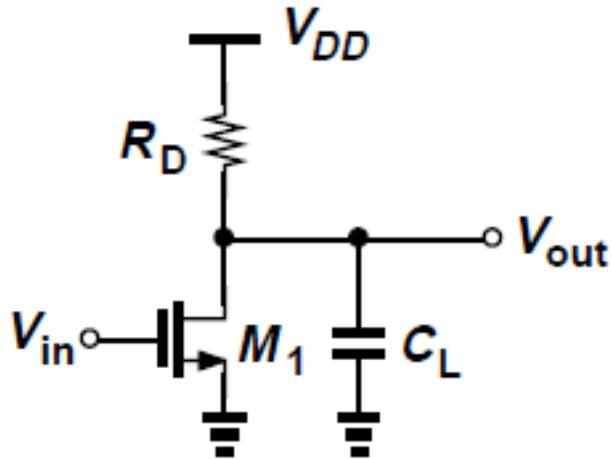
$z_m$  and  $p_n$  are zeros and poles

Bode's rules to fast construct  $A(\omega)$  plot approximately:

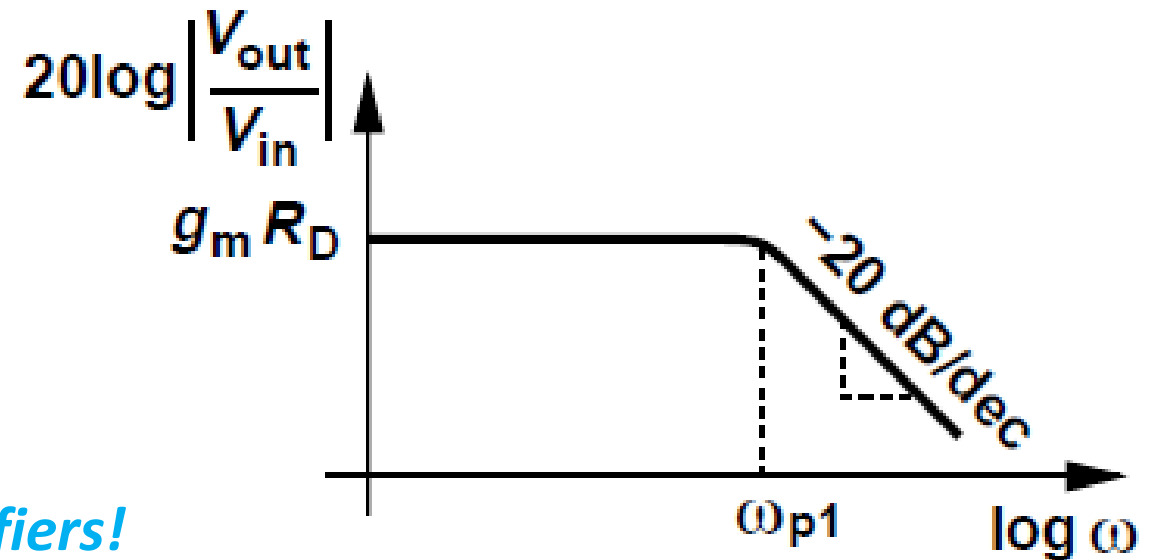
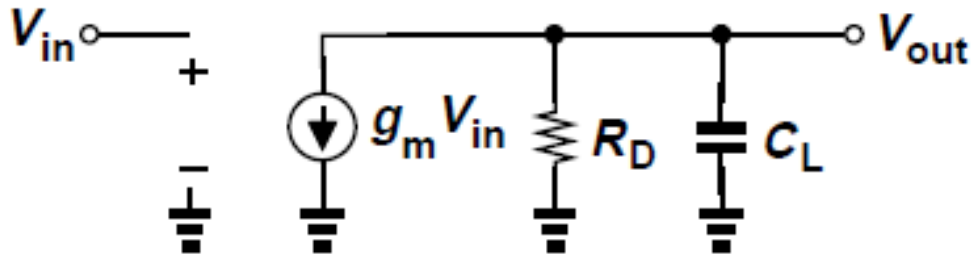
As  $\omega$  passes a **pole** / **zero**, the slope  $A(\omega)$  **decreases** / **increases** by 20 dB/dec.



# Example



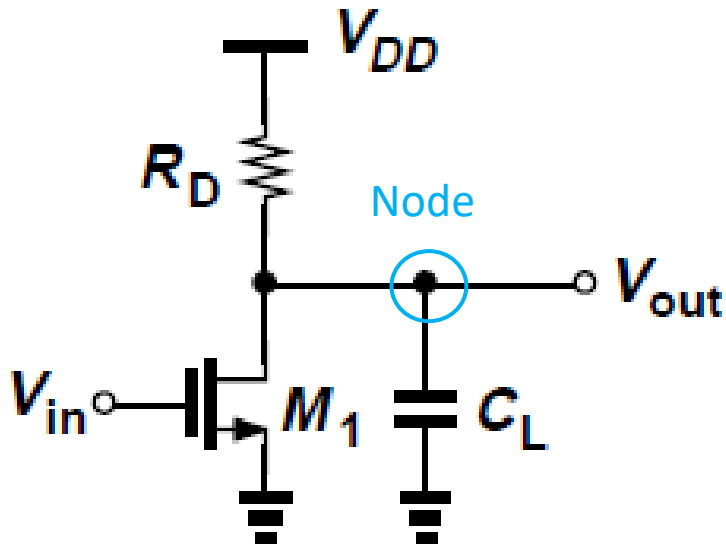
$$A(\omega) = \left| -\frac{g_m R_D}{j\omega R_D C_L + 1} \right| = \frac{g_m R_D}{\sqrt{(\omega R_D C_L)^2 + 1}}$$
$$|\omega_{p1}| = \frac{1}{R_D C_L}$$



*Capacitors generally limit the BW of amplifiers!*

# Finding poles by inspection

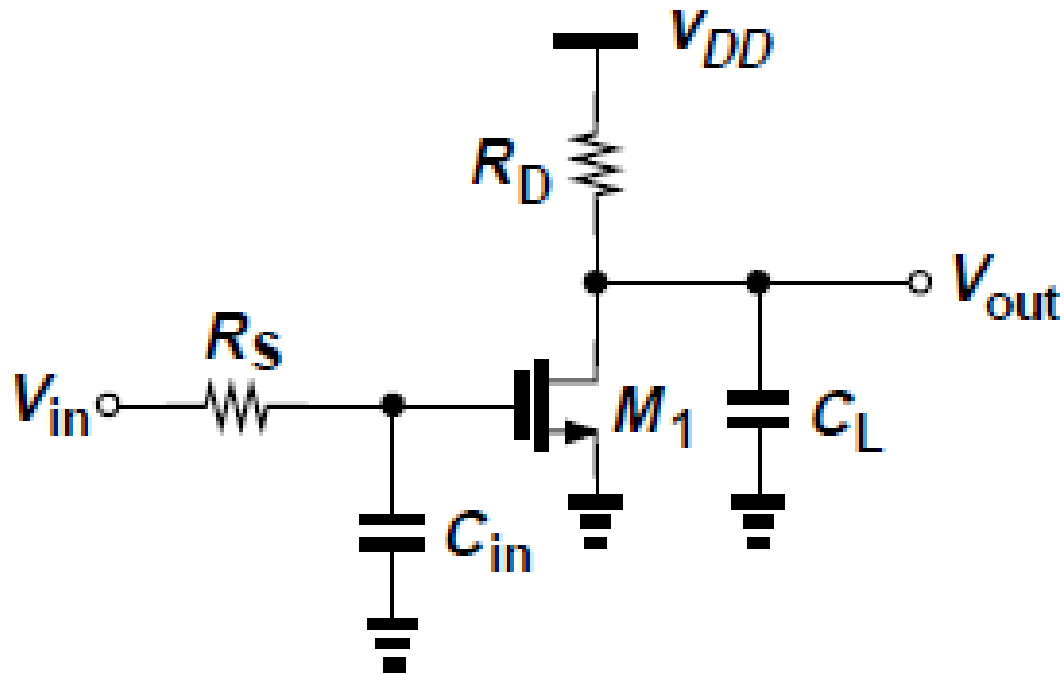
- Set all independent source to 0:
  - voltage source  $\rightarrow$  short circuit; Current source  $\rightarrow$  open circuit.
- Find capacitors connected between the node and AC ground.
- Find resistors connected between that node and AC ground.



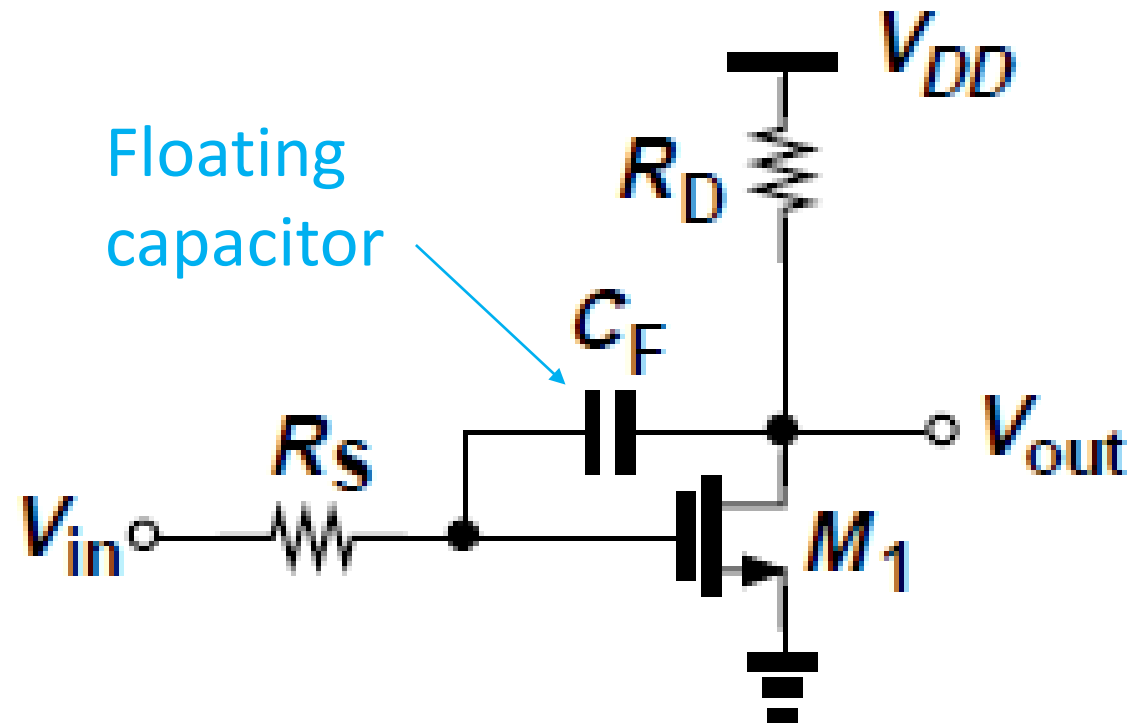
$$\omega_p = \frac{1}{R_D C_L}$$

$$H(\omega) = \frac{V_{out}}{V_{in}}(\omega) = -\frac{g_m R_D}{1 + s/\omega_p}$$

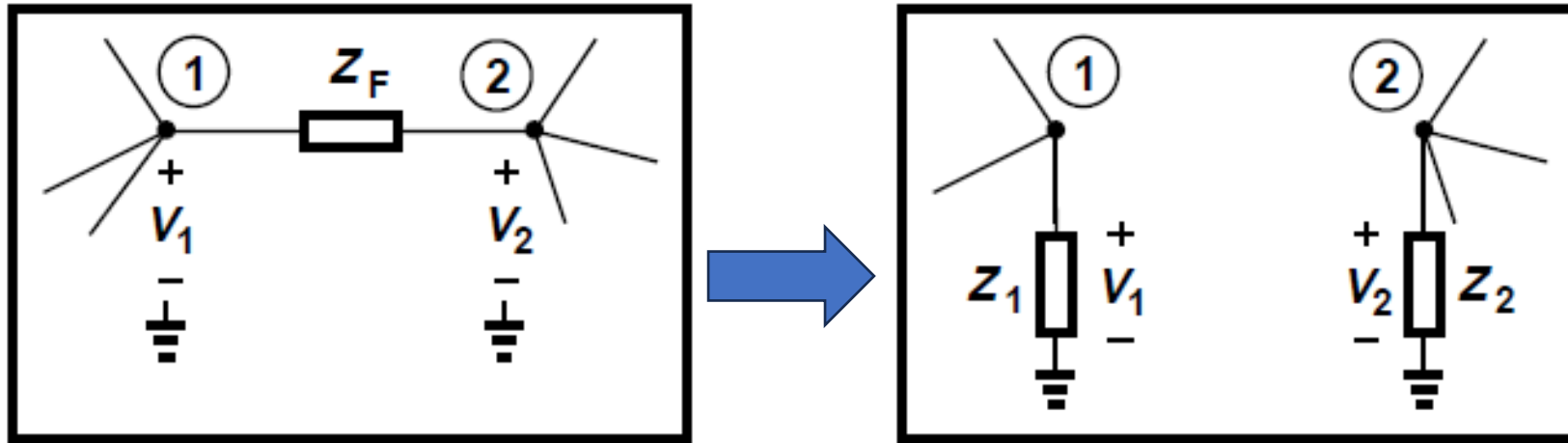
Quiz: find the poles and plot  $|H(j\omega)|$



Can we find the poles?



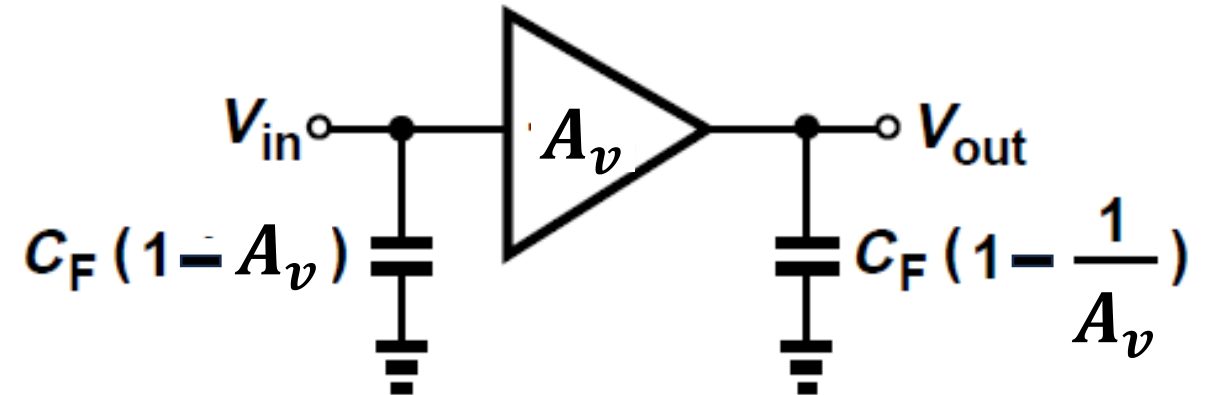
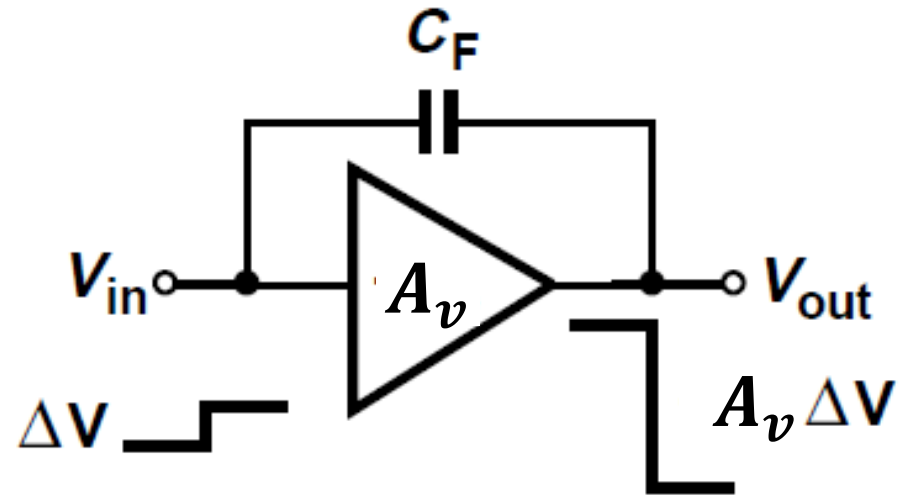
# Miller's theorem



Decompose a floating capacitor to two grounded capacitors.

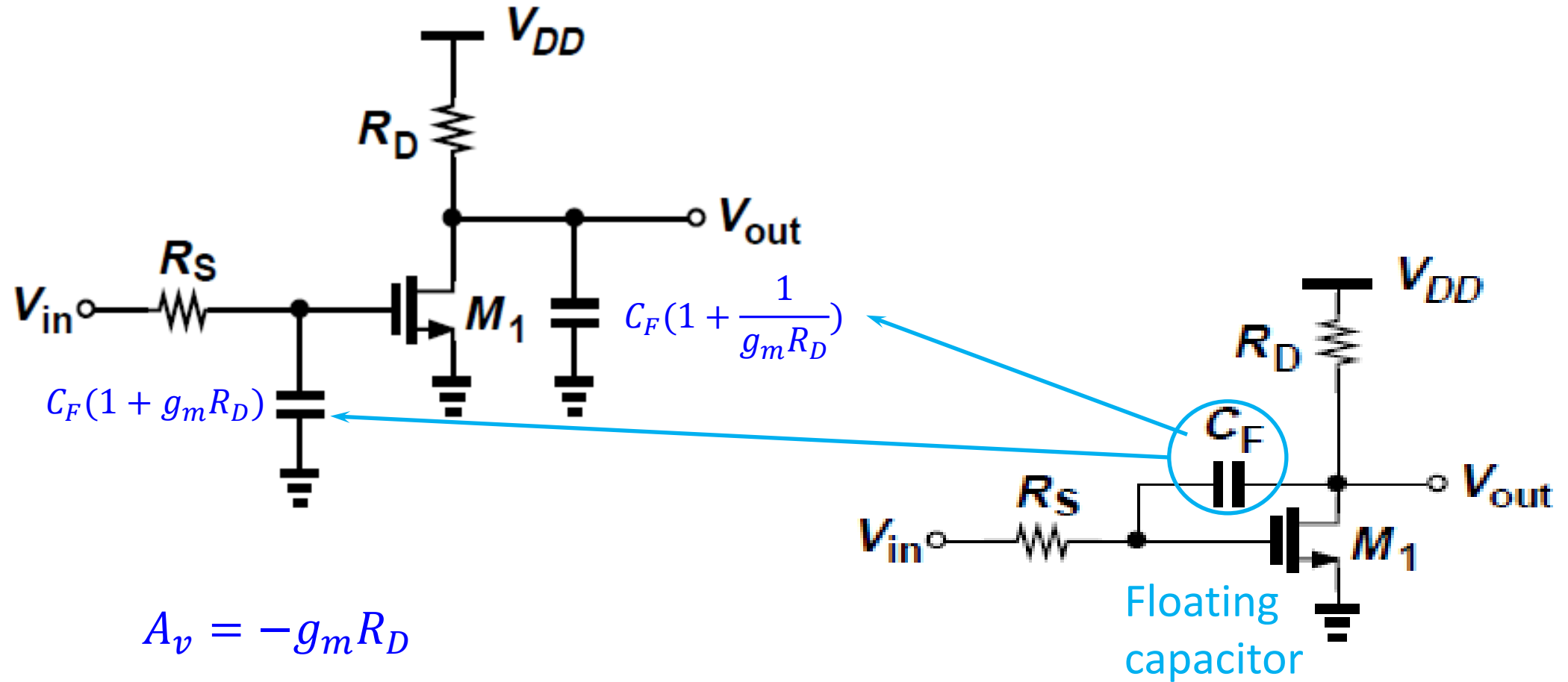
$$Z_F \Rightarrow Z_1 \text{ \& \; } Z_2$$

# Example



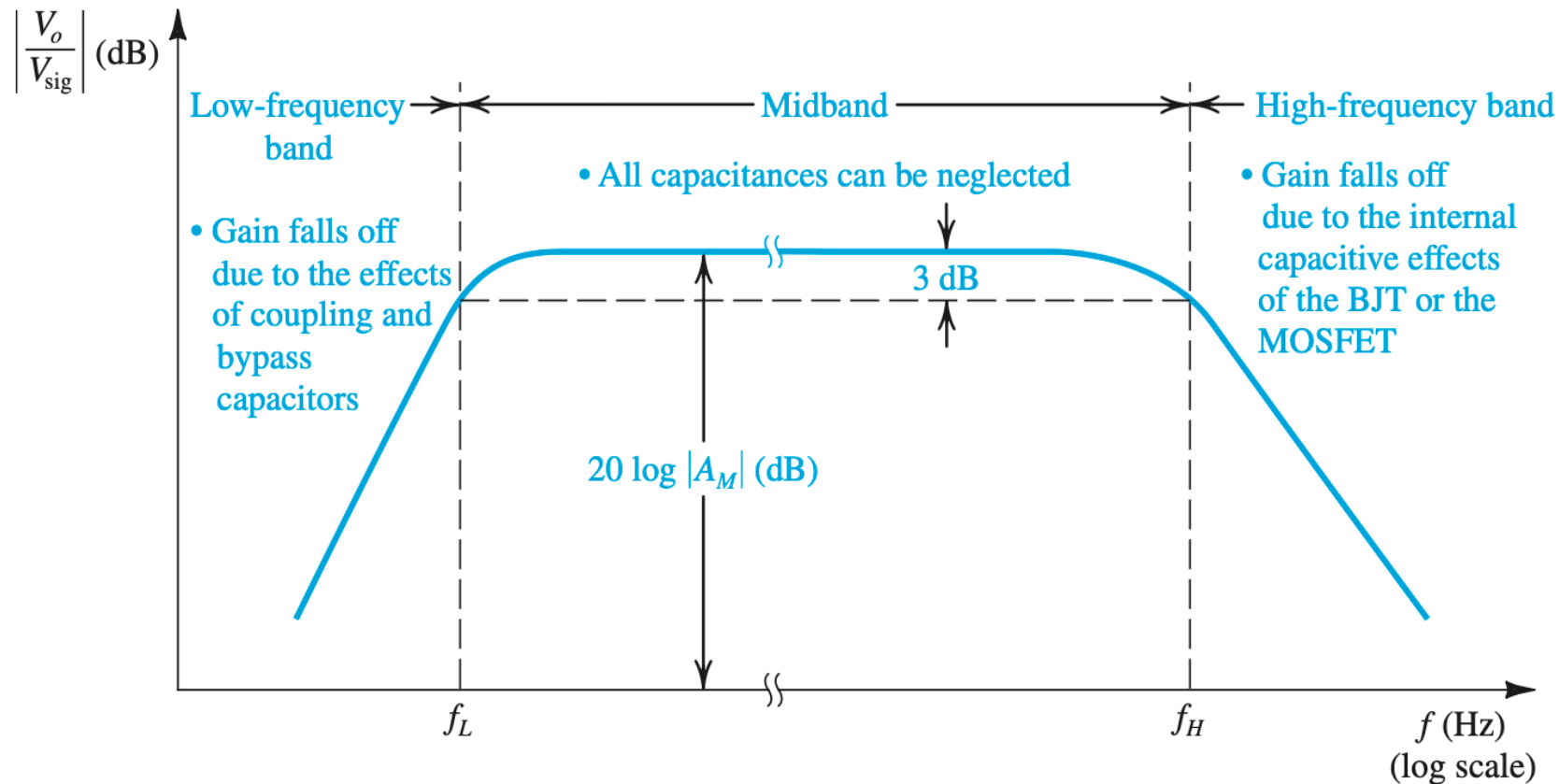
$$A_v = V_{out}/V_{in}$$

We can find the poles by Miller's approximation



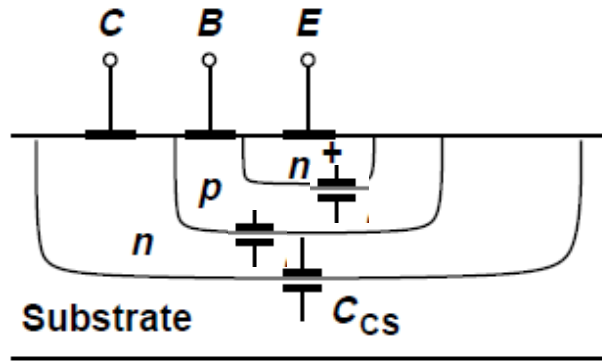


# The role of capacitors

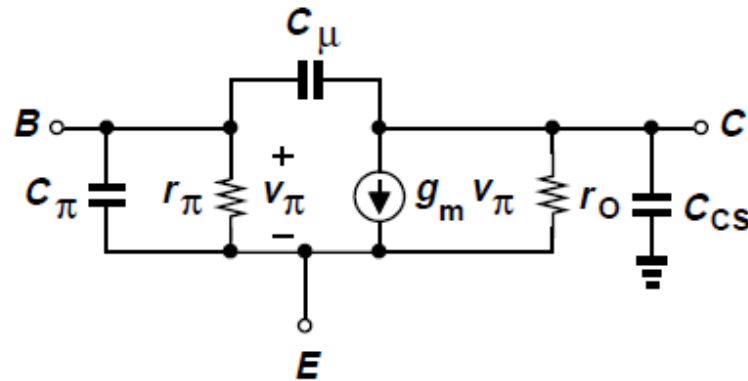


**Figure 10.1** Sketch of the magnitude of the gain of a discrete-circuit BJT or MOS amplifier versus frequency. The graph delineates the three frequency bands relevant to frequency-response determination.

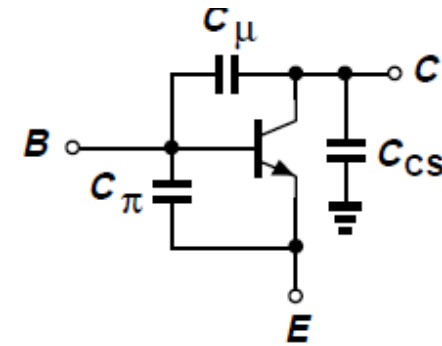
# High-frequency models of BJT



(a)



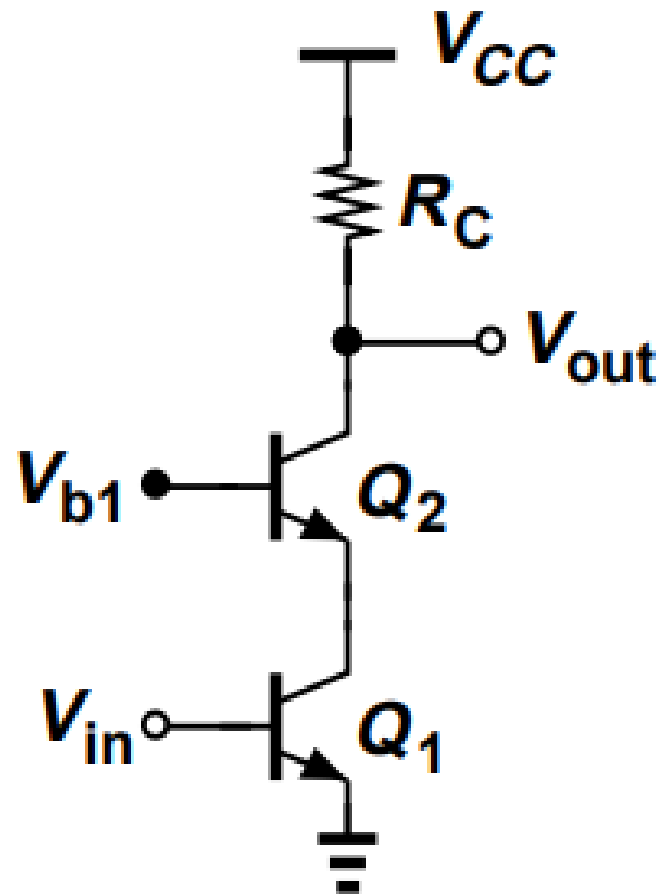
(b)



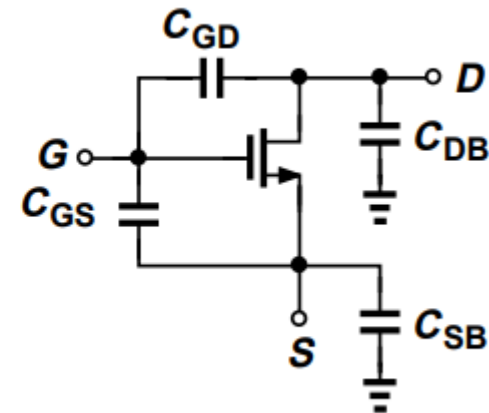
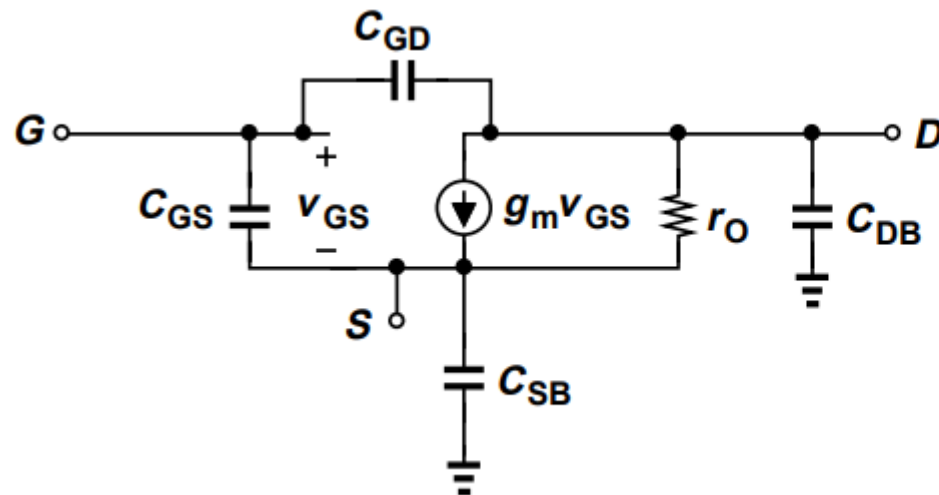
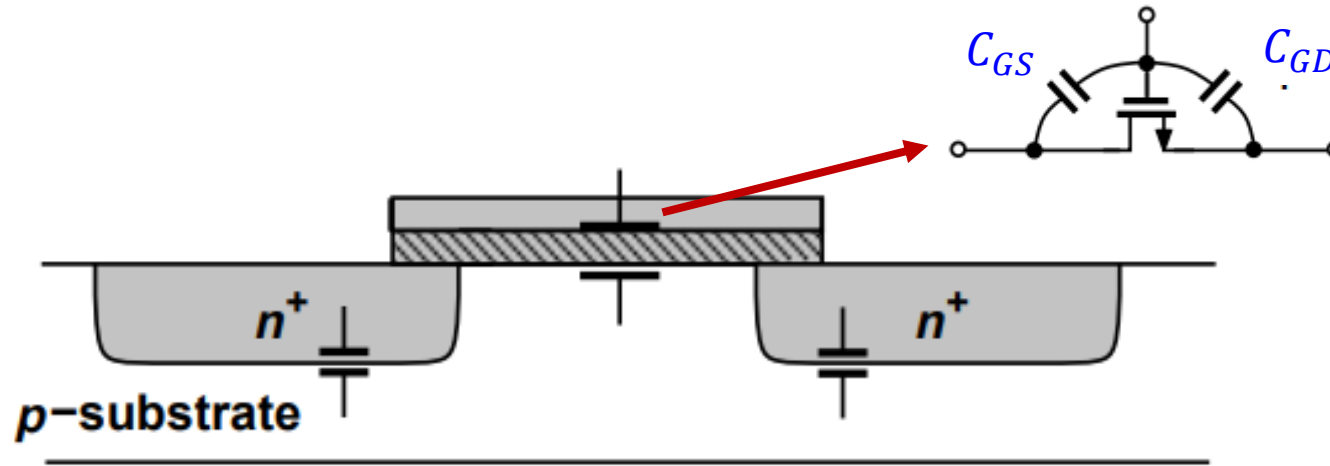
(c)

(a) Structure of an integrated bipolar transistor, (b) small-signal model including collector-substrate capacitance, (c) device symbol with capacitances shown explicitly.

Example : identify all of the capacitances



# High-frequency models of MOSFET



Quiz: identify all of the capacitances

