		5	4	3	2		1		
_		Contents			Revisions				
						Rev Descript	ion	Date	App
		ım				X1 Initial	Draft	02/01/13	
_	OP 0 0	ZRFACE	-			1. Remov 2. Remap 3. Add o	e Motor Control Connector J12 J1, J2, J9, J10 pinout series resister R31 on Y1 5, R35 to isolate OpenSDA and		
5	I/O Headers	and Power Supply					CU on net RST_TGTMCU to avoid	03/05/13	
						5. Add R 6. Add R measurin	13 to bypass voltage drop on D2 11 (10ohm) to support current g		
						MBR120VL	e BAT54C (D2, D3, D6) to SFT1G diodes (D2, D8, D3, D7,		

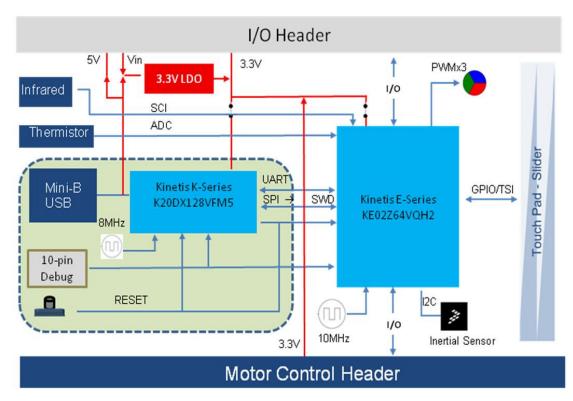
FREEDOM KE02Z

fr	eesca	ale	Automotive, Industrial & Multi- Market Solutions Group 6501 William Cannon Drive West Austin, TX 78735-8598				
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capability.

8. Add R59 (0 ohm) to separate net P3V3_KE02Z and VREFH





In dicates option al items that will not be populated by default

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