

共 12 分

## 9.2

because synchronous devices have **same clock signal**, therefore the **processer could know when to read/write data**.

(2', 意思相近即可, 两点各1')

## 9.6

If KBSR[15] is 0, **there is no input which we have not read**. So it may **read an outdated value**. (or read same character several times)

(2', 答到没有新输入或读入重复字符都可以给分)

## 9.10

(题目修正 display hardware → program)

If DSR[15] is 0, the **output device is still processing last value in DDR**. So it may cause that **data to be lost**.

(2', 同上, 合理即可)

## 9.14

It will **NOT** load from memory. For that **xFE02** is in I/O page, it refers to KBSR register.

(4' 答到 **xFE02** 指向 KBSR 则满分, 如果解释不对, 回答不会访问mem可得 1')

## 9.26

R7 will be covered when call JSR multiple times.

(2' 意思相同即可, 题目中说了要少于 10 词, 太长可以扣一分)