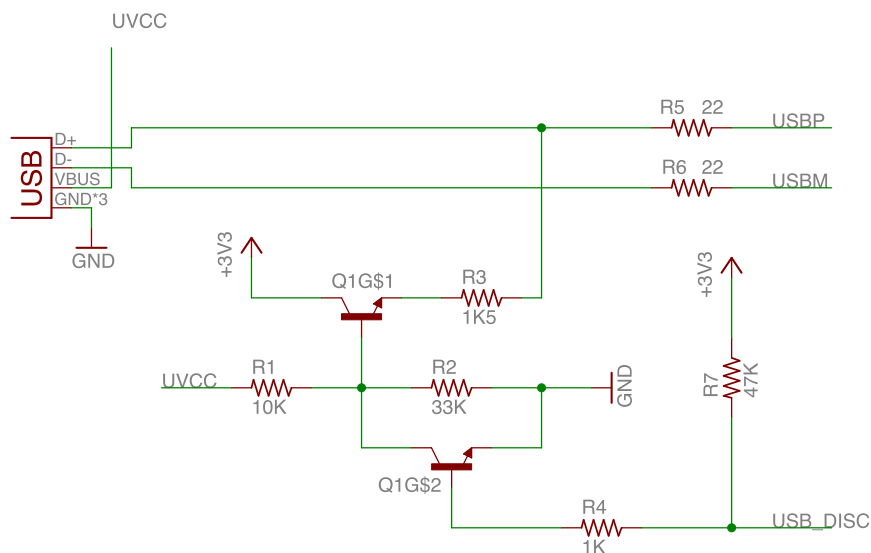
[illegible]

Spark Core

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SPI_MOSI	28	PB15
SPI_MISO	27	PB14
SPI_SCK	26	PB13
WIFI_CS	25	PB12
WIFI_INT	22	PB11
USB_DISC	21	PB10
MEM_CS	46	PB9
WIFI_EN	45	PB8

U2PORTB_R

D0	43	PB7
D1	42	PB6
D2	41	PB5
D3	40	PB4
D4	39	PB3
BTN	20	PB2
A7	19	PB1
A6	18	PB0

U2PORTB_L

D5	38	PA15
D6	37	PA14
D7	34	PA13
USBP	33	PA12
USBM	32	PA11
LED4	31	PA10
LED3	30	PA9
LED2	29	PA8

U2PORTA_R

PA7	17	A5
PA6	16	A4
PA5	15	A3
PA4	14	A2
PA3	13	RX
PA2	12	TX
PA1	11	A1
PA0	10	A0

U2PORTA_L

PC13	2	OSC32_IN
PC14	3	OSC32_OUT
PC15	4	OSC32_OUT

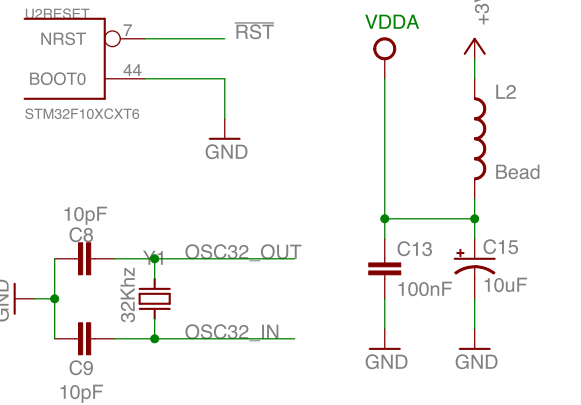
U2PORTD/OSC	5	OSC_IN
PD0/OSC<=	6	OSC_OUT
PD1/OSC=>	6	OSC_OUT

STM32 GPIO

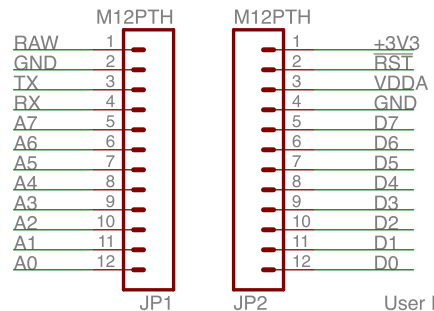
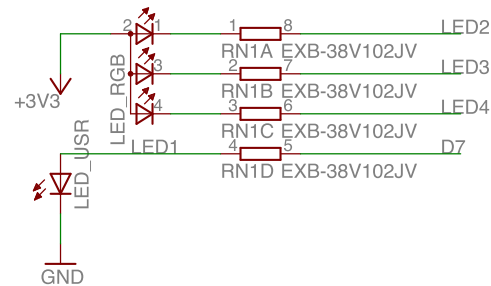
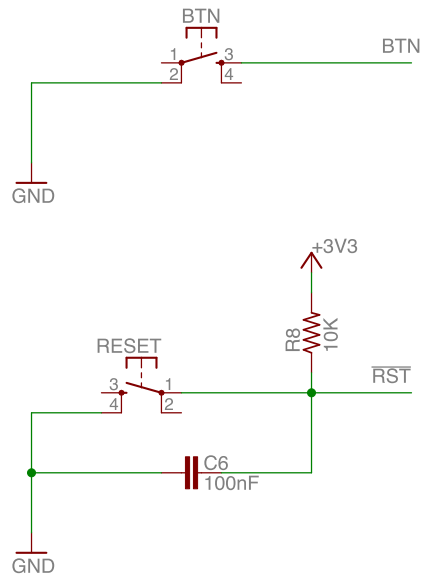
+3V3	24	VDD_1
	36	VDD_2
	48	VDD_3
GND	23	VSS_1
	35	VSS_2
	47	VSS_3

+3V3	9	VDDA
	8	VSSA
	1	VBAT

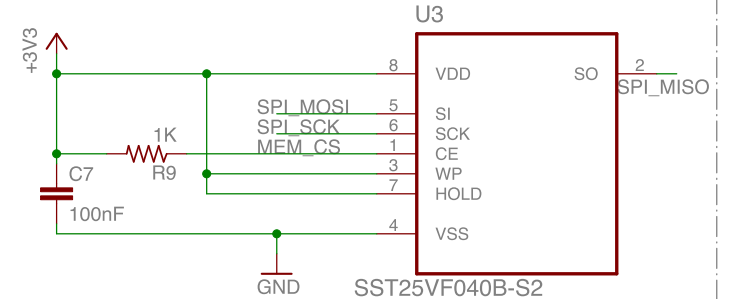
STM32 Power



STM32 Decoupling, Reset, Crystal, & Boot



User IO



External FLASH

Spark Core

TITLE: core

Document Number:

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Date: 8/15/13 10:06 AM

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