Assembly Format

Math 2R:	MATH MOP Rd Rs	Push 2R:	PUSH MOP Rd Rs
Math 3R:	MATH MOP Rd Rs1 Rs2	Push 3R:	PUSH MOP Rd Rs1 Rs2
Math DO:	COPY Rd #	Push DO:	PUSH MOP Rd #
Math NoR:	HALT	Load 2R:	LOAD Rd Rs #
Branch 2R:	BRANCH BOP Rs Rd #	Load 3R:	LOAD Rd Rs1 Rs2
Branch 3R:	BRANCH BOP Rs1 Rd Rs2 #	Load DO:	LOAD Rd #
Branch DO:	JUMP Rd #	Load NoR:	RETURN
Branch NoR:	JUMP #	Store 2R:	STORE Rd Rs #
Call 2R:	CALL BOP Rs Rd #	Store 3R:	STORE Rd Rs1 Rs2
Call 3R:	CALL BOP Rs1 Rd Rs2 #	Store DO:	STORE Rd #
Call DO:	CALL Rd #	Pop 2R:	PEEK Rd Rs #
Call NoR:	CALL #	Pop 3R:	PEEK Rd Rs1 Rs2
		Pop DO:	POP Rd

***Rd, Rs, Rs1, Rs2: These are registers in which you replace ‘d’,’s’,’s1’, and ‘s2’ with a number from 0-31**

***MOP: This is a math operation. The options are: “AND”, “OR”, “XOR”, “NOT”, “SHIFT LEFT”, “SHIFT RIGHT”, “ADD”, “SUB”, and “MULT”**

***BOP: This is a Boolean operation. The options are: “EQ” (equal), “NEQ” (not equal), “LT” (less than), “GE” (greater than or equal), “GT” (greater than), and “LE” (less than or equal)**