

N-Channel Enhancement Mode MOSFET

# **Product Summary**

V <sub>(BR)DSS</sub>	RDS(on)MAX	l <sub>D</sub>
50V	3Ω@10V	0.224
	4Ω@4.5V	0.22A

#### **Feature**

- Advanced trench process technology
- High density cell design for ultra low on-resistance

# **Application**

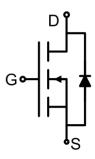
- Load Switch for Portable Devices
- DC/DC Converter
- Direct logic-level interface: TTL/CMOS

## **Package**

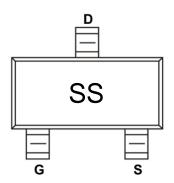


SOT-23

## Circuit diagram



# Marking



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# **BSS138**

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## Absolute maximum ratings (Ta=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DS</sub>	50	V
Gate-Source Voltage	$V_{GS}$	±20	V
Continuous Drain Current	I <sub>D</sub>	0.22	Α
Pulsed Drain Current	I <sub>DM</sub>	0.7	А
Power Dissipation	$P_D$	0.35	W
Junction Temperature	TJ	150	$^{\circ}$
Storage Temperature	T <sub>STG</sub>	-55 ~ +150	$^{\circ}$

Electrical characteristics (T<sub>A</sub>=25 °C, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> =250μA	50			V
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> =48V,V <sub>GS</sub> = 0V			1	μΑ
Gate-body leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 20V, V_{DS} = 0V$			±5	μΑ
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	0.8		1.6	V
Drain accuracy an register coll)	Ь	V <sub>GS</sub> =10V, I <sub>D</sub> =0.5A			3	Ω
Drain-source on-resistance <sup>1)</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =0.2A			4	
Dynamic characteristics <sup>2)</sup>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V,V <sub>GS</sub> =0V,f =1MHz		27		pF
Output Capacitance	Coss			13		
Reverse Transfer Capacitance	Crss			6		
Turn-on delay time	t <sub>d(on)</sub>			5		
Rise time	tr	V <sub>DD</sub> =25V, V <sub>GS</sub> =10V,		18		
Turn-off delay time	t <sub>d(off)</sub>	$I_D$ =0.3A , $R_{GEN}$ =6 $\Omega$		36		nS
Fall time	t <sub>f</sub>			15		
Source-Drain Diode characterist	ics					
Diode Forward Current <sup>1)</sup>	Is				0.22	Α
Diode Forward voltage	V <sub>DS</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =0.5A			1.4	V

Notes: 1) Pulse Test: Pulse Width < 300µs, Duty Cycle ≤2%.

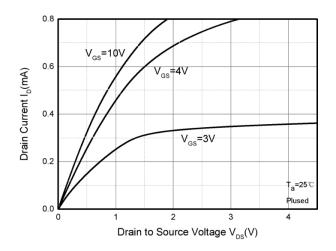
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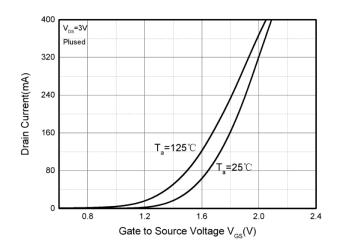
<sup>2)</sup> Guaranteed by design, not subject to production testing.



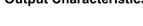
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#### **Test Circuits and Waveforms**

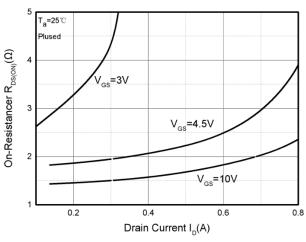


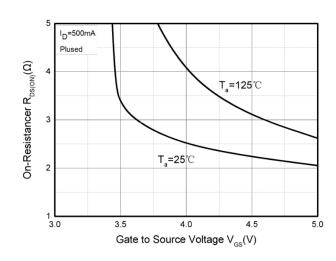


#### **Output Characteristics**



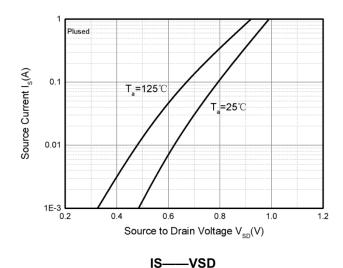
**Transfer Characteristics** 

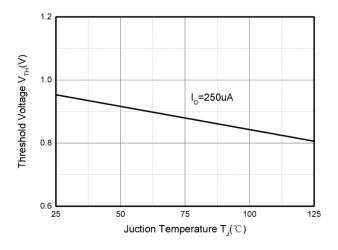




## RDS(ON)——ID

## RDS(ON)——VGS





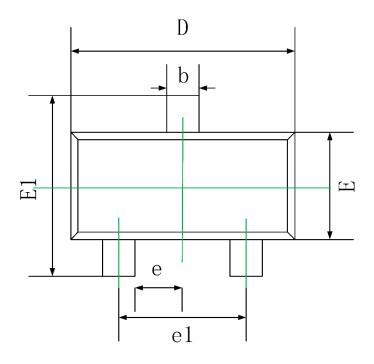
**Threshold Voltage** 

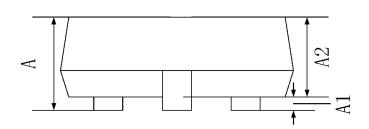
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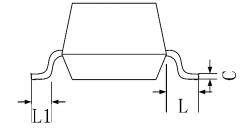


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# **SOT-23 Package Information**







Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
А	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
С	0.080	0.200	0.003	0.008
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
е	0.950	0.950 TYP.		TYP.
e1	1.800	2.000	0.071	0.079
L	0.550	0.550 REF.		REF.
L1	0.300	0.500	0.012	0.020

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