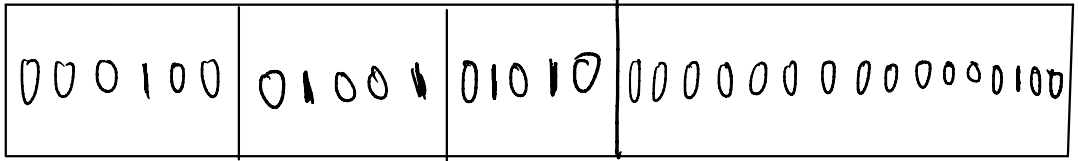
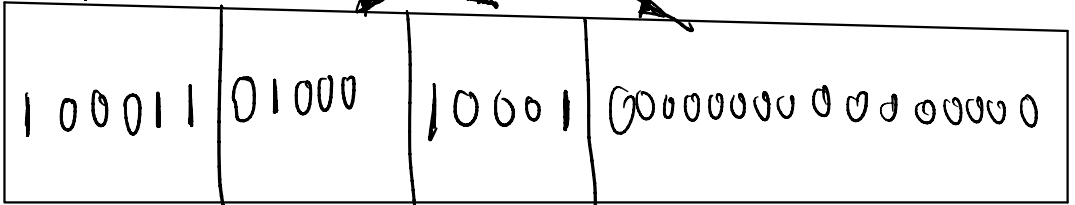


beq \$t1, \$t2, done



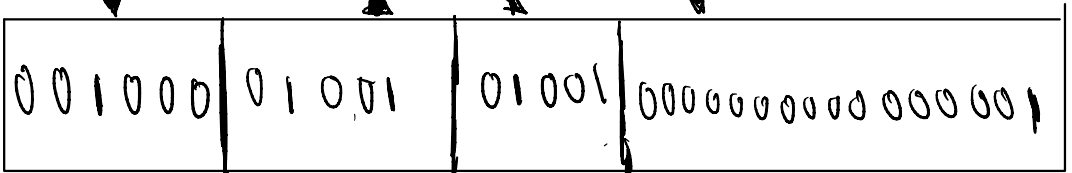
6 bits (4)    5 bits (9)    5 bits (10)    16 bits  
 OP            RS            RT            Offset

lw \$s1, 0(\$t0)



6 bits (5)    5 bits (8)    5 bits (1)    16 bits (0)

addi \$t1, \$t1, 1



8            9            9            1