



T5: Applied Mathematics and Plasma Physics, Los Alamos National Laboratory

Towards ACOPF Feasibility: Exploring the Combined Perspectives of DCOPF and ACPF

Prepared by:

Michael A. Boateng (ECE* PhD Student, Georgia Tech)

LANL Mentors:

Russell Bent (PhD), Sidhant Misra (PhD)

*ECE – Electrical and Computer Engineering

Brief Bio

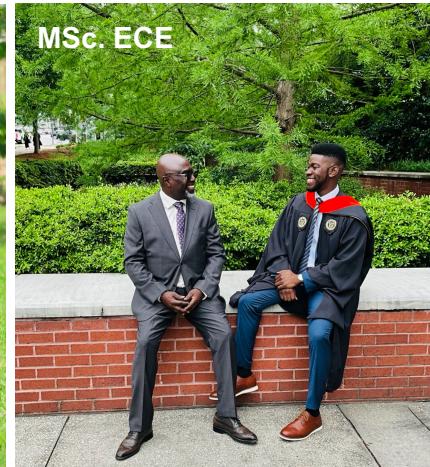
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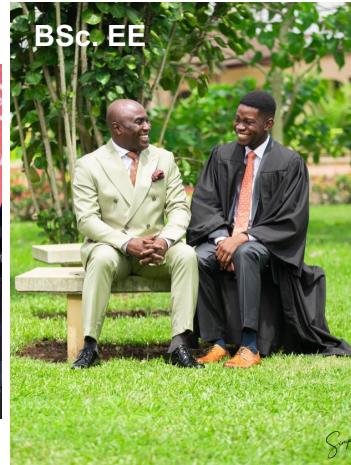
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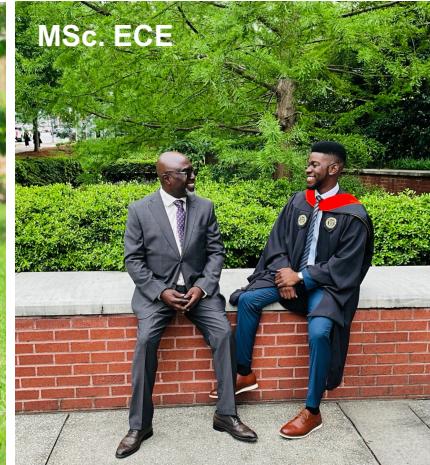
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MSc. ECE



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Project Collaborators

- Los Alamos National Lab
- Georgia Tech
- AI Institute for Advances in Optimization



Fig.1 – Project Collaborators across LANL, GTech, IIT-Roorkee and AI4OPT

Market Clearing in Power Systems

- Balance Power Supply and Demand while setting Market Prices
- Requires a 2-Stage Approach (**DCOPF** [2], [3], **ACPF** [4])

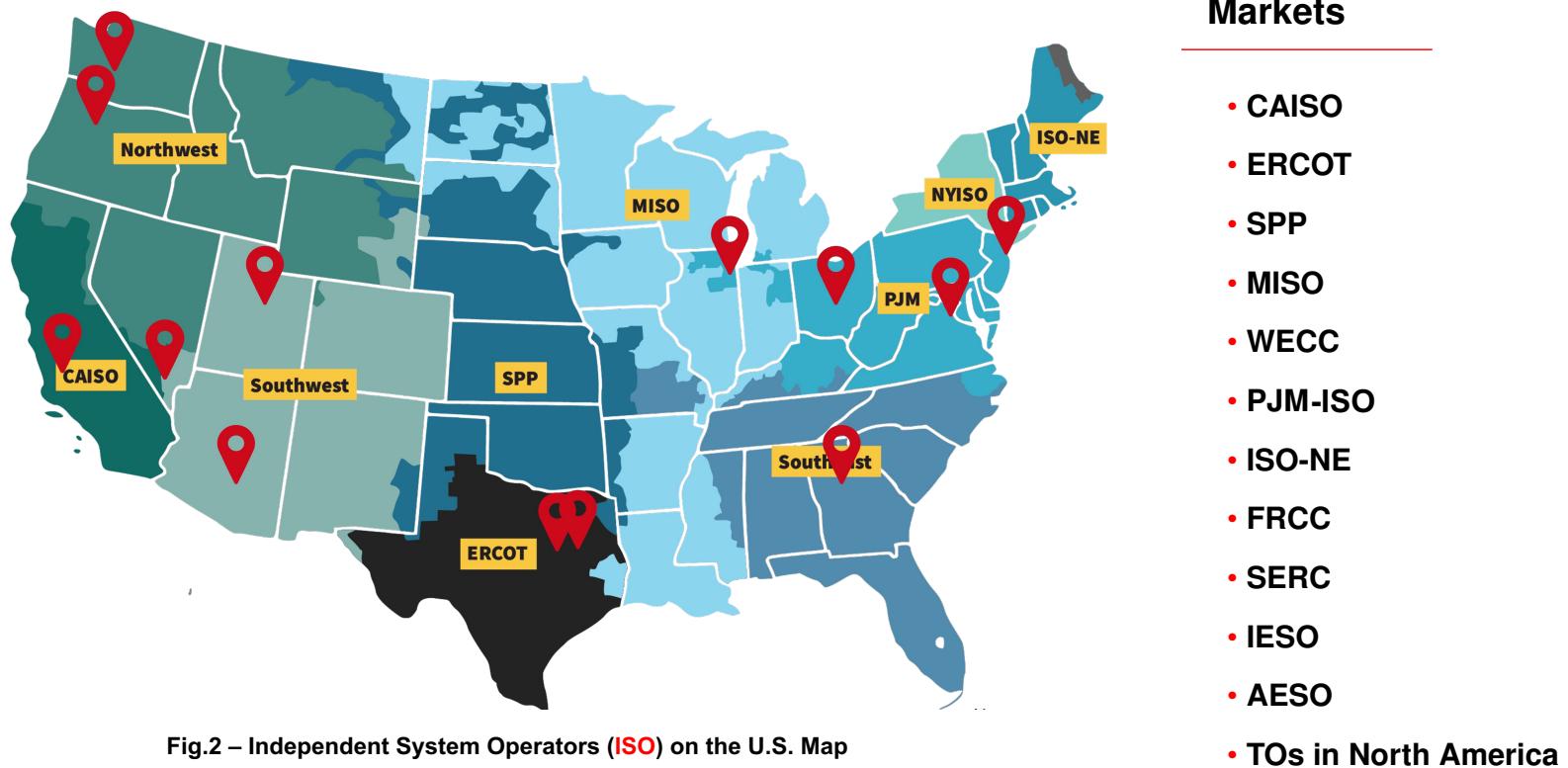


Fig.2 – Independent System Operators (ISO) on the U.S. Map

DCOPF – DC Optimal Power Flow | **ACPF** – AC Power Flow | **ISO** – Independent System Operators

[2] H. Zhao, M. Tanneau, and P. V. Hentenryck, "A linear outer approximation of line losses for DC-based optimal power flow problems," *Electric Power Systems Research*, vol. 212, p. 108272, 2022, presented at the 22nd Power Systems Computation Conference (PSCC).

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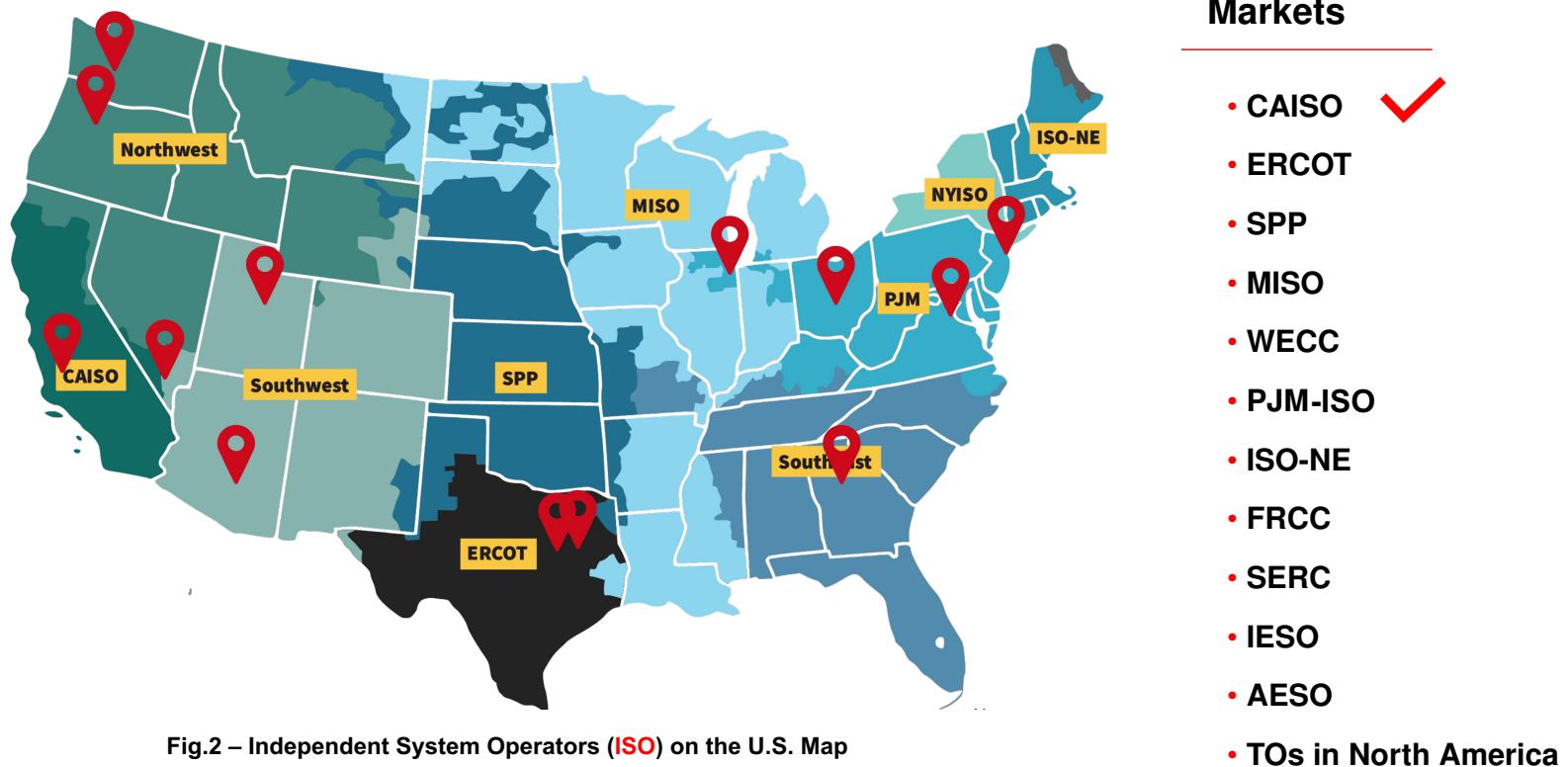


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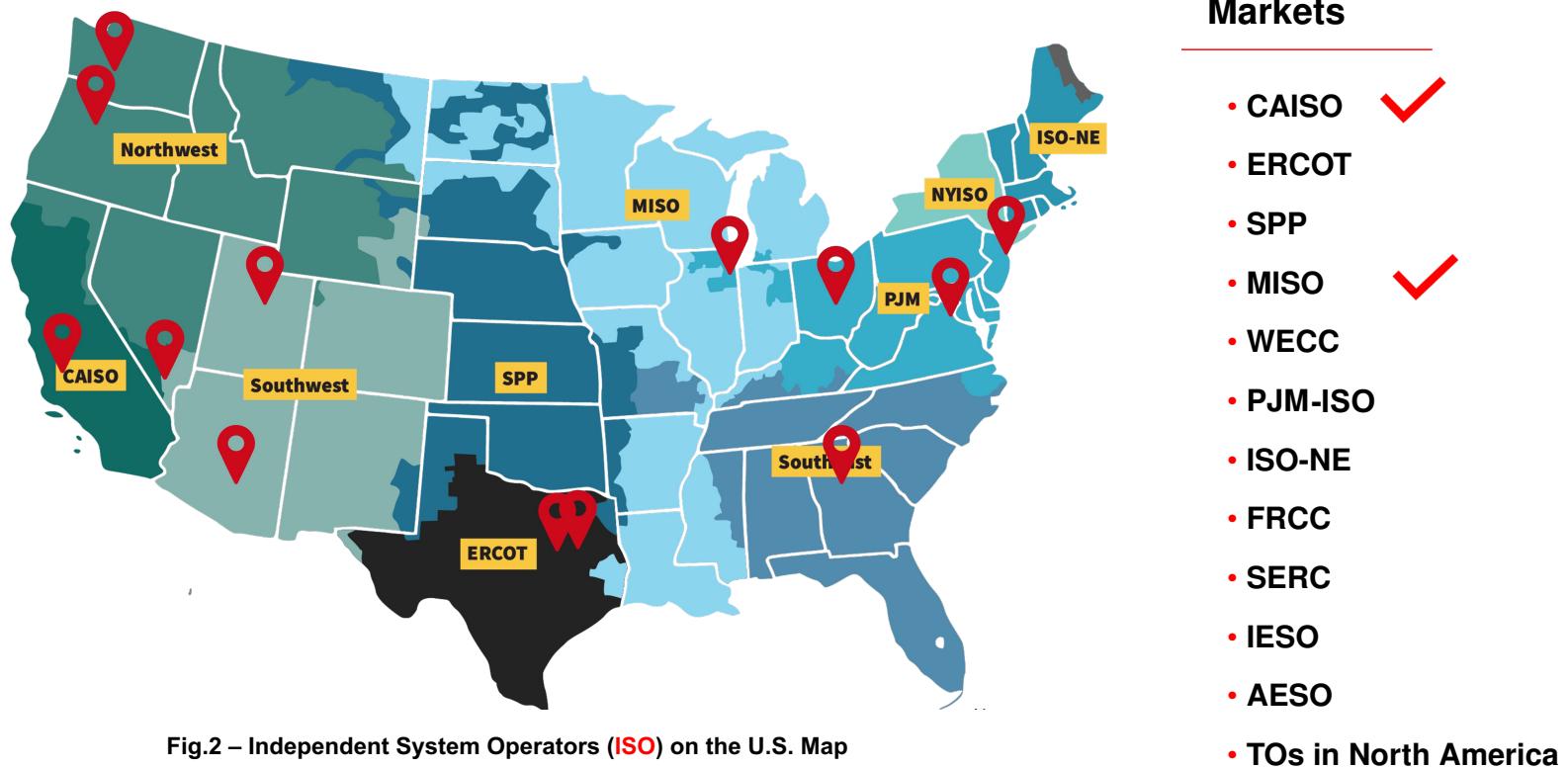


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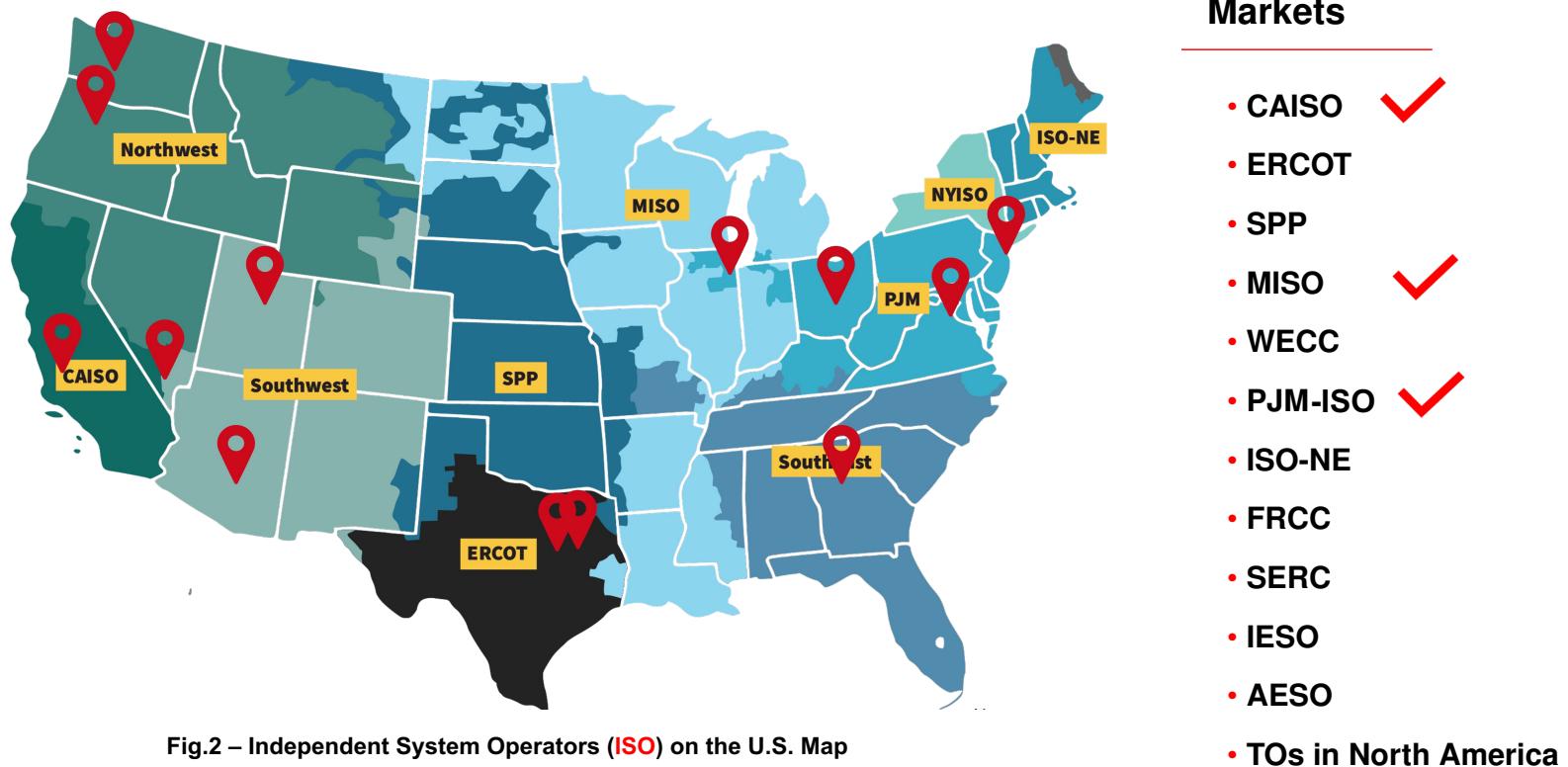


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Project Workflow: The Lossy DCOPF + Principled ACPF

- Why not just use the **ACOPF**?
- Lossy DCOPF construction (**4 types**_[3])
- 💡 Wait! I thought DCOPF is never AC feasible_[5]!

Objective:

$$\min_{\mathbf{P}_g} \mathbf{c}^T \mathbf{P}_g \quad \text{Cost Function} \quad (1)$$

subject to:

AC flow $\mathbf{S}_g - \mathbf{S}_d = \text{diag}(\bar{\mathbf{V}}) \bar{\mathbf{Y}}_{\text{bus}} \bar{\mathbf{V}}^*$ (2)

Line Current $|Y_{i \rightarrow j} \bar{\mathbf{V}}| \leq \mathbf{I}_{\text{line}}^{\max}$ (3)

$$|Y_{j \rightarrow i} \bar{\mathbf{V}}| \leq \mathbf{I}_{\text{line}}^{\max} \quad (4)$$

or Apparent Flow $|\bar{V}_i Y_{i \rightarrow j} \bar{\mathbf{V}}^*| \leq S_{i \rightarrow j}^{\max}$ (5)

$$|\bar{V}_j Y_{j \rightarrow i} \bar{\mathbf{V}}^*| \leq S_{j \rightarrow i}^{\max} \quad (6)$$

Gen. Active Power $\mathbf{P}_g^{\min} \leq \mathbf{P}_g \leq \mathbf{P}_g^{\max}$ (7)

Gen. Reactive Power $\mathbf{Q}_g^{\min} \leq \mathbf{Q}_g \leq \mathbf{Q}_g^{\max}$ (8)

Voltage Magnitude $\mathbf{V}^{\min} \leq \mathbf{V} \leq \mathbf{V}^{\max}$ (9)

Voltage Angle $\Theta_{\min} \leq \Theta \leq \Theta_{\max}$ (10)

Eqn. set 1 – The Full ACOPF (Equations 1-10)

DCOPF – DC Optimal Power Flow | ACPF – AC Power Flow | ACOPF – AC Optimal Power Flow

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subject to:

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- Different inputs to DC and AC engines
- Record violations to observe ACOPF feasibility
- Major engines can be swapped for varying DCOPFs and ACOPFs

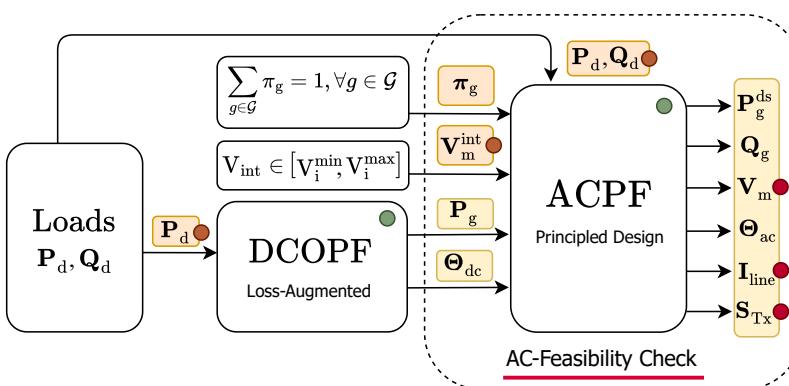


Fig.3 – Project Pipeline (DCOPF->ACPF)

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subject to:

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The Principled AC Power Flow

- 3 bus-types in an ACPF (**Slack**, **Load**, and **Generator**)
- Reserve-based Distributed Slack
- Tolerance-aware Bus-switching

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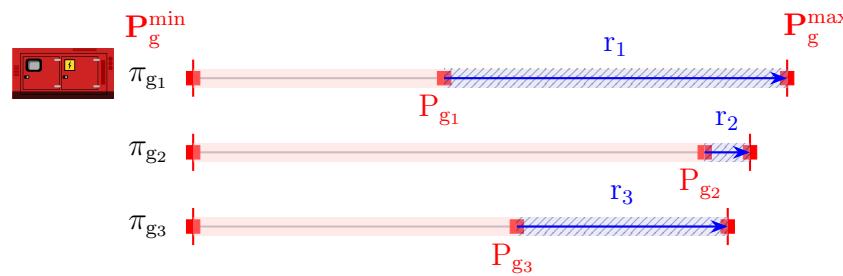


Fig.3 – **Single Slack**

r_i, r_j – Remaining Reserves | π_{g_i} – Participation Factor | \mathcal{S} – Generators used for Loss Sharing | \mathcal{G} – All Generators

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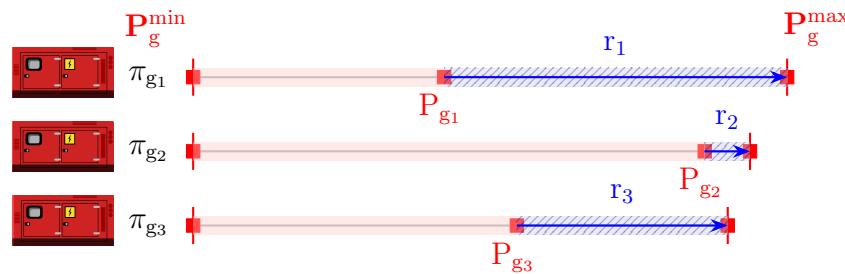


Fig.3 – **Distributed Slack**

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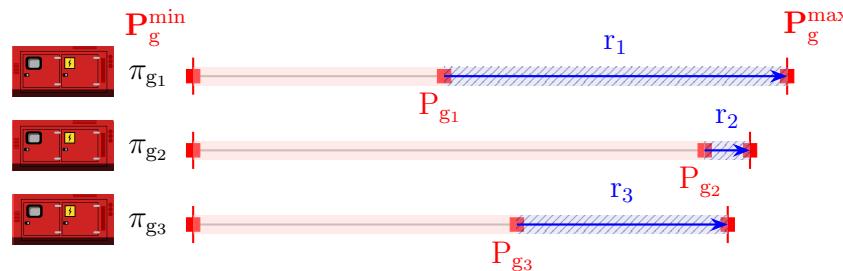


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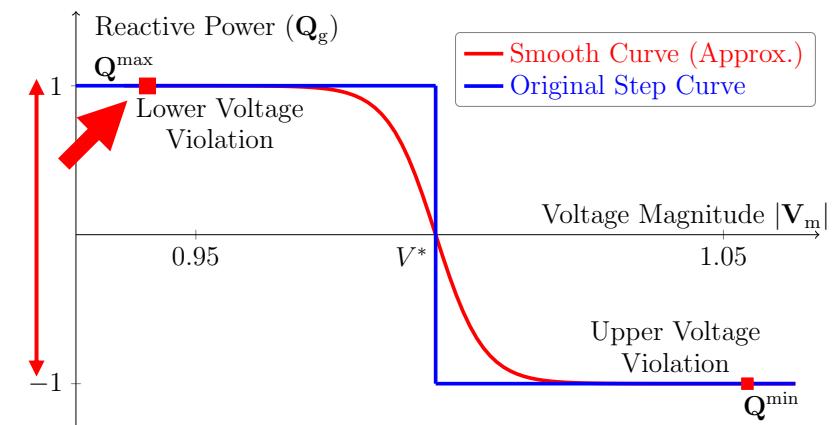


Fig.4 – **Bus-type Switching**

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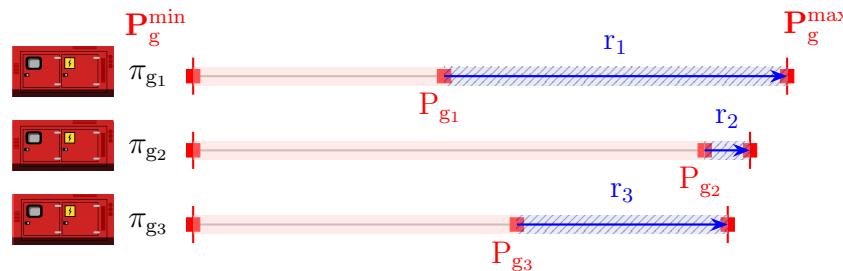


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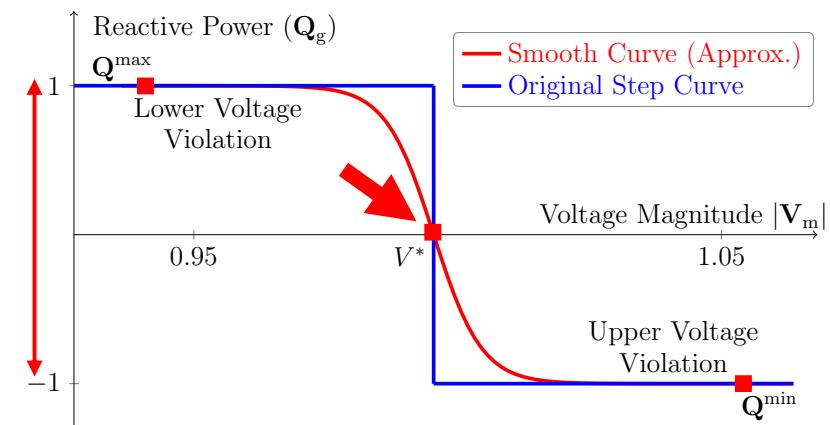


Fig.4 – **Bus-type Switching**

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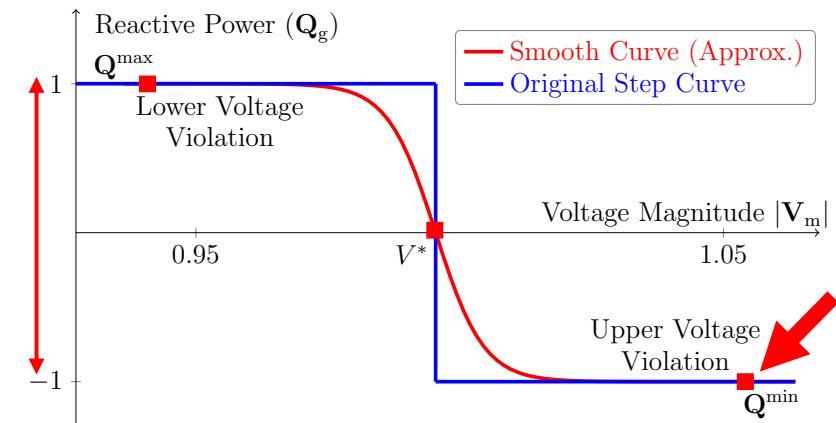


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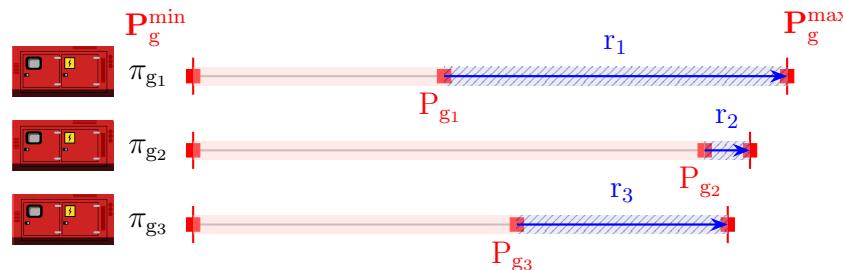


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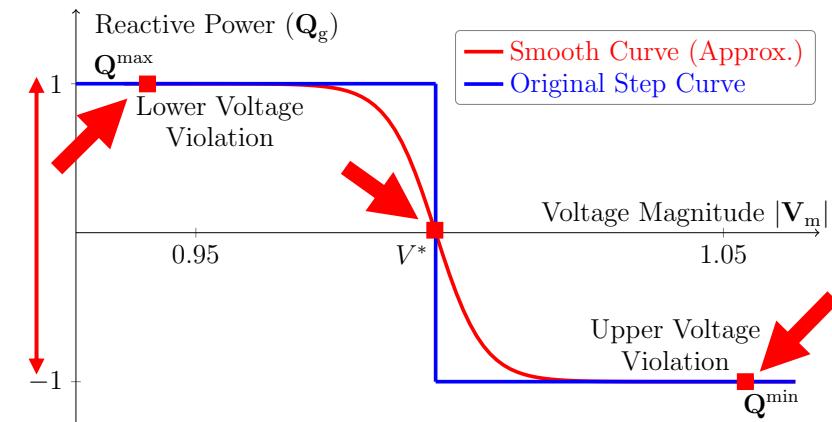


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Data: Tested Grid Cases

- Small, Medium, and Large (24 – 13,695)
- 0, 2, 5, 10, 15% (20,000 samples)
- ACTIVSg, PEGASE, RTE, and IEEE

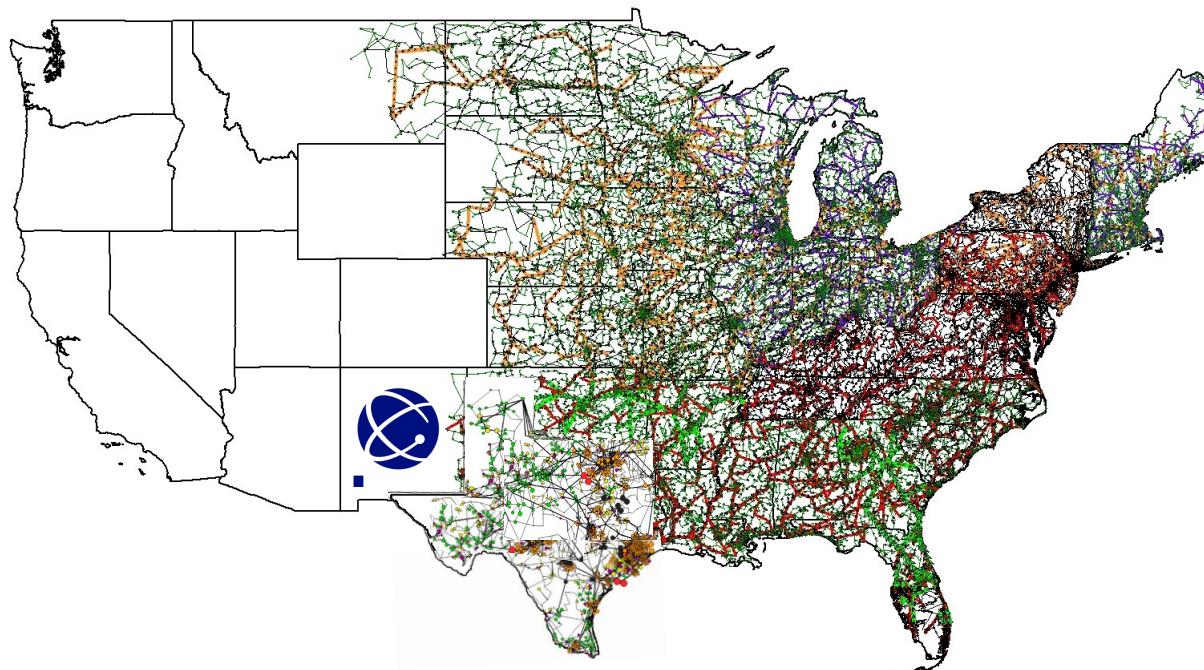


Fig.5 – U.S. Map of some cases used

ACTIVSg – Texas A&M Consortium Test Cases | PEGASE – Pan European Grid | RTE – Réseau de Transport d’Électricité | IEEE - Institute of Electrical and Electronics Engineers

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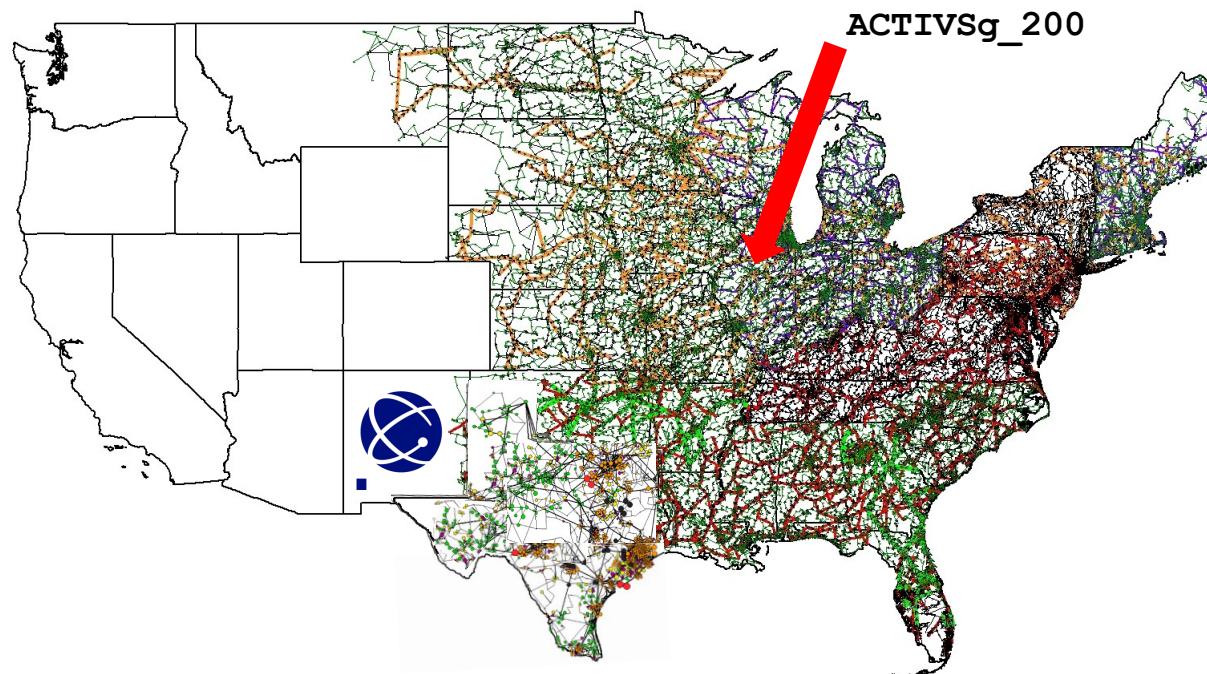


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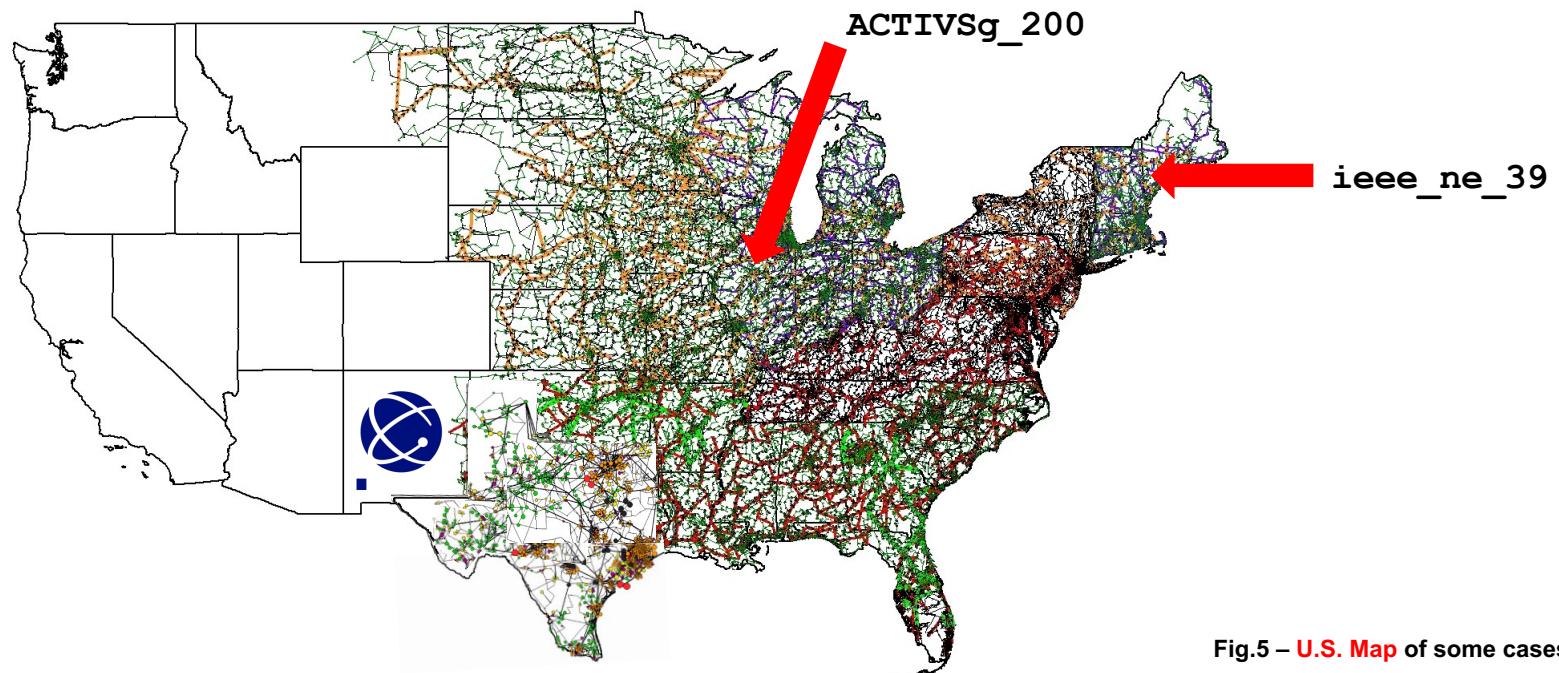


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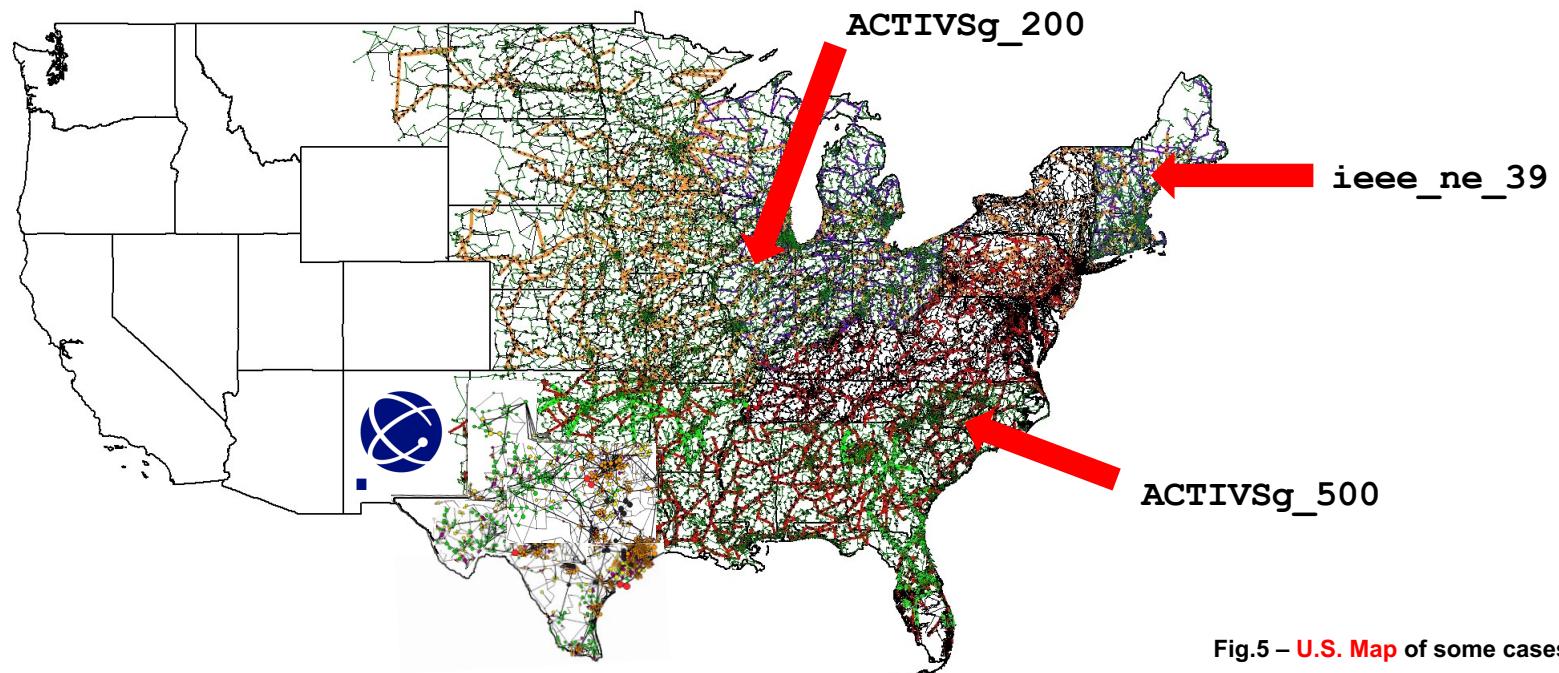


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ACTIVSg – Texas A&M Consortium Test Cases | **PEGASE** – Pan European Grid | **RTE** – Réseau de Transport d’Électricité | **IEEE** - Institute of Electrical and Electronics Engineers

[6] Zimmerman, R. D., Murillo-Sánchez, C. E., & Thomas, R. J. (2011). **MATPOWER**: Steady-State Operations, Planning, and Analysis Tools for Power Systems Research and Education. *IEEE Transactions on Power Systems*, 26(1), 12–19.

[7] Coffrin, C., Bent, R., & Sundar, K. (2018). **PowerModels.jl**: An Open-Source Framework for Exploring Power Flow Formulations. *Proceedings of the 2018 Power Systems Computation Conference (PSCC)*, Dublin, Ireland.

Data: Tested Grid Cases

- Small, Medium, and Large (24 – 13,695)
- 0, 2, 5, 10, 15% (20,000 samples)
- ACTIVSg, PEGASE, RTE, and IEEE

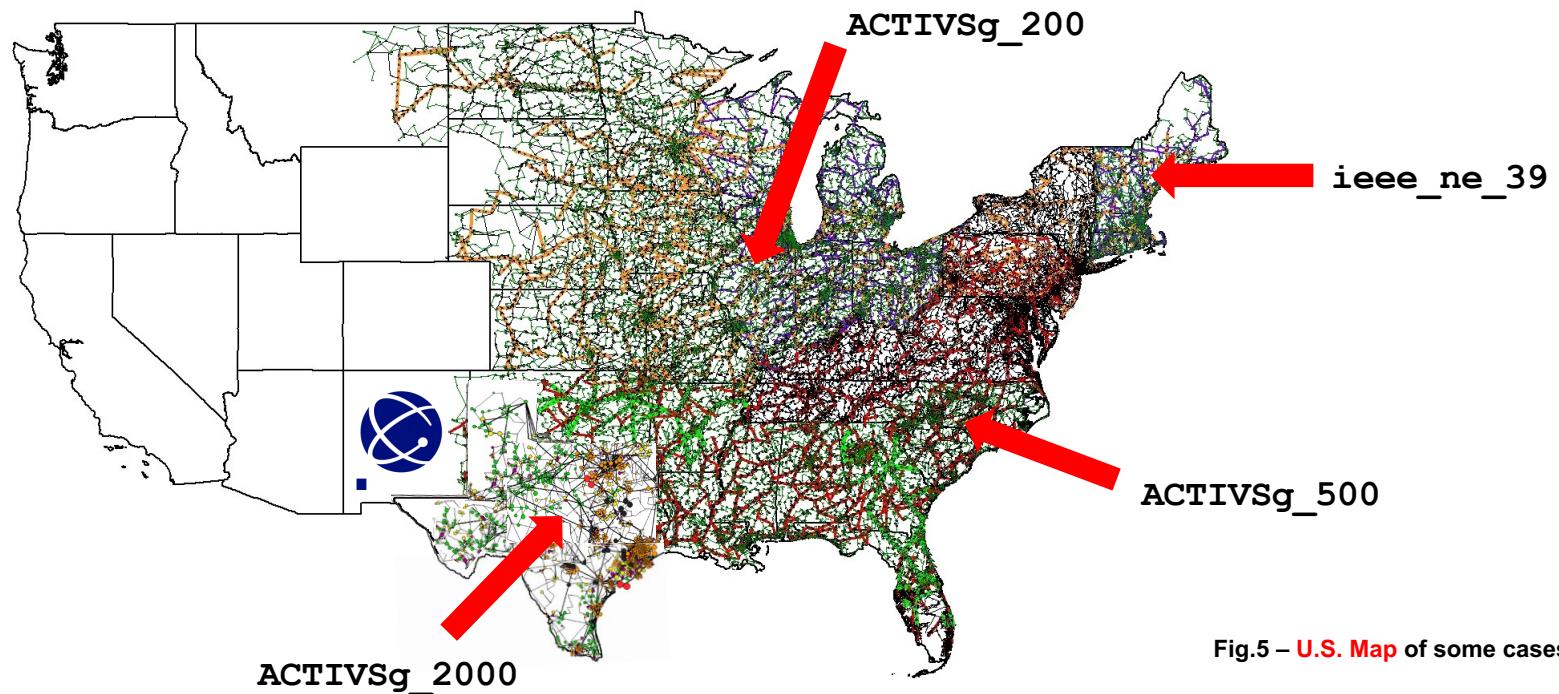


Fig.5 – U.S. Map of some cases used

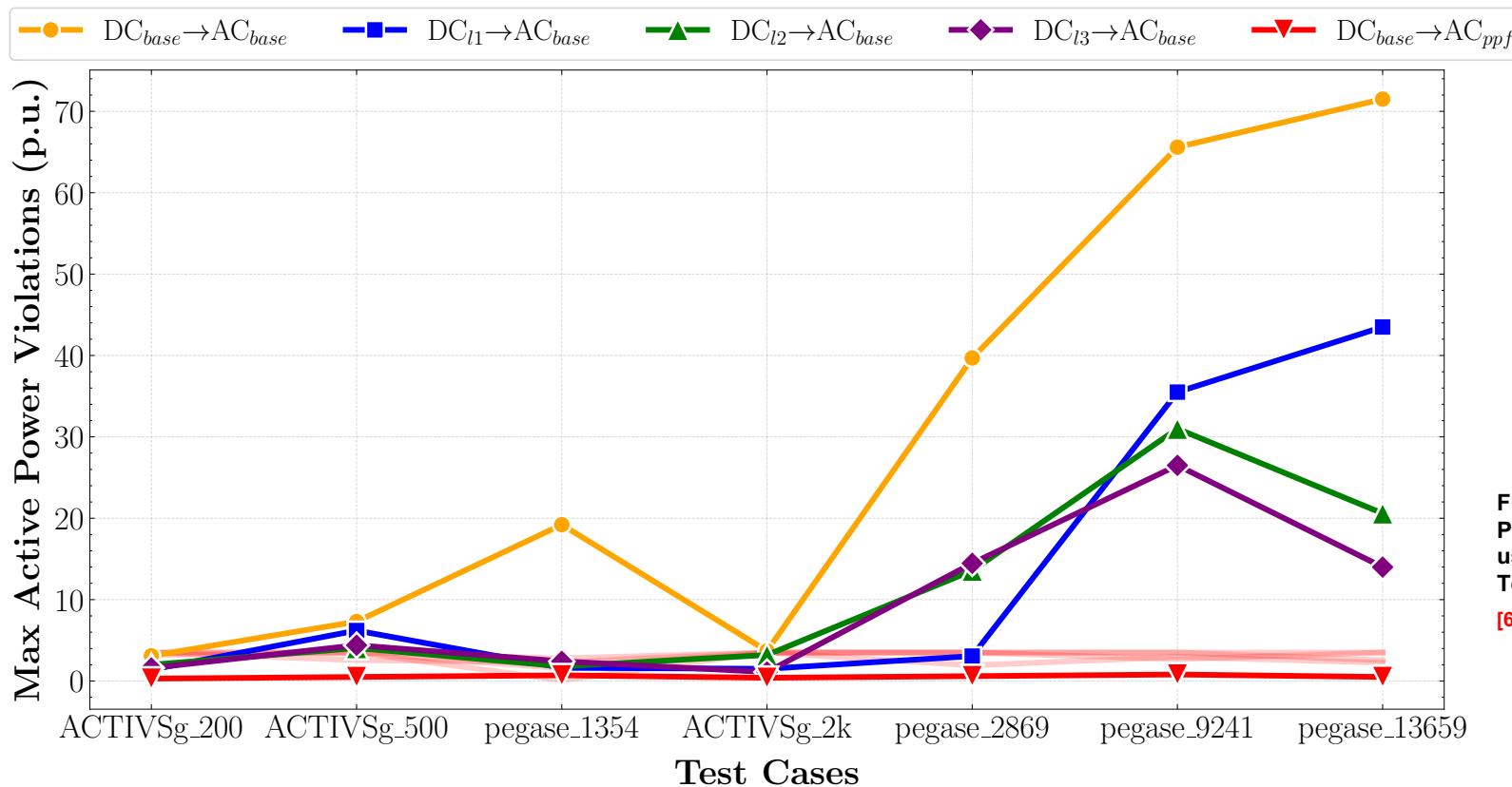
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1

Results: Max Power Violations (Base PF vs Principled PF)



- DC_{base} → Principled AC (with sensitives from 0, 2, 5, 10%)
- Single Slack Cases have larger violations

[6] Zimmerman, R. D., Murillo-Sánchez, C. E., & Thomas, R. J. (2011). MATPOWER: Steady-State Operations, Planning, and Analysis Tools for Power Systems Research and Education. IEEE Transactions on Power Systems, 26(1), 12–19.

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1

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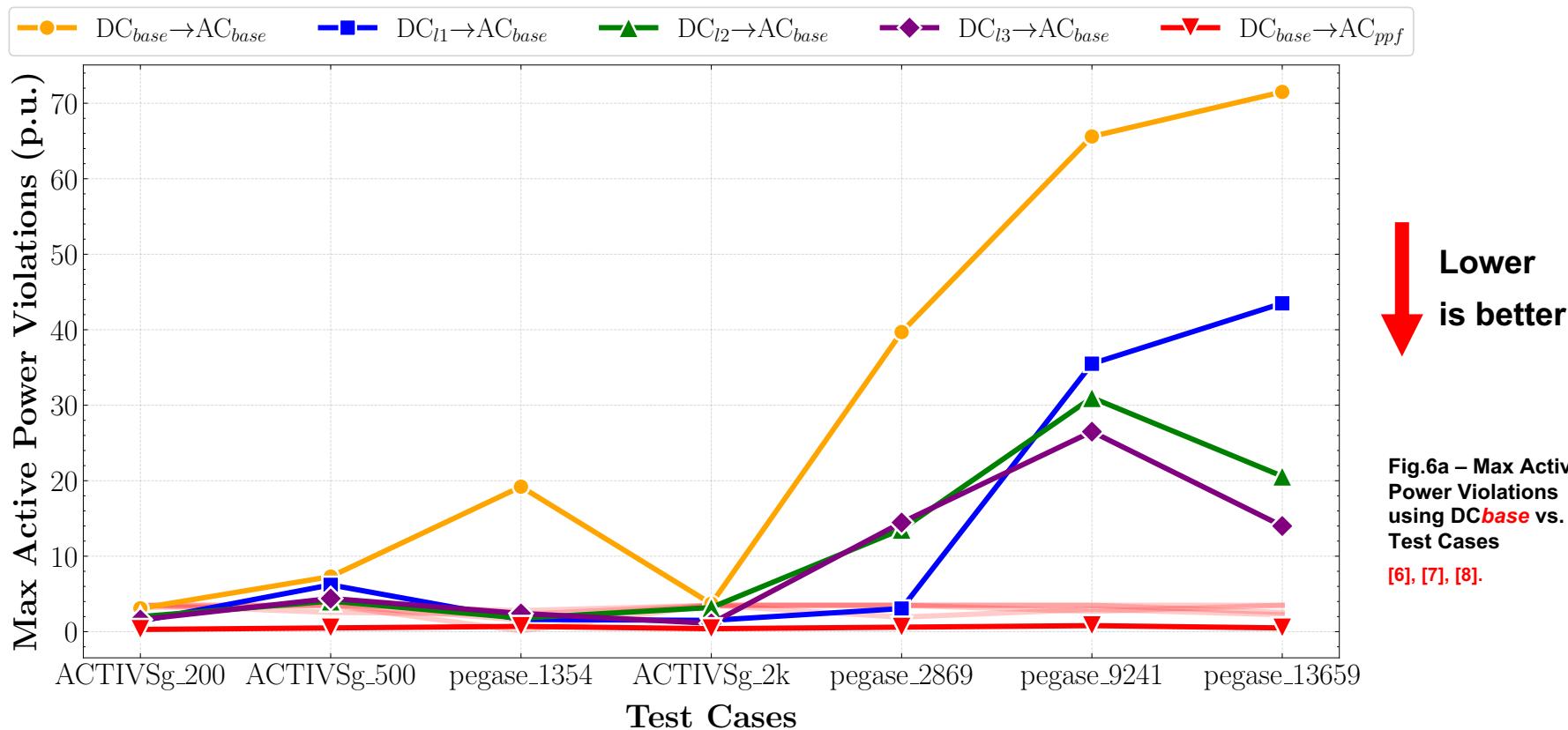


Fig.6a – Max Active Power Violations using DC_{base} vs. Test Cases
[6], [7], [8].

- DC_{base} → Principled AC (with sensitivities from 0, 2, 5, 10%)
- Single Slack Cases have larger violations

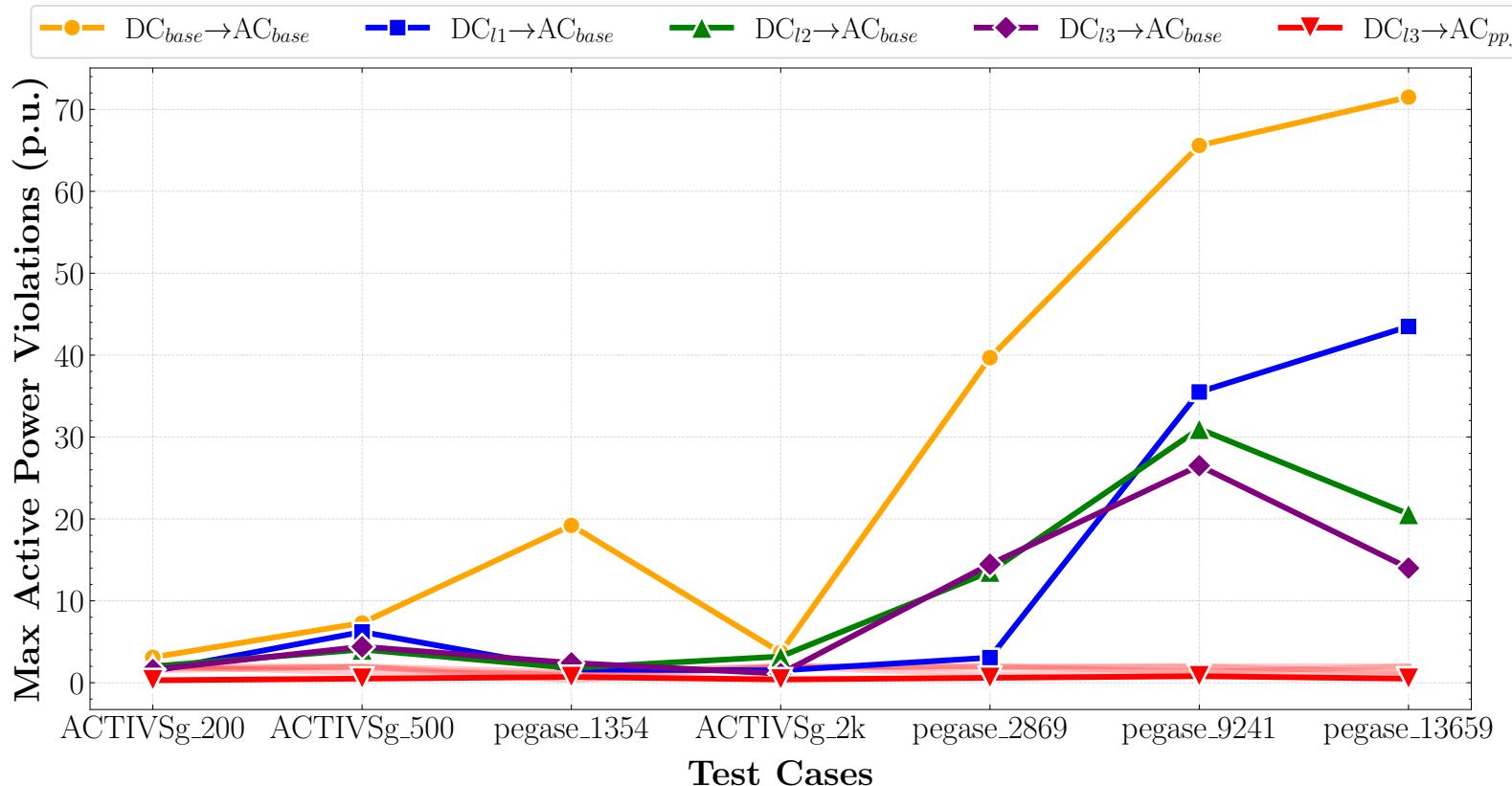
[6] Zimmerman, R. D., Murillo-Sánchez, C. E., & Thomas, R. J. (2011). MATPOWER: Steady-State Operations, Planning, and Analysis Tools for Power Systems Research and Education. IEEE Transactions on Power Systems, 26(1), 12–19.

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²

Results: Max Power Violations (Base PF vs Principled PF)



- DC_{lossy3} -> Principled AC (with sensitivities from 0, 2, 5, 10%)
- Best DC and Best AC = Fewer violations

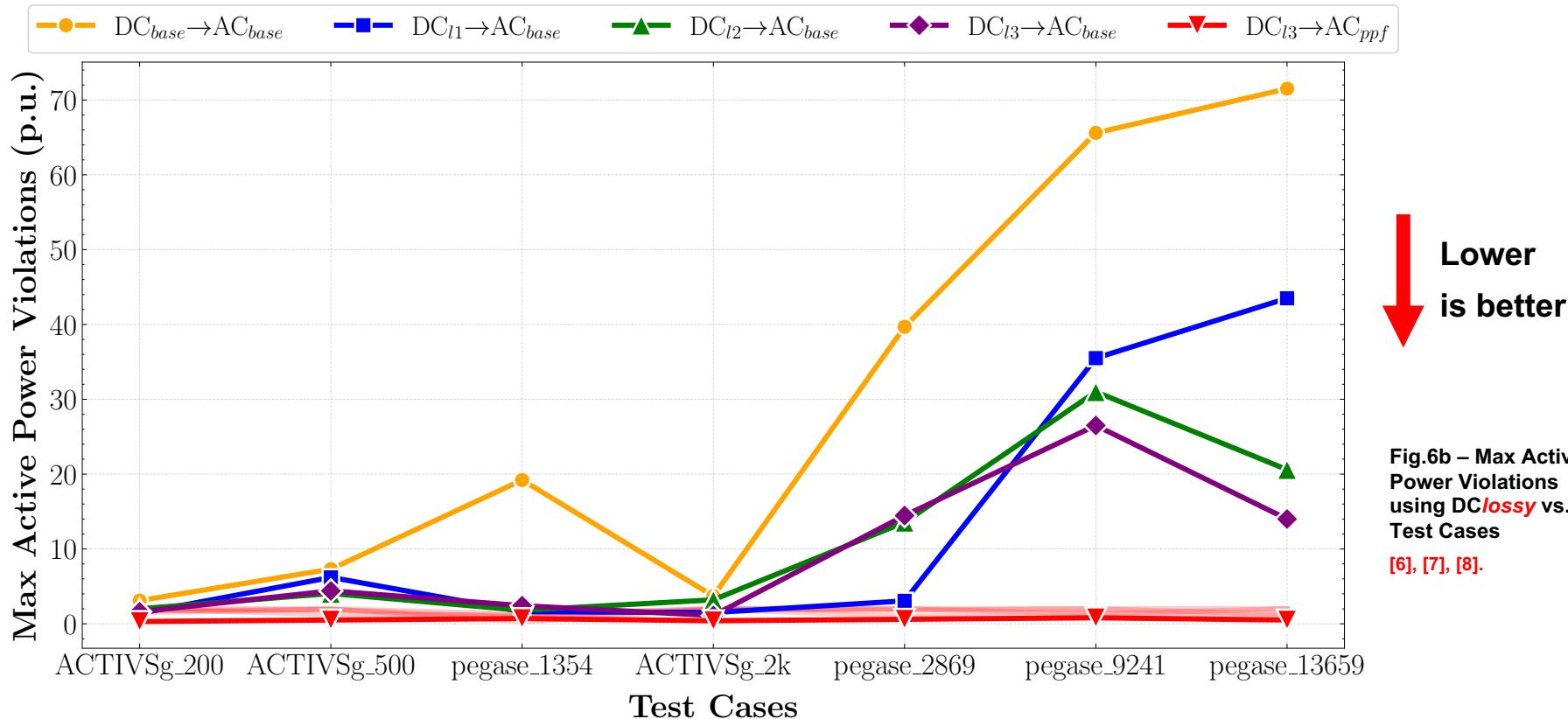
[6] Zimmerman, R. D., Murillo-Sánchez, C. E., & Thomas, R. J. (2011). MATPOWER: Steady-State Operations, Planning, and Analysis Tools for Power Systems Research and Education. IEEE Transactions on Power Systems, 26(1), 12–19.

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3

Results: Max Line Violations (Base PF vs Principled PF)

Test Cases

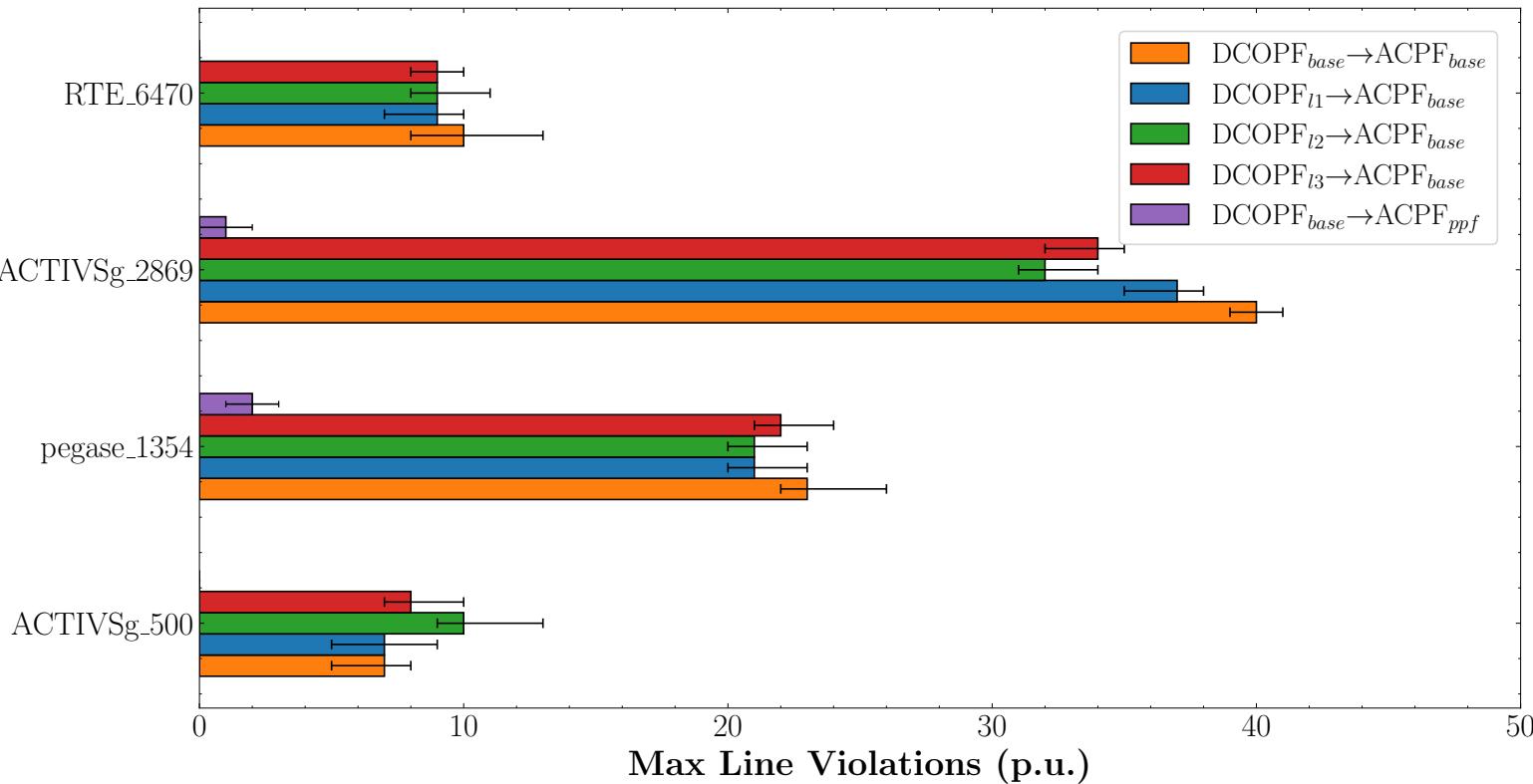


Fig.7 – Max Violations on Lines for different DC-AC pipelines.

[6], [7], [8].

- DC_{base} → Principled AC (with sensitivities from 0, 2%)
- Line violations reduce drastically (even when using DC_{base})

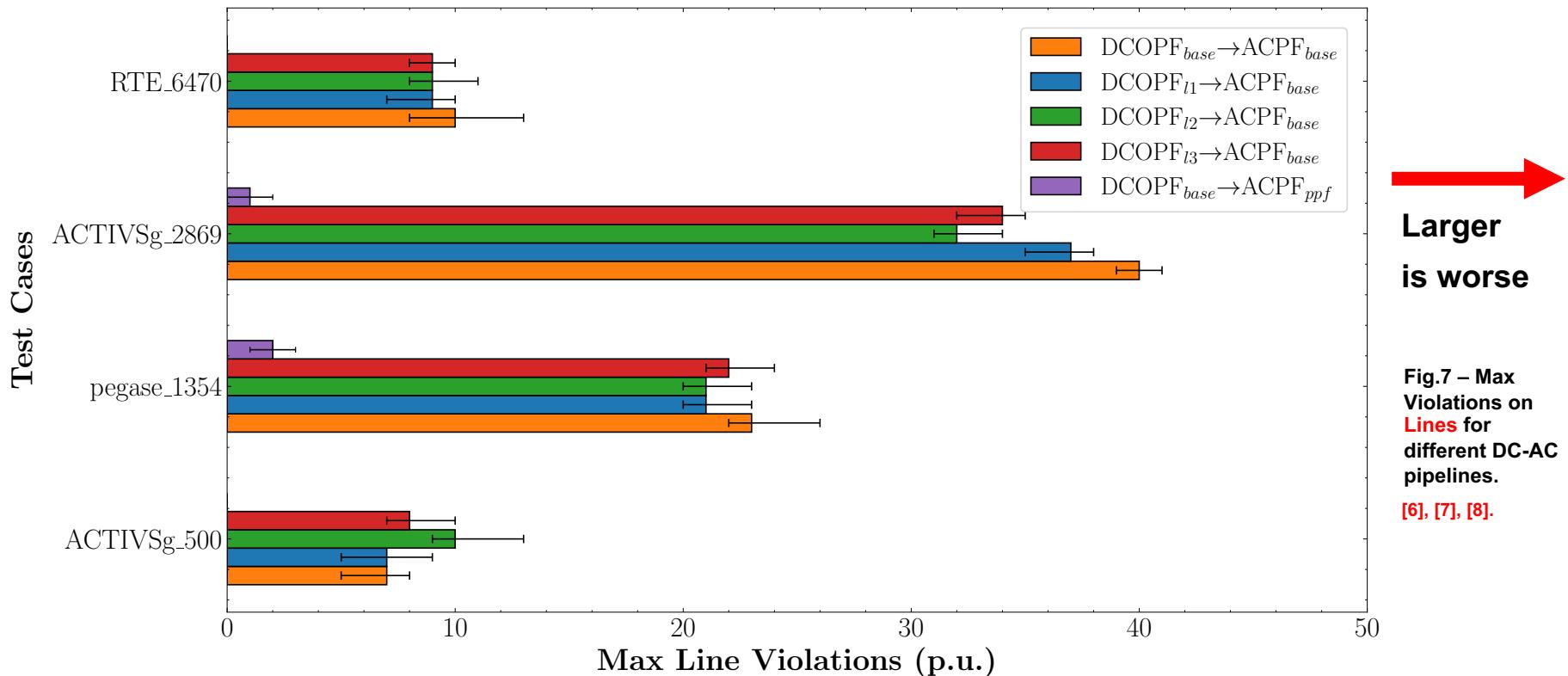
[6] Zimmerman, R. D., Murillo-Sánchez, C. E., & Thomas, R. J. (2011). MATPOWER: Steady-State Operations, Planning, and Analysis Tools for Power Systems Research and Education. IEEE Transactions on Power Systems, 26(1), 12–19.

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[8] Los Alamos National Laboratory. Darwin High Performance Computing Cluster. Los Alamos National Laboratory (LANL), Los Alamos, NM, USA.

Results: Voltage Kernel Density Estimate (Principled PF)

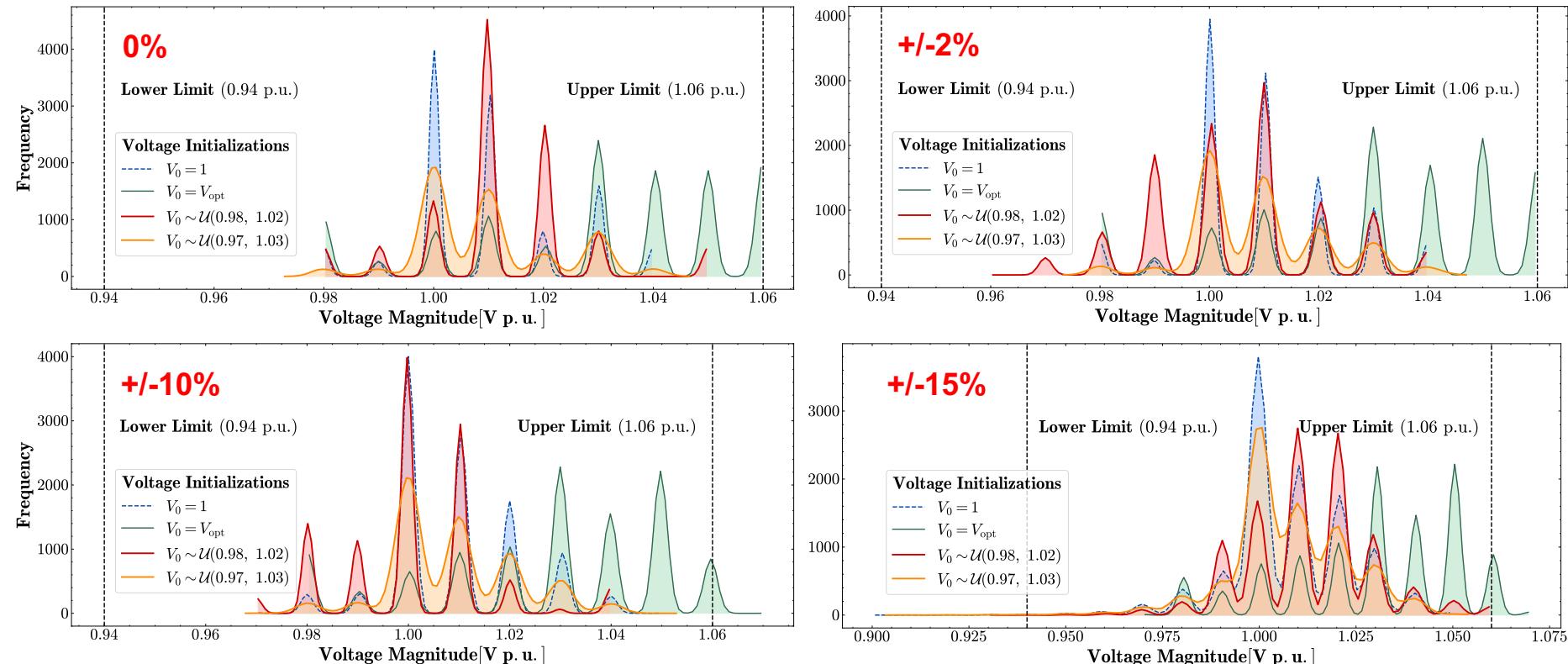


Fig.8 – Voltage Magnitude violations for DC-AC [6], [7], [8].

- Voltage Distribution (with sensitivities from 0, 2, 10, 15%) for case IEEE 39
- For reference with a Single slack ACPF violations in 0% **5 -10%** with Lossy DC

[6] Zimmerman, R. D., Murillo-Sánchez, C. E., & Thomas, R. J. (2011). **MATPOWER**: Steady-State Operations, Planning, and Analysis Tools for Power Systems Research and Education. *IEEE Transactions on Power Systems*, 26(1), 12–19.

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Main Takeaways:

- Market Clearing requires a more Principled ACPF for Feasibility Check.
- DCOPF is never AC Feasible
- Significant Improvements in Violations for Voltage Magnitude (10%), Active Power Limits (60-70%), and Line Limits (x10), when using a Distributed Slack vs. a Single Slack bus.

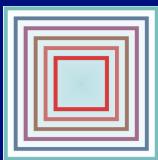
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Next Steps:



Two Abstracts Submitted and Accepted at:
Power Systems Computational Conference 2026

Final Submission is on October 1st

[9] PSCC (2026) Logo

[9] Logo of the 2026 Power Systems Computation Conference (PSCC). Source: <https://pscc2026.cy>.

Thank You!

- Mentors, Advisors, Colleagues, Family, Friends, Leaders, Administrative and Cleaning Staff.



Towards ACOPF Feasibility: Exploring the Combined Perspectives of DCOPF and ACPF Constraints

Michael A. Boateng ^{1, 2, 3} Russell Bent ³ Sidhant Misra ³ Parikshit Pareek ⁴ Daniel Molzahn ^{1, 2} Pascal Van Hentenryck ^{1, 2}
¹Georgia Institute of Technology ²AI Institute for Advances in Optimization ³Los Alamos National Lab ⁴Indian Institute of Technology Roorkee



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Extras (QnA)

1

Algorithm 1a: Bus Switching Logic with Tolerances

Input: Initial state $s \in \{\text{PV}, \text{PQ}^{\min}, \text{PQ}^{\max}\}$
 Generator reactive limits: $Q_{g_i}^{\min}, Q_{g_i}^{\max}$
 Generator setpoint: Q_{g_i}
 Voltage magnitude V_i and setpoint V_i^{sp}
 Tolerances: $\varepsilon_q, \varepsilon_v > 0$

Output: Updated bus type s_i for all $i \in \mathcal{N}$

```

1   foreach  $i \in \mathcal{N}$  do
2     if  $s_i = \text{PV}$  then
3       if  $Q_{g_i} < Q_{g_i}^{\min} - \varepsilon_q$  then
4          $Q_{g_i} \leftarrow Q_{g_i}^{\min};$ 
5          $s_i \leftarrow \text{PQ}^{\min};$ 
6       else if  $Q_{g_i} > Q_{g_i}^{\max} + \varepsilon_q$  then
7          $Q_{g_i} \leftarrow Q_{g_i}^{\max};$ 
8          $s_i \leftarrow \text{PQ}^{\max};$ 
9       else
10         $s_i$ , remains unchanged;
11     else if  $s_i = \text{PQ}^{\min}$  and  $V_i > V_i^{\text{sp}} + \varepsilon_v$  then
12        $s_i \leftarrow \text{PV};$ 
13     else if  $s_i = \text{PQ}^{\max}$  and  $V_i < V_i^{\text{sp}} - \varepsilon_v$  then
14        $s_i \leftarrow \text{PV};$ 
15     else
16        $s_i$ , remains unchanged;
```

2

Algorithm 1b: Smooth Bus Switching Formulation

Input: Voltage target: V_i^* for all $i \in \mathcal{N}$
 Reactive limits: Q^{\min}, Q^{\max}
 Complex voltage: V_i for all $i \in \mathcal{N}$
 Complex current: I_i for all $i \in \mathcal{N}$

Output: Slack variables $\{V_i^+, V_i^-, x_i\}$ for all $i \in \mathcal{N}$

```

1   foreach  $i \in \mathcal{N}$  do
2     Slack constraint:
3        $V_{d,i}^2 + V_{q,i}^2 \leftarrow (V_i^*)^2 - V_i^- + V_i^+$ 
4     Reactive mismatch:
5        $x_i \leftarrow Q_i^{\max} - f_Q(V_{d,i}, V_{q,i})$ 
6     Complementarity:
7        $V_i^- \cdot x_i = 0, \quad V_i^+ \cdot (Q_i^{\max} - Q_i^{\min} - x_i) = 0$ 
8     Feasibility:
9        $Q_i^{\max} - Q_i^{\min} - x_i \geq 0$ 
10    Non-negativity:
11       $V_i^+, V_i^-, x_i \geq 0$ 
12    Reactive injection:
13       $f_Q \leftarrow V_{d,i} \cdot I_{q,i} - V_{q,i} \cdot I_{d,i}$ 
14    Voltage decomposition:
15       $V_{d,i} \leftarrow \Re(V_i), \quad V_{q,i} \leftarrow \Im(V_i)$ 
16    Current computation:
17       $I_i \leftarrow G_{ii} \cdot V_i + \sum_{j \in \mathcal{N}} Y_{ij} \cdot V_j$ 
return  $\{V_i^+, V_i^-, x_i\}$ 
```

4

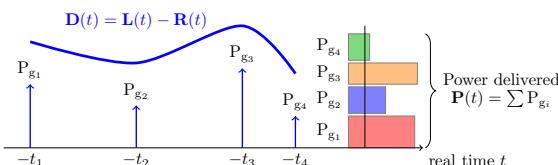


Fig. 1: Each vertical arrow marks a staged DCOPF dispatch at time t_i , based on the net demand forecast $D(t) = L(t) - R(t)$, with $L(t)$ as load and $R(t)$ as expected renewables. As forecasts improve, updated set-points P_{g_i} are determined and retained. The stacked bars at real-time t reveals the cumulative power injection, $P(t) = \sum P_{g_i}$, which is checked for AC-feasibility under full network constraints.

5

Algorithm 2: Reserve-Based Slack Distribution

Input: Generator setpoints: $P_{g_i}^{\text{set}}$
 Max limits: $P_{g_i}^{\max}$
 Total loss: ℓ_{total}

Output: Updated generation targets P_{g_i} for all $i \in \mathcal{G}$

```

1   foreach  $i \in \mathcal{G}$  do
2      $r_i \leftarrow \max(P_{g_i}^{\max} - P_{g_i}^{\text{set}}, 0);$ 
3      $\mathcal{S} \leftarrow \{i \in \mathcal{G} \mid r_i > 0\};$ 
4      $R \leftarrow \sum_{j \in \mathcal{S}} r_j;$ 
5      $C \leftarrow \sum_{j \in \mathcal{S}} P_{g_j}^{\max};$ 
6     foreach  $i \in \mathcal{S}$  do
7       if  $R > 0$  then
8          $\pi_i \leftarrow \frac{r_i}{R};$ 
9       else
10         $\pi_i \leftarrow \frac{P_{g_i}^{\max}}{C};$ 
11      $P_{g_i} \leftarrow P_{g_i}^{\text{set}} + \pi_i \cdot |\ell_{\text{total}}|;$ 
12 return  $\{P_{g_i}\}_{i \in \mathcal{G}}$ 
```

3

TABLE I: OVERVIEW OF DCOPF VARIANTS

Label	Formulation	Description
DC	Angle $[P_g, \Theta_g^{\text{dc}}]$	Vanilla DC (Lossless)
DC $_{\ell_1}$	PTDF $[P_g]$	Linearized Line Loss Factor
DC $_{\ell_2}$	Angle $[P_g, \Theta_g^{\text{dc}}]$	Quadratic Convex
DC $_{\ell_3}$	PTDF $[P_g]$	Line Loss Outer Approximation
DC $_{\ell_4}$	Angle $[P_g, \Theta_g^{\text{dc}}]$	ML-based DC Parameter Optimizer

TABLE II: SUMMARY OF TEST SYSTEMS FROM MATPOWER

Source	System	Buses	Branches	Generators
IEEE	ieee_rts_24	24	38	33
	ieee_30	30	41	6
	ieee_ne_39	39	46	10
	ieee_118	118	186	54
ACTIVSg	ACTIVSg_200	200	245	49
	ACTIVSg_500	500	597	90
	ACTIVSg_2000	2,000	3,206	544
RTE	rte_6468	6,468	9,000	1,296
	rte_6470	6,470	9,005	1,331
PEGASE	pegase_89	89	210	12
	pegase_1354	1,354	1,991	260
	pegase_2869	2,869	4,582	510
	pegase_9241	9,241	16,049	1,445
	pegase_13659	13,659	20,467	4,092

6

$$\text{MAE} = \frac{1}{G} \|\mathbf{p}_g^{* \text{DC} \rightarrow \text{AC}} - \mathbf{p}_g^{* \text{AC}}\|_1$$

7

VI. SENSITIVITY ANALYSIS

In the sensitivity analysis, Gaussian multiplicative perturbations were applied to the active power demands P_d at each load bus. For $P_d > 0$, the demand was scaled by a factor $\xi \sim \mathcal{N}(1.0, \sigma^2)$, where σ denotes the perturbation level (e.g., $\sigma = 0.10$ for approximately $\pm 10\%$ variation). The corresponding reactive power Q_d was recomputed using a randomly sampled power factor from the range $[0.95, 1.0]$, while preserving the original sign of Q_d to distinguish inductive and capacitive behavior. Loads with $P_d \leq 0$ were excluded from perturbation to maintain physical consistency.