

LI-IMX274-MIPI-CS Data Sheet

Key Features

- Sony Diagonal 7.20 mm (Type 1/2.5) CMOS Image Sensor IMX274
- Active pixels: 3864H x 2196V
- Pixel size: 1.62 um x 1.62 um
- Color sensor
- Interface: MIPI output
- Support CS lens
- Module Size: 38mmx38mm
- Weight: 54 g
- Part#: LI-IMX274-MIPI-CS

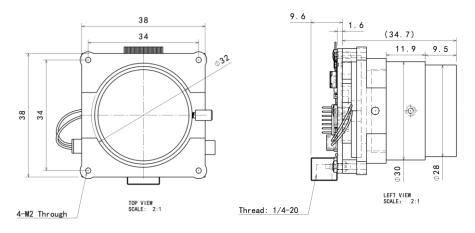


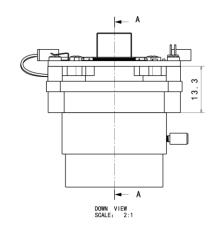


Lens Spec

- Model: ES0522F.IR
- Focal length: 5.0 mm
- Aperture, F/#: 2.2
- Built in 650nm IR cut filter
- FOV (D/H/V): 87 °/76 °/43 °
- TV Distortion: < -8%
- Mount Type: CS

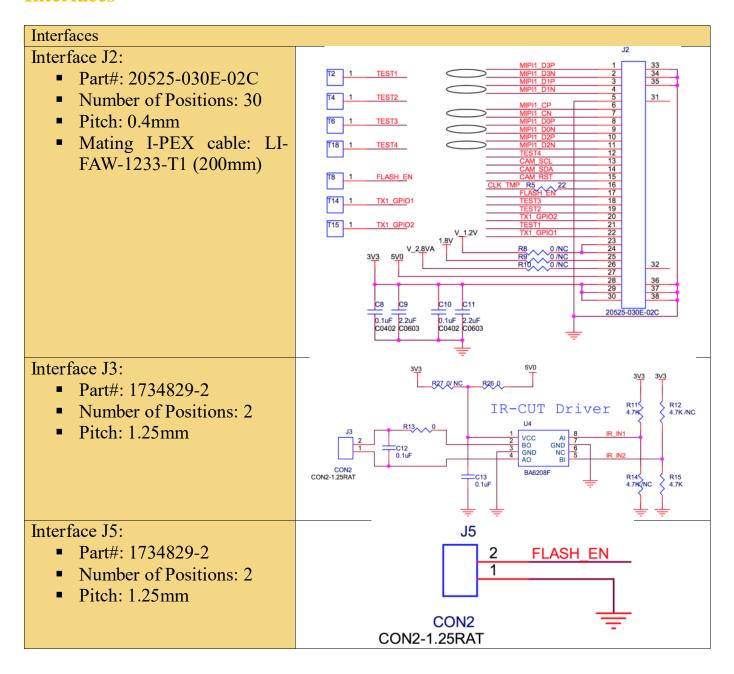
Dimensions







Interfaces



IMX274 Sensor Spec

Absolute Maximum Ratings

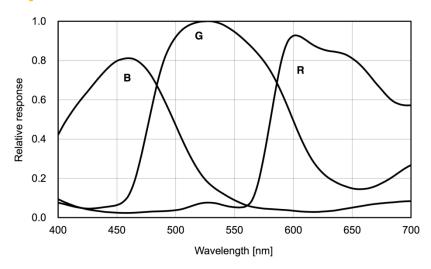
Item	Symbol	Ratings	Unit
Supply voltage (Analog)	V _{ADD} *1	-0.3 to +3.3	V
Supply voltage (Digital 1)	V _{DDD1} *2	-0.5 to +2.0	V
Supply voltage (Digital 2)	V _{DDD2} *3	-0.5 to +3.3	V
Input voltage (Digital)	Vı	-0.3 to $V_{DDD2} + 0.3$	V
Output voltage (Digital)	Vo	-0.3 to $V_{DDD2} + 0.3$	V
Guaranteed operating temperature	T _{OPR}	-30 to +75	°C
Storage guarantee temperature	T _{STG}	-30 to +80	°C
Performance guarantee temperature	T _{SPEC}	-10 to +60	°C

Recommended Operating Conditions

Item	Symbol	Rating	Unit
Supply voltage (Analog)	V _{ADD} *1	2.8 ± 0.1	V
Supply voltage (Digital 1)	V _{DDD1} *2	1.2 ± 0.1	V
Supply voltage (Digital 2)	V _{DDD2} *3	1.8 ± 0.1	V
Input voltage (Digital)	Vı	-0.1 to V _{DDD2} + 0.1	V

^{*1} V_{ADD}: V_{DD}SUB, V_{DD}HCM, V_{DD}HPX, V_{DD}HDA, V_{DD}HCP (2.8 V power supply)

Spectral Sensitivity Characteristics





^{*2} V_{DDD1}: V_{DD}LCN, V_{DD}LSC1 to 2, V_{DD}LPA, V_{DD}LPL1, V_{DD}LPL2 to 3, V_{DD}LIF (1.2 V power supply)

^{*3} V_{DDD2}: V_{DD}MIO, V_{DD}MIF (1.8 V power supply)

DC Characteristics

Current Consumption and Gain Vatriable Range

 $(V_{ADD}$ = 2.9 V, V_{DDD1} = 1.3 V, V_{DDD2} = 1.9 V, Tj = 60 °C, Reference Gain (0 dB) All pixel scan mode (MODE0), 29.97 frame/s)

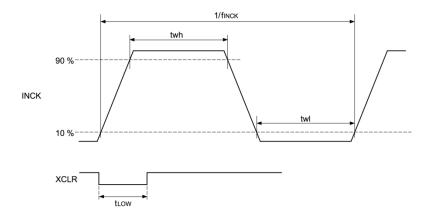
Item	Symbol	Min.	Тур.	Max	Unit	Remarks
Current consumption (Analog)	I_{ADD}	_	_	62	mA	
Current consumption (Digital 1)	I _{DDD1}	_	_	190	mA	
Current consumption (Digital 2)	I_{DDD2}	_	_	1	mA	
Standby current (Analog)	I _{ADDSTB}	_	_	35	μA	In the dark
Standby current (Digital 1)	I _{DDD1STB}	_	_	13	mA	In the dark
Standby current (Digital 2)	I _{DDD2STB}	_	_	20	μA	In the dark
PGA gain variable range	PGAG	0	_	27	dB	

Supply Voltage and I/O Voltage

Ite	em	Pins	Symbol	Min.	Тур.	Max.	Unit
	Analog	V _{DD} SUB, V _{DD} HCM, V _{DD} HPX, V _{DD} HDA, V _{DD} HCP	V _{ADD}	2.70	2.80	2.90	V
Supply voltage	Digital 1	V _{DD} LCN, V _{DD} LSC1 to 2, V _{DD} LPL1, V _{DD} LPA, V _{DD} LPL2 to 3, V _{DD} LIF	V _{DDD1}	1.10	1.20	1.30	V
	Digital 2	$V_{DD}MIO, V_{DD}MIF$	V_{DDD2}	1.70	1.80	1.90	V
		SDA,	V _{IH1}	$0.7 \times V_{DDD2}$	_	1.9	V
Digital in	put	SCL	V _{IL1}	-0.3		$0.3 \times V_{DDD2}$	V
voltage		XCLR,	V _{IH2}	0.65 × V _{DDD2}	_	$V_{DDD2} + 0.3$	V
		INCK	V _{IL2}	-0.3	_	0.35 × V _{DDD2}	V
Digital ou voltage	ıtput	XHS, XVS	V _{HVOUT}	_	V_{DDD2}	_	V

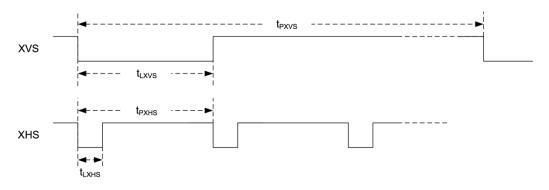
AC Characteristics

INCK, XCLR



Item	Symbol	Min.	Тур.	Max.	Unit
INCK clock frequency	f _{INCK}	6	_	27	MHz
INCK Low level pulse width	twl	5	_	_	ns
INCK High level pulse width	twh	5	_	_	ns
Clock duty	_	40	50	60	%
XCLR Low level pulse width	t _{LOW}	100	_	_	ns

XHS, XVS (Output)



Item	Symbol	Min.	Тур.	Max.	Unit	Remarks
XHS Low level pulse width	t _{LXHS}		222		ns	16 clk@72MHz
XHS pulse period	t _{PXHS}		HMAX*1		clk@72MHz	
XVS Low level pulse width	t _{LXVS}		t _{PXHS}		clk@72MHz	
XVS pulse period	t _{PXVS}		HMAX ^{*1} × VMAX ^{*2}		clk@72MHz	

^{*1} The value set as HMAX (address 30F6h, bit [7:0] and address 30F7h, bit [7:0])

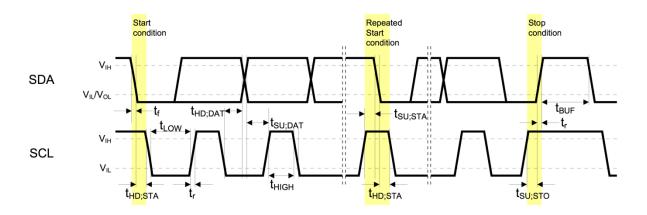


1130 Cadillac Ct., Milpitas, CA 95035,USA Phone: +1-408-263-0988 Fax: +1-408-217-1960

Email:sales@leopardimaging.com Website: www.leopardimaging.com www.leopard-security.com

^{*2} The value set as VMAX (address 30F8h, bit [7:0], address 30F9h, bit [7:0] and address 30FAh, bit [3:0]).

I²C Communication



I²C Specification

Item	Symbol	Min.	Тур.	Max.	Unit	Remarks
Low level input voltage	V _{IL}	-0.3	_	0.3 × V _{DDD2}	V	
High level input voltage	V _{IH}	$0.7 \times V_{DDD2}$	_	1.9	V	
Low level output voltage	V _{OL}	0	_	$0.2 \times V_{DDD2}$	V	V _{DDD2} < 2 V, Sink 3 mA
Output fall time	tof	_	_	250	ns	Load 10 pF to 400 pF, 0.7 × V _{DDD2} to 0.3 × V _{DDD2}
Input current (SCL, SDA, XCLR, INCK)	li	-10	_	10	μA	$0.1 \times V_{DDD2}$ to $0.9 \times V_{DDD2}$
Input capacitance of SCL / SDA	Ci	_	_	10	pF	

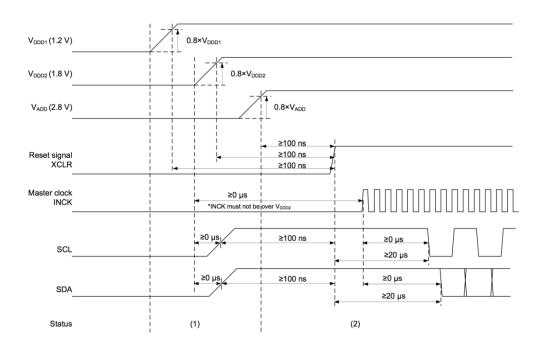
I²C AC Characteristics

Item	Symbol	Min.	Тур.	Max.	Unit
SCL clock frequency	f _{SCL}	0	_	400	kHz
Hold time (Start Condition)	t _{HD;STA}	0.6	_	_	μs
Low period of the SCL clock	t _{LOW}	1.3	_	_	μs
High period of the SCL clock	t _{HIGH}	0.6	_	_	μs
Set-up time (Repeated Start Condition)	t _{SU;STA}	0.6	_	_	μs
Data hold time	t _{HD;DAT}	0	_	0.9	μs
Data set-up time	t _{SU;DAT}	100	_	_	ns
Rise time of both SDA and SCL signals	t _r	_	_	300	ns
Fall time of both SDA and SCL signals	t _f	_	_	300	ns
Set-up time (Stop Condition)	t _{SU;STO}	0.6	_	_	μs
Bus free time between a STOP and START Condition	t _{BUF}	1.3	_	_	μs



Website: www.leopardimaging.com www.leopard-security.com

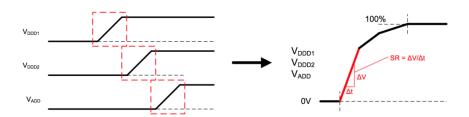
Power-on Sequence



Period name	Remarks
(1) Power stabilization	All input signals are set to Low level.
period	There are no constraints of the power-on sequence with V_{ADD} , V_{DDD1} , and V_{DDD2} .
(2) Register communication period for standby cancel	Wait 100 ns after the last power supply in V_{ADD} , V_{DDD1} and V_{DDD2} . Then set XCLR to "H" and start the standby cancel sequence.

Slew Rate Limitation of Power-on Sequence

Conform to the slew rate limitation shown below when power supply change 0 V to each voltage (0 % to 100 %) in power-on sequence.



Item	Symbol	Power supply	Min.	Max.	Unit	Remarks
		V _{DDD1} (1.2 V)		25	mV/us	
Slew rate	SR	V _{DDD2} (1.8 V)		25	mV/us	
		V _{ADD} (2.8 V)	_	25	mV/us	

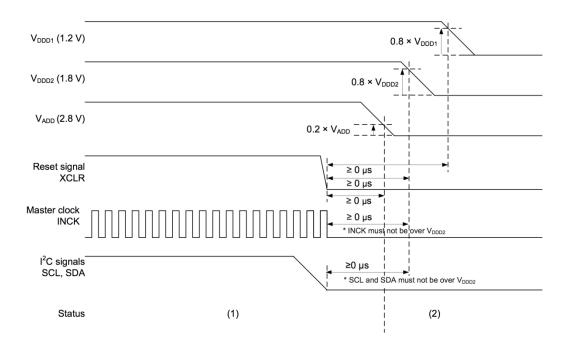


1130 Cadillac Ct., Milpitas, CA 95035,USA Phone: +1-408-263-0988

Fax: +1-408-217-1960

Email:sales@leopardimaging.com Website: www.leopardimaging.com www.leopard-security.com

Power-off Sequence



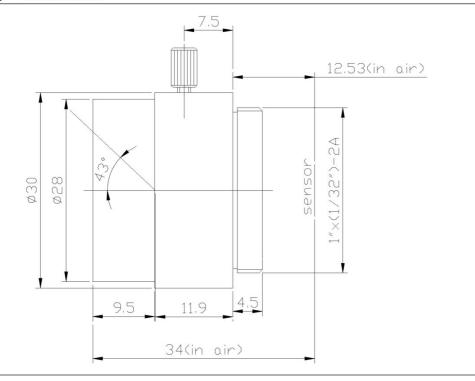
Period name	Remarks	
(1) Pixel output period	Pixel signal output period	
(2) Power-off period	Turn the power supplies off after all input signals are set to "Low" level except SCL and SDA. Set SCL and SDA to "Low" level at the same time with turning off the power supply of V_{DDD2} . There are no constraints of the power-off sequence with V_{ADD} , V_{DDD1} , and V_{DDD2} .	

10 Megapixel IR Series





有效焦距	E.F.L.		5 mm
相对孔径	F No.		F2.2
接口类型	Mount Type		CS
水平视角	H.FOV	1/1.8"	72°
像面尺寸	Image Format (Max)		1/1.8"
光圈类型	Iris Type		Fixed
近 摄 距	M.O.D		0.3m
畸 变	TV Distortion	1/1.8"	-8%
+0 +0 +-+	Occaliant	变倍 Zoom	1
操控方式	Control Type	聚焦 Focus	Manual
工作温度	Operating Temperature		-30℃ ~ 70℃
镜头重量	Weight		39g
尺寸规格	Dimensions		





1130 Cadillac Ct., Milpitas, CA 95035,USA Phone: +1-408-263-0988 Fax: +1-408-217-1960 Email:sales@leopardimaging.com Website: www.leopardimaging.com