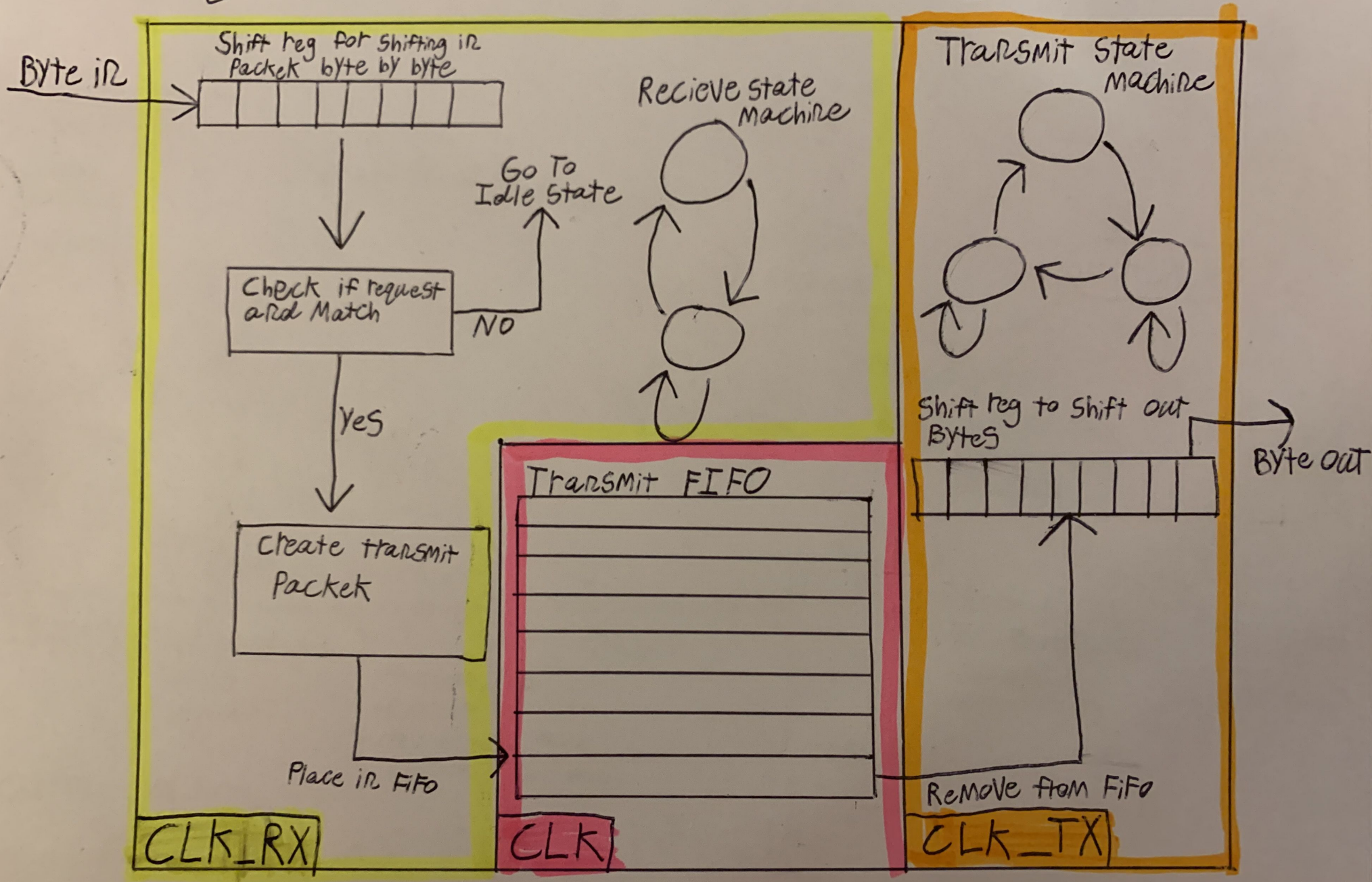
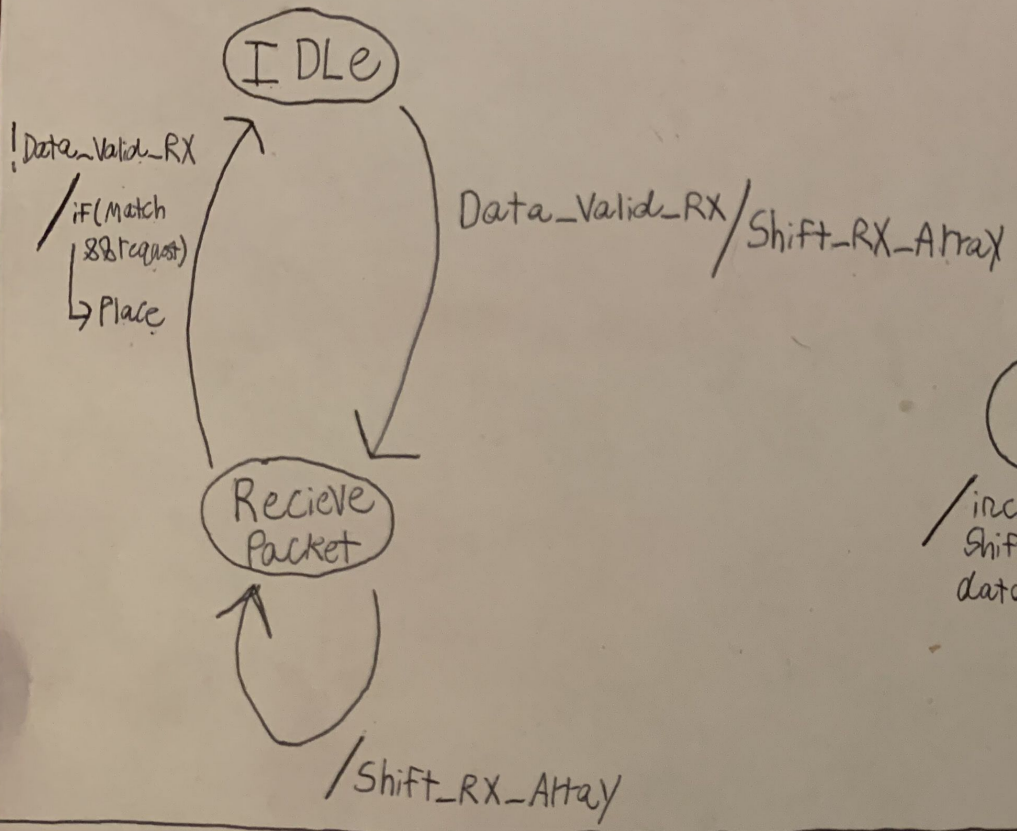
High Level Conceptual Design



RX (CLK_RX)



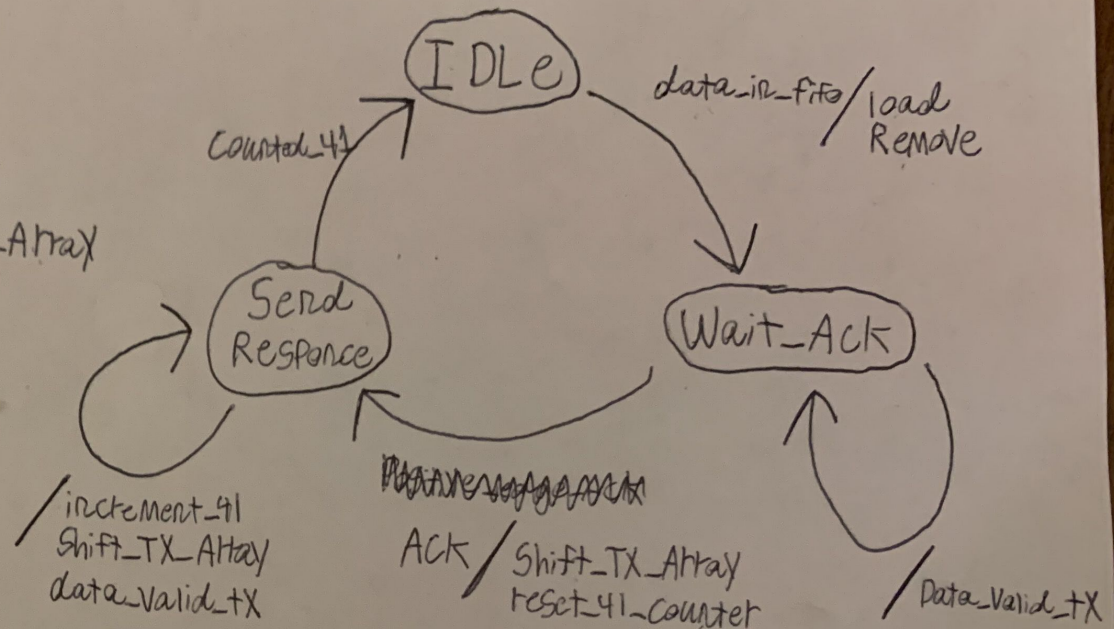
Inputs: • Data_Valid_RX
• Match
• request

Outputs: • Shift_RX_Array
• Place

Devices: • RX_Byte_Shift_array

(CLK) Devices:
• transmit_fifo
• Counter
• Positive_edge_Place
• Negative_edge_Remove

TX (CLK_TX)



Inputs: • data_in_fifo
• Counted_41
• Data_Ack_TX

Outputs: • load
• remove
• data_valid_tX
• Shift_TX_Array
• reset_41_Counter
• increment_41

Devices
• Counter_41
• TX_Byte_Shift_Array