

Figure 1: Overall Design of Objects and Interconnections

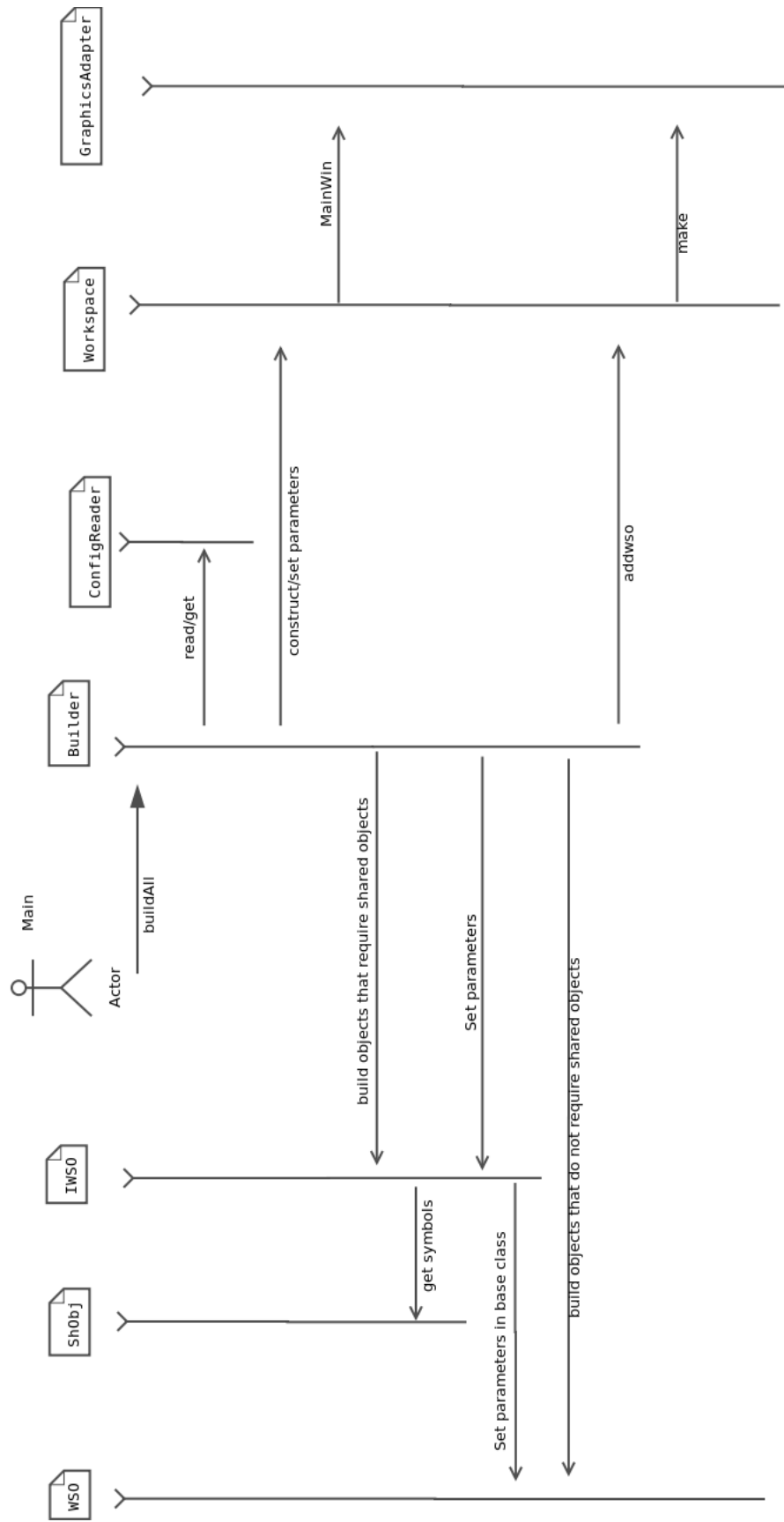


Figure 2: Use cases to show flow during initialization

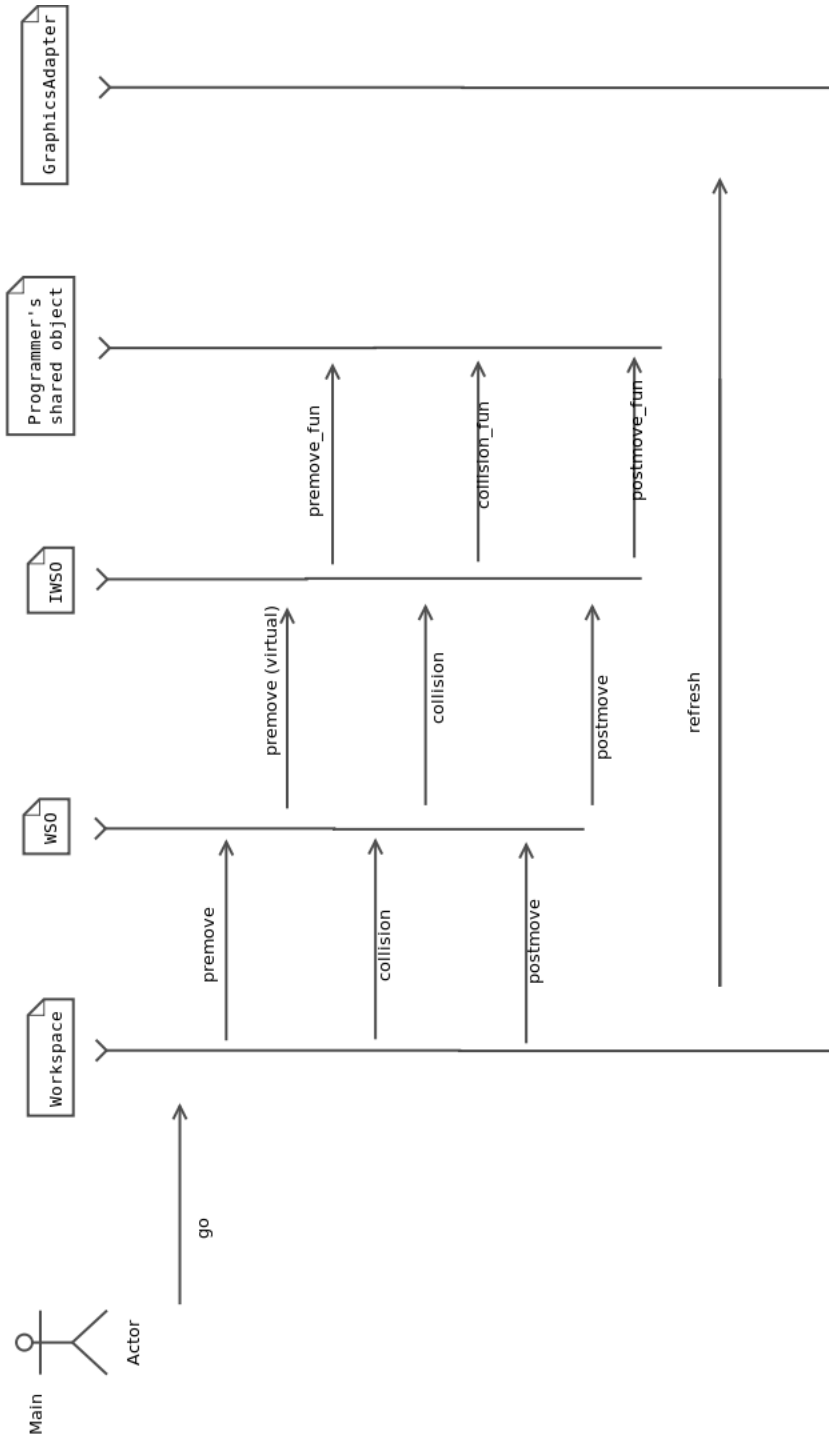


Figure 3: Use cases to show flow during regular execution (main loop)