

# Quentin Pierce

Aloha, OR - Email me on Indeed: [indeed.com/r/Quentin-Pierce/797665fe374a95df](https://www.indeed.com/r/Quentin-Pierce/797665fe374a95df)

Authorized to work in the US for any employer

## WORK EXPERIENCE

### Software Engineer

Intel - Hillsboro, OR - 2008 to 2016

Software Engineer with a central SW tools group.

Provided maintenance and new features for a custom SW layer between two vendors' software. Wrote rules for automating register-RTL generation. Wrote rules for RTL static checking (linting). Created automation for the static checking flows. Familiar with the world-class processes used by Intel.

Reduced RTL model simulation time by days using a modified checkpointing-SW; then co-authored a peer-reviewed paper and disseminated the process and paper to design teams.

Wrote static-check rules that encapsulated RTL knowledge from senior engineers and passed that knowledge to junior engineers, enabling their quicker learning and faster debugging.

Led a Makers' Movement group inside Intel; this resulted in great personal development.

Learned both teaching skills and IoT design by creating a series of after work lectures on IoT.

### Hardware Engineer

TEKTRONIX - Beaverton, OR - 2000 to 2007

Hardware Design Engineer in the Oscilloscope Product Line.

Designed digital interface-circuitry for several Tektronix oscilloscope models. Supported manufacturing by providing troubleshooting processes, design notes, and circuit theory summaries. Designed FPGA firmware and assisted in driver software development. Used oscilloscopes, logic analyzers, and computer-aided test systems. Familiar with the world-class processes used by Tektronix.

Learned the Verilog language, wrote the firmware, and synthesized an FPGA to fit a need for a new piece of hardware. Then partnered with SW engineering on driver and GUI changes.

Worked with software engineers to develop and debug hardware drivers. Worked with hardware engineers to teach them trends in computer design.

Designed two thermal printer systems and learned mechanical control, mixed signal design, and power supply design.

Designed differential signaling to connect circuit systems together. Used LVDS in two FPGA designs. Designed modern single-ended signals as well.

Studied high-speed analog-to-digital conversion and high-speed signaling theory with industry leaders.

Created two interface board designs, reused them twice, and documented all four board designs for posterity.

The boards contained PCI, USB, Ethernet, and RS232 and were reused by other product teams as well.

Wrote peer-reviewed design specifications, shared them with peers and technicians, and was responsible for training both personnel to become familiar with the interface circuitry.

## EDUCATION

### MSEE in Computer Architecture

Port - Portland, OR

2007 to 2010

## SKILLS

RTL (5 years), System Verilog (5 years), Analog Circuit Design (7 years), C++ (6 years), FPGA design (4 years), Digital Circuit Design (7 years), Oscilloscopes, Test and Measurement Equipment (9 years), Static Code Checking, Linting. (4 years), DSP, Digital Signal Processing (Less than 1 year)

## LINKS

<http://www.linkedin.com/in/quentin-t-pierce>