Michaela Perez

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VISA STATUS: US Citizen LOCATION: Richardson, TX

EDUCATION

August 2017- Computer Engineering

GPA: 3.817

May 2021 **GPA:** 3.817

Dean's List: Fall 2017, Fall 2018

Professional Societies:

Institute of Electrical and Electronics Engineers August 2018-Present

IEEEUTD The Forge February 2020-May 2020

Society of Women Engineers Operations Chair

Co-founder of Ladies in Tech Mentoring Program

AUGUST 2019-PRESENT APRIL 2020-PRESENT APRIL 2020-PRESENT

The University of Texas at Dallas

EXPERIENCE

Engineering Intern Ameren

St. Louis, MO (Remote)

May 2020-Present

- Working within Power Operations in the Supervisory Control and Data Acquisition group at Ameren Missouri

 Detecting and resolving inconsistencies between the power grid and our monitoring systems to provide our customers reliable energy

Undergraduate Research Fellow The Center for Power Optimization of Electro-Thermal Systems Champaign, IL May 2019-August 2019

- Worked under Dr. Nenad Miljkovic and Dr. Allison Mahvi in the Energy Transportation Research Lab using SolidWorks and LabVIEW to design and test a 3D-printed, metal heat exchanger for electric vehicle batteries
- Communicated the needs and findings of the project with a wide-range of professionals during development and at the Illinois Summer Research Symposium and the 2019 POETS Annual Meeting
- Found that DMLM results in highly variable, rough and porous microchannel heat exchangers

Research Intern SAN ANTONIO, TX

The University of Texas Health Science Center at San Antonio

OCTOBER 2016-MAY 2017

- Worked under Dr. HR Rawls in the Department of Comprehensive Dentistry to improve the mechanical properties of dental restorative composites
- Collaborated with field experts, analyzed collected data, and presented findings at a formal poster session

SKILLS

PROGRAMMING Java, C++, MIPS Assembly, Verilog, MATLAB

APPLICATIONS SolidWorks, Fusion 360, LogicWorks, LabVIEW, MS Excel, PSpice

OTHER Microsoft Office, G Suite, LATEX, Windows, Unix Systems

PROJECTS

IEEEUTD THE FORGE	Built a troubleshooting desktop application for the visually-impaired	Spring 2020
	customers of the non-profit Computers for the Blind	
IC Tester	Designed a tester to distinguish between AND- and OR-chips and	April 2020
	whether they functioned correctly	
Mini-CPU	Designed a 4-bit mini-CPU which performed 4 instructions (add,	April 2018
	AND, OR, NOT) using basic logic gates and flip flops	