


# Celine Clock 2021

Michal Makowka

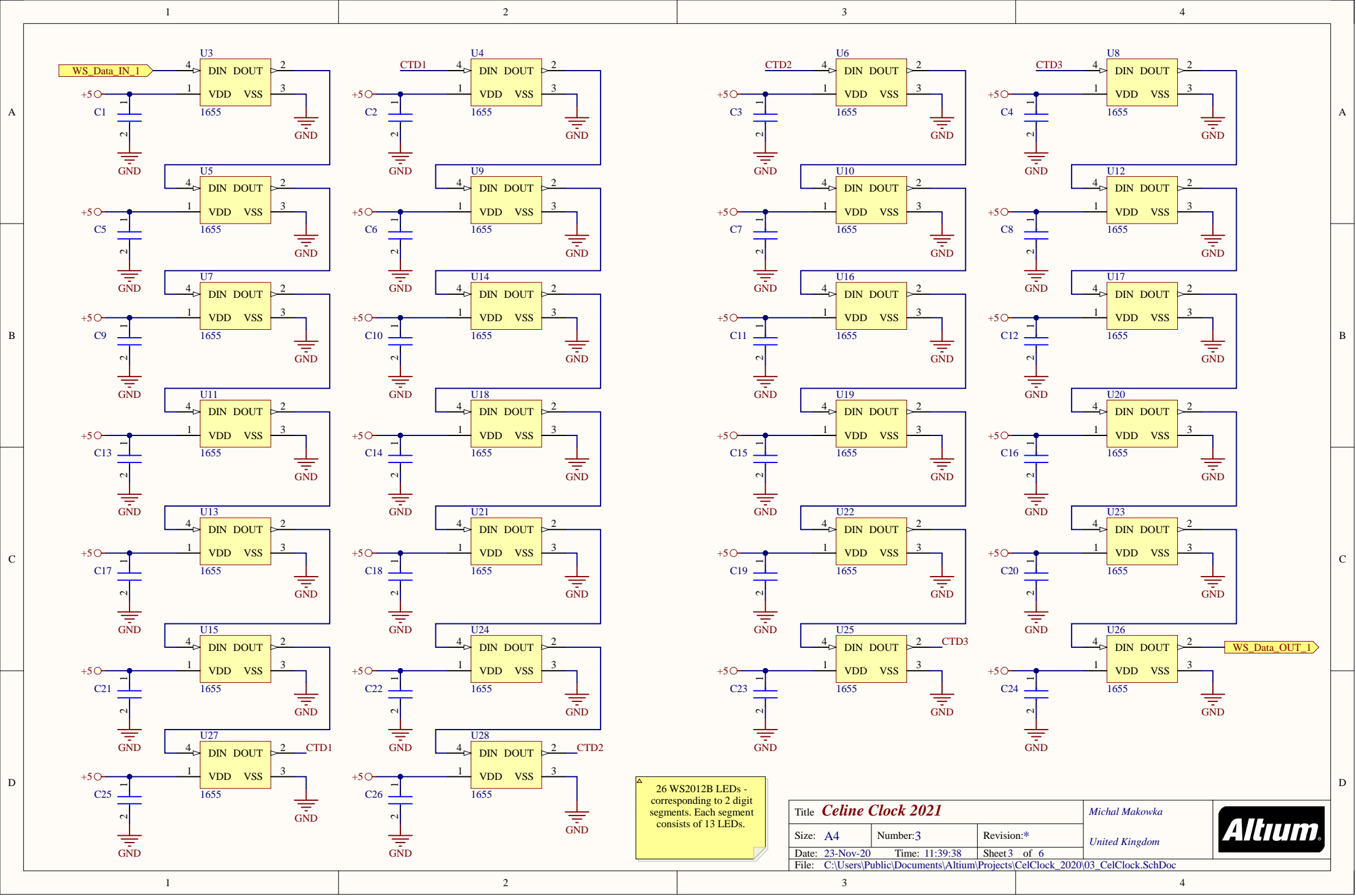
## Technical Reference Notes:

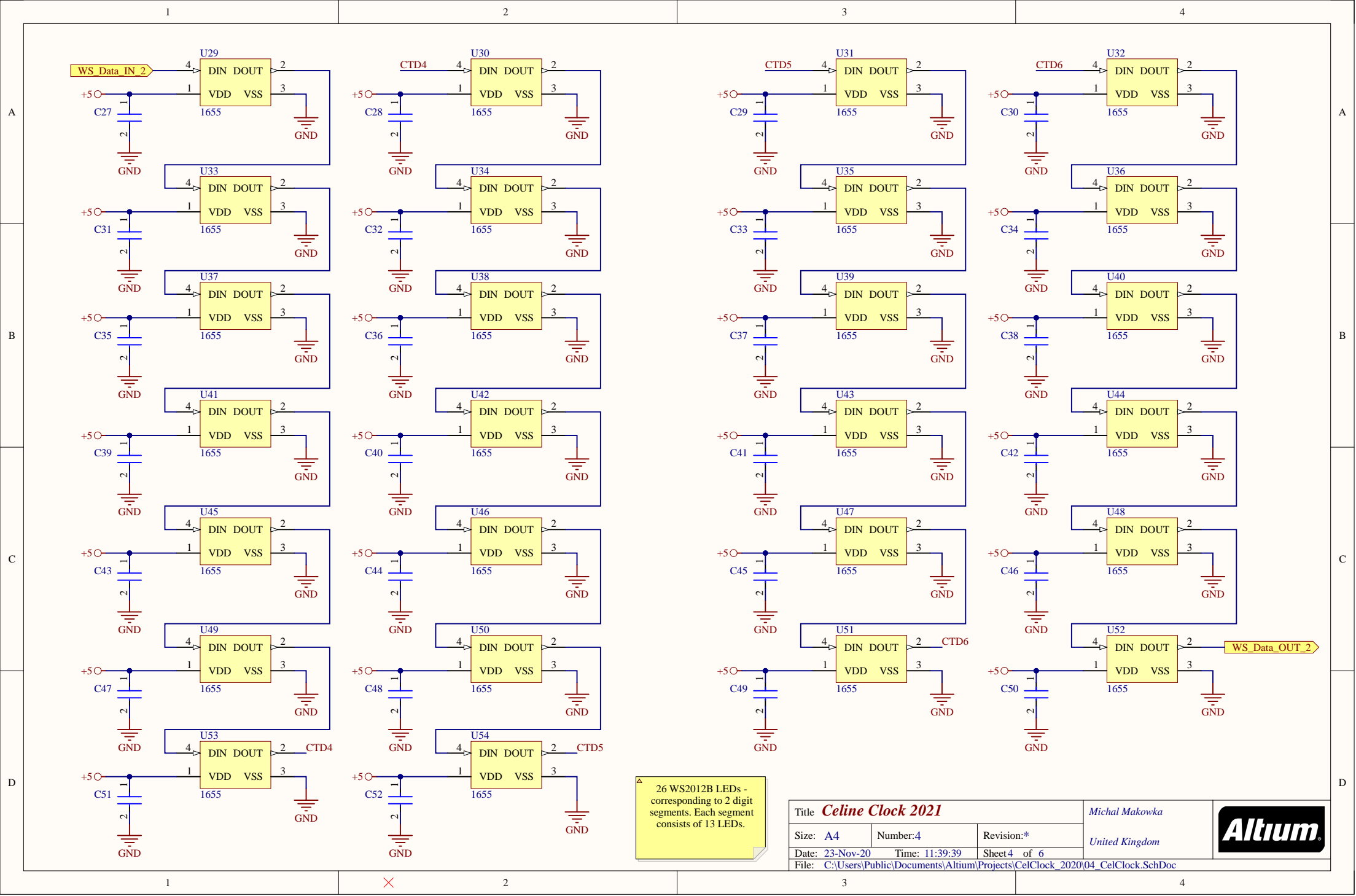
- Schematic [4 Pages]
- PCB 3D Model

Michal Makowka's Technology

Title <i>Celine Clock 2021</i>			Michal Makowka  United Kingdom	
Size: A4	Number: 1	Revision:*		
Date: 23-Nov-20	Time: 11:39:38	Sheet 1 of 6		
File: C:\Users\Public\Documents\Altium\Projects\CelClock_2020\01_CelClock_Coversheet.SchDoc				







A

B

C

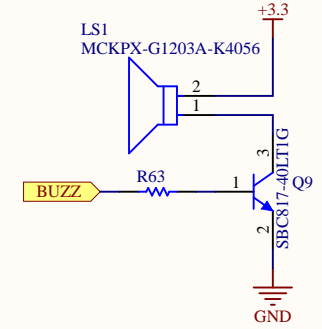
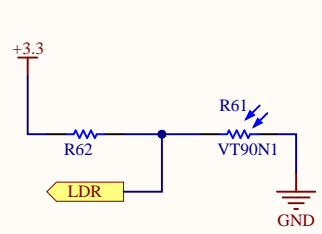
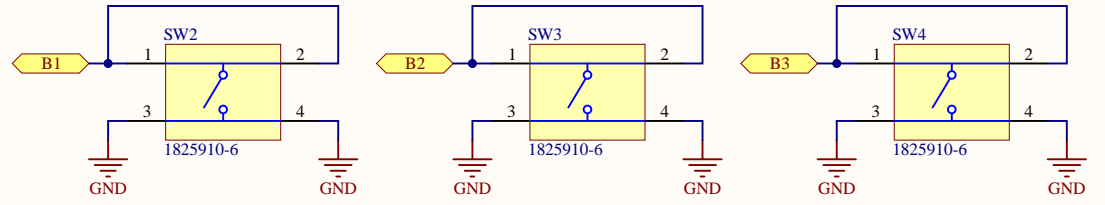
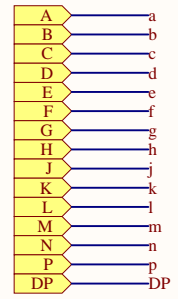
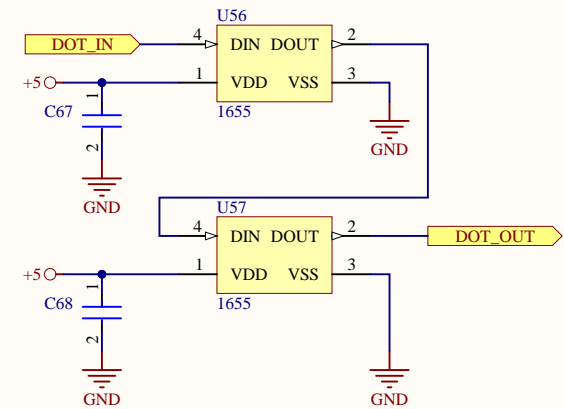
D

A

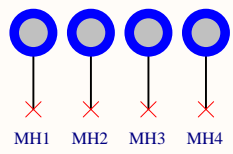
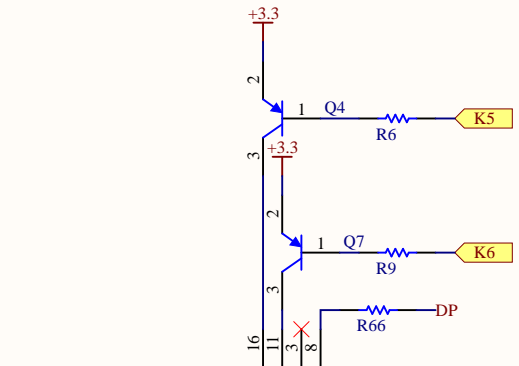
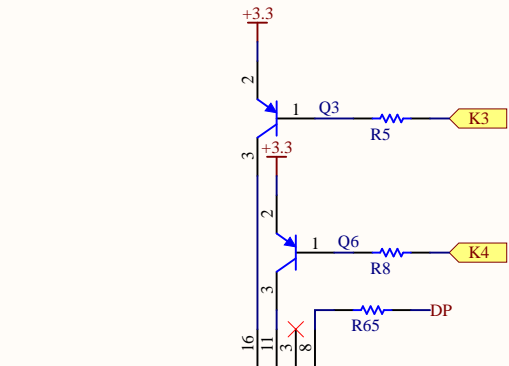
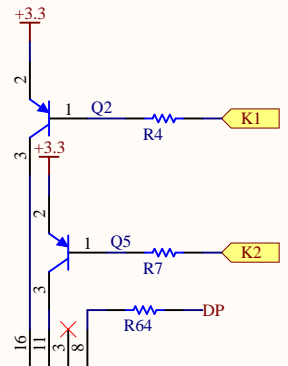
B

C

D




**3 X HDSP-A22C (Broadcom)**  
**Common Anode 16-Seg Display**



# Revision History

16-NOV-2020: Schematic Completed and Compiled

Title <i>Celine Clock 2021</i>			<i>Michal Makowka</i>  <i>United Kingdom</i>	
Size: <i>A4</i>	Number: <i>6</i>	Revision: <i>*</i>		
Date: <i>23-Nov-20</i>	Time: <i>11:39:39</i>	Sheet <i>6</i> of <i>6</i>		
File: <i>C:\Users\Public\Documents\Altium\Projects\CelClock_2020\06_CelClock_Release.SchDoc</i>				