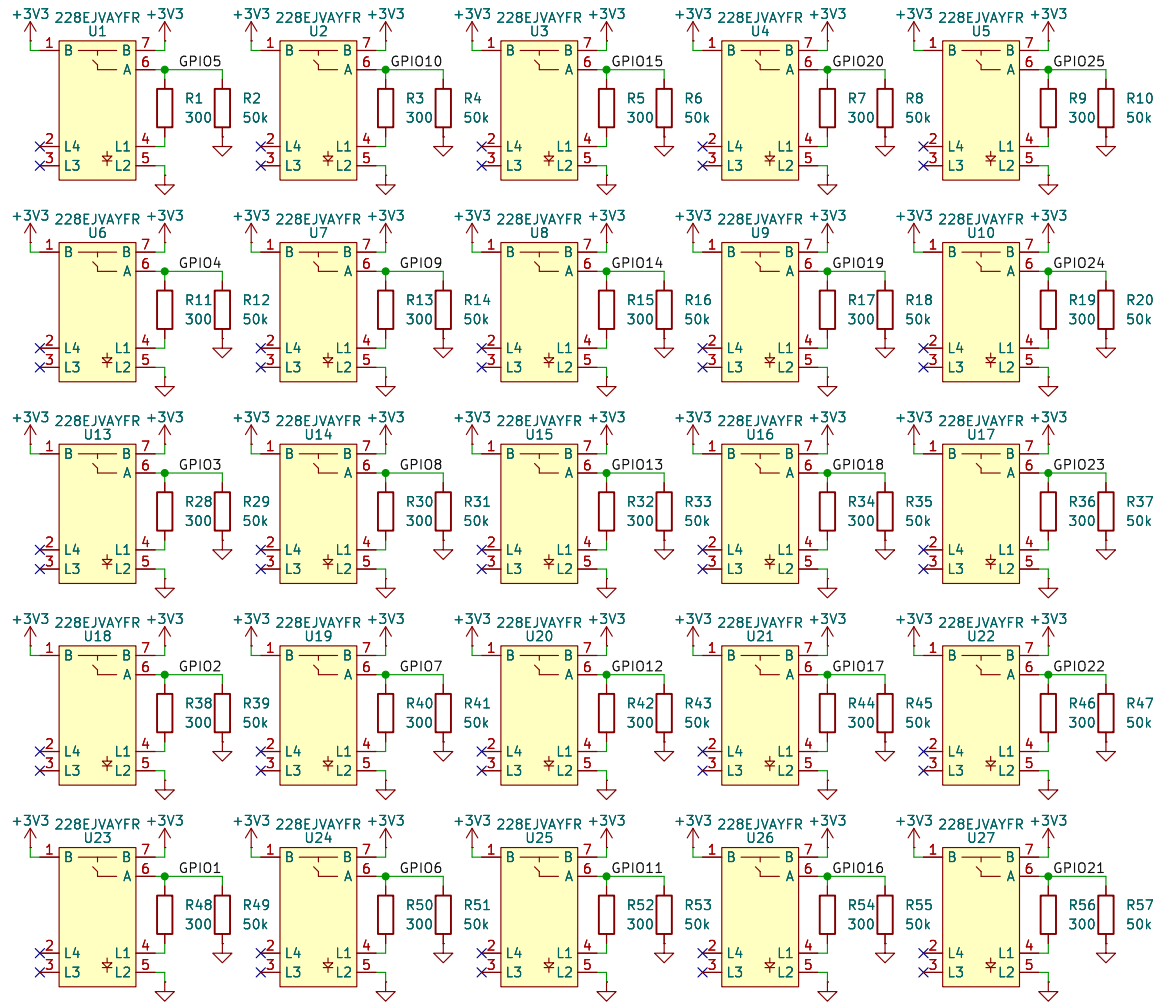
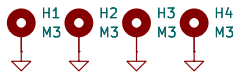


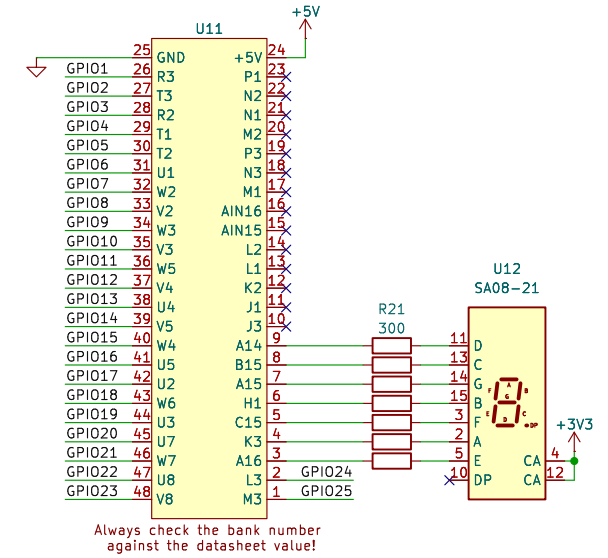
Button Matrix



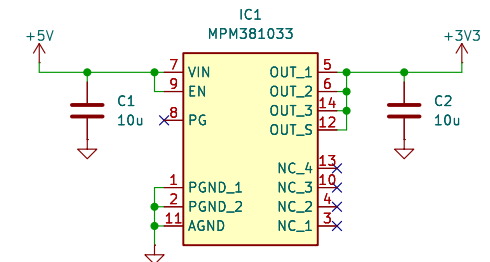
Mounting Holes



Cmod A7 Header



Voltage Regulator



MIT CBA – Michal Varsanyi

Sheet: /

File: BrakeoutBoard.kicad_sch

Title: FPGA Neural Network Demo Board

Size: A4

Date: 2026-02-05

KiCad E.D.A. 9.0.6

Rev: 1.0

Id: 1/1