

Root [0]

Technical Sheet Only!
Please refer to the subsequent sheets for schematic

FTDI



File: ftdi.kicad_sch

Programming

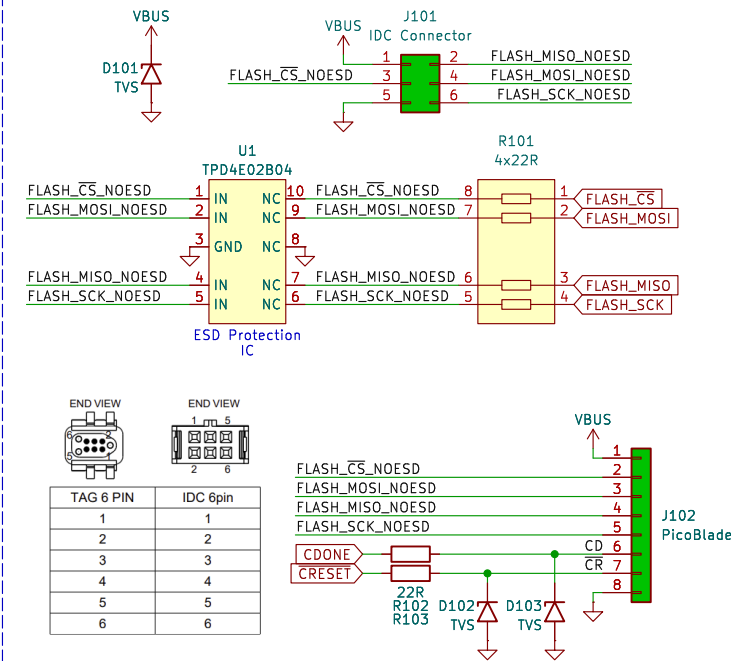


File: programming.kicad_sch

Czech Technical University in Prague		
Sheet: /		
File: Programmer_Board.kicad_sch		
Title: VoxSense Sensor Board Programmer – Root		
Size: A4	Date: 2025–12–09	Rev: 1.0
KiCad E.D.A. 9.0.6		Id: 0/3

Programming Interfaces [1]

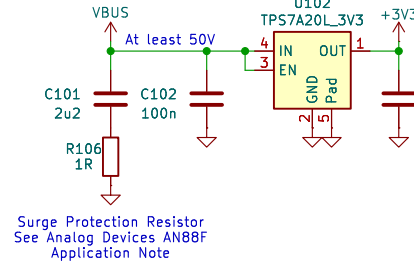
Tag Connect Programming Interface



Mounting Holes

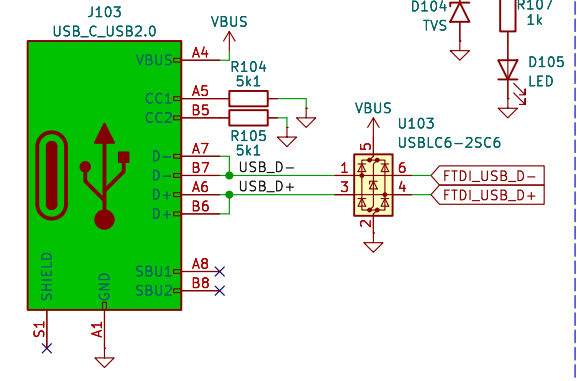


Core Supply Voltage



Surge Protection Resistor
See Analog Devices AN88F
Application Note

USB 2.0



Czech Technical University in Prague

Sheet: /Programming/
File: programming.kicad_sch

Title: Programming Interfaces

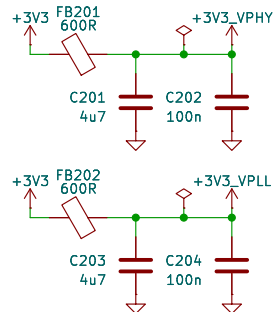
Size: A4 Date: 2025-12-13

KiCad E.D.A. 9.0.6

Rev: 1.0

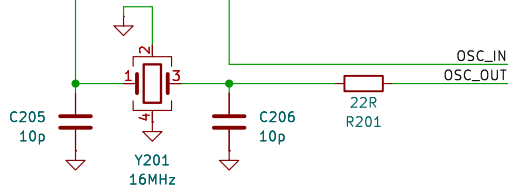
Id: 1/3

FTDI [2]

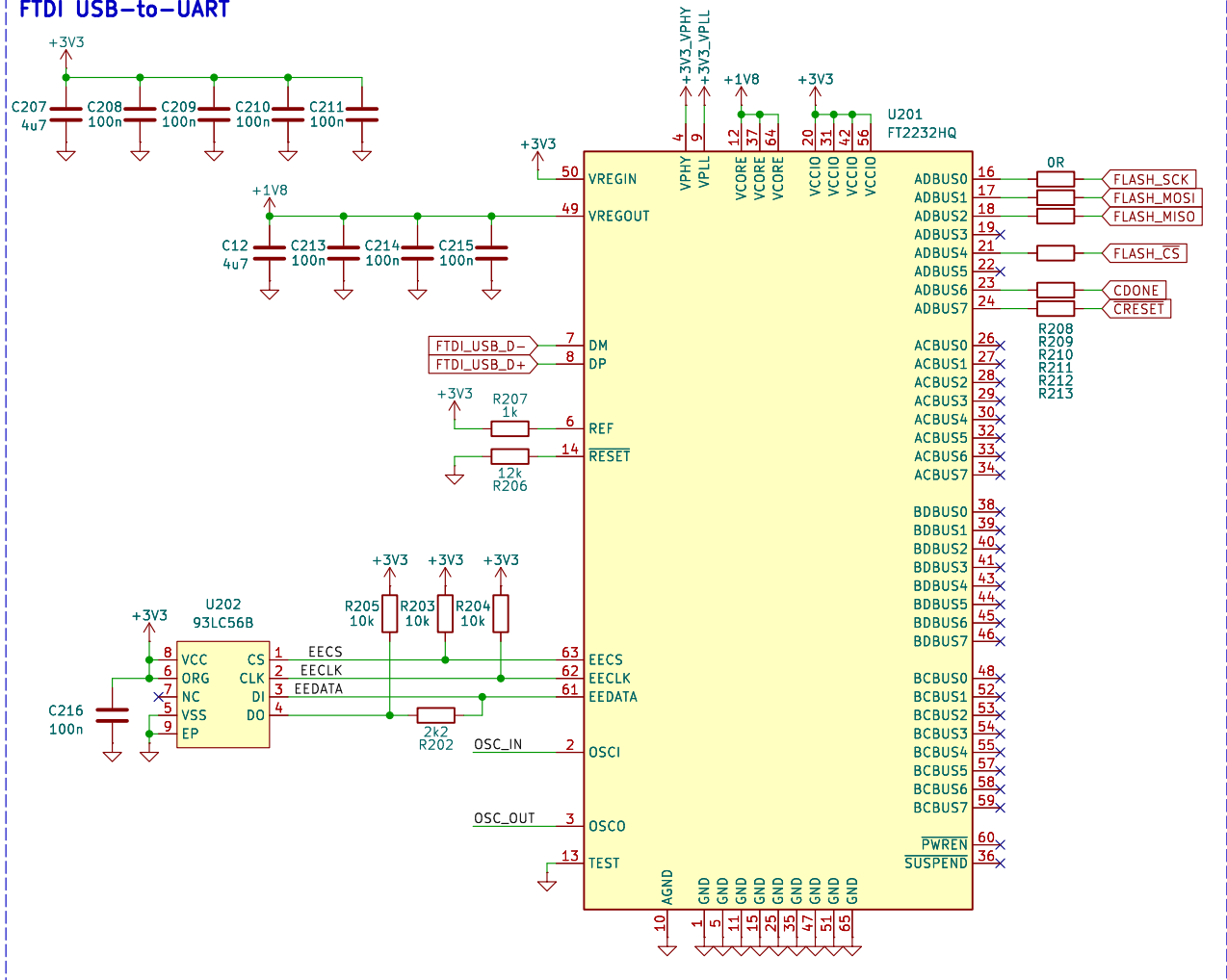


Ferrite beads must
be rated at least for 600mA

Crystal


$$(10\text{pF Load Capacitance} - 5\text{pF Stray Capacitance}) * 2 = 10\text{pF Load Capacitors}$$

FTDI USB-to-UART



Czech Technical University in Prague

Sheet: /FTDI/
File: ftdi.kicad_sch

Title: *FTDI*

Size: A4	Date: 2025-12-13
----------	------------------

KiCad E.D.A. 9.0.6

Rev: 1.0

Id: 2/3