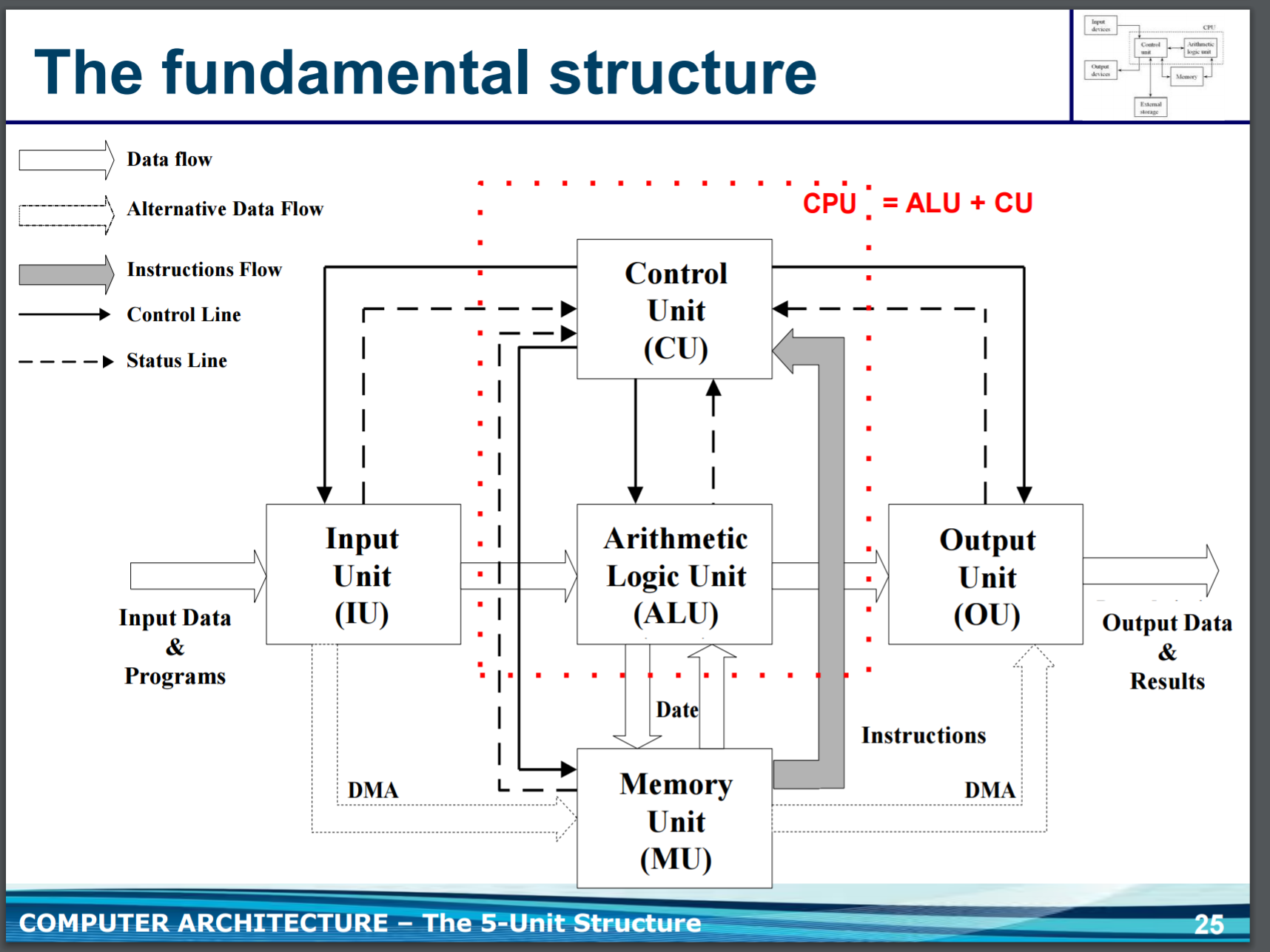
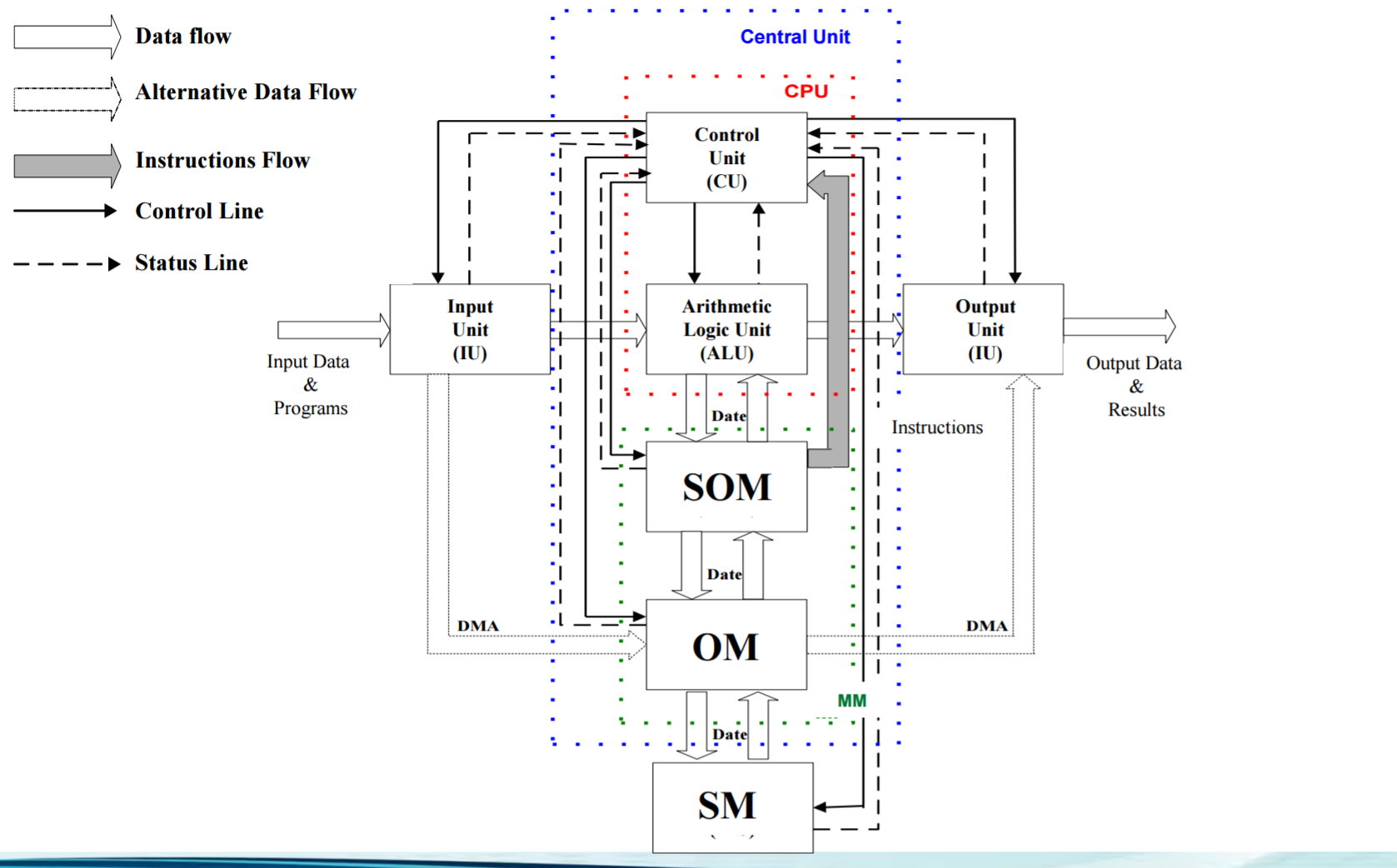
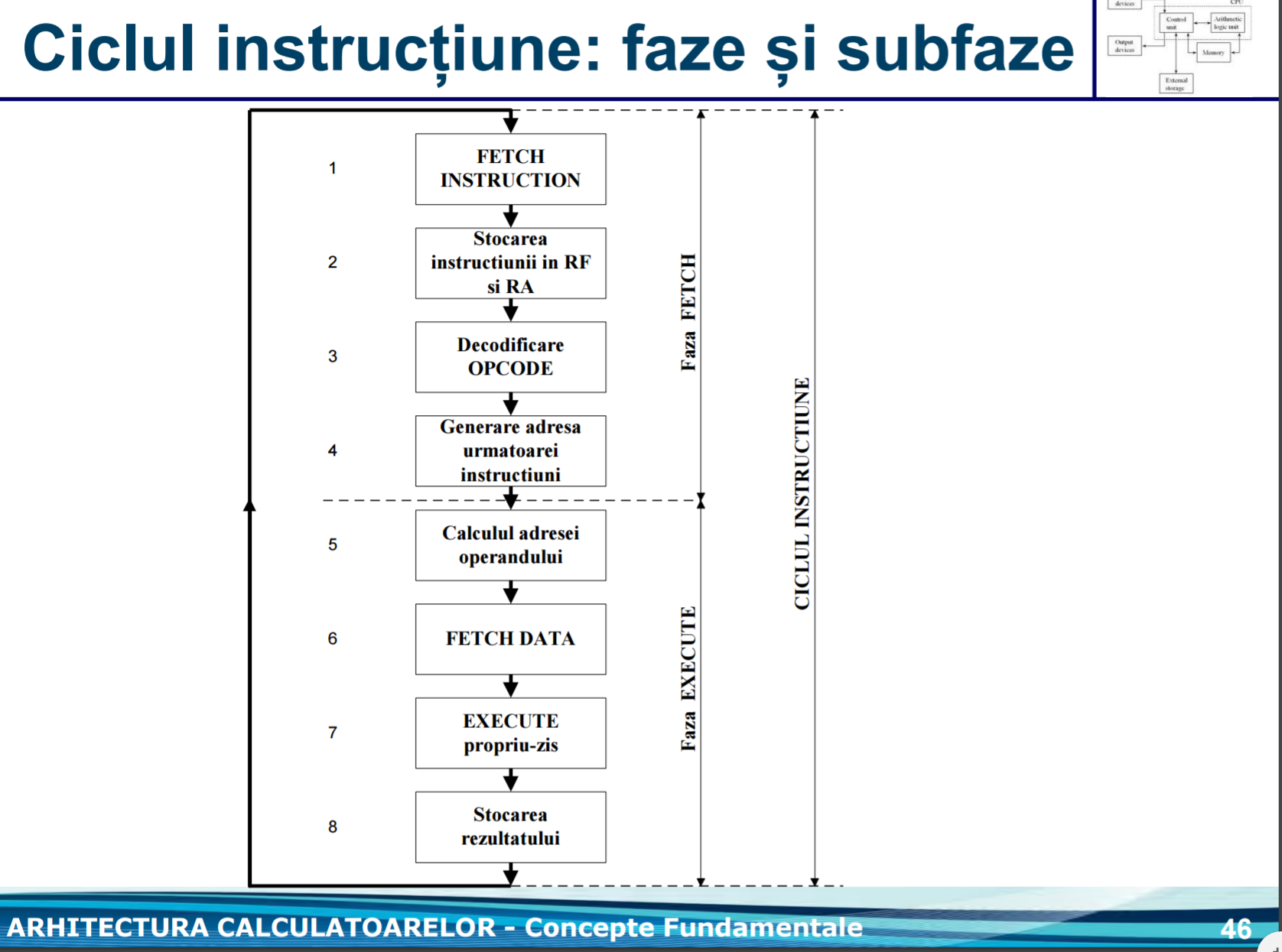
Schema 1: – Von Neumann’s fundamental structure.



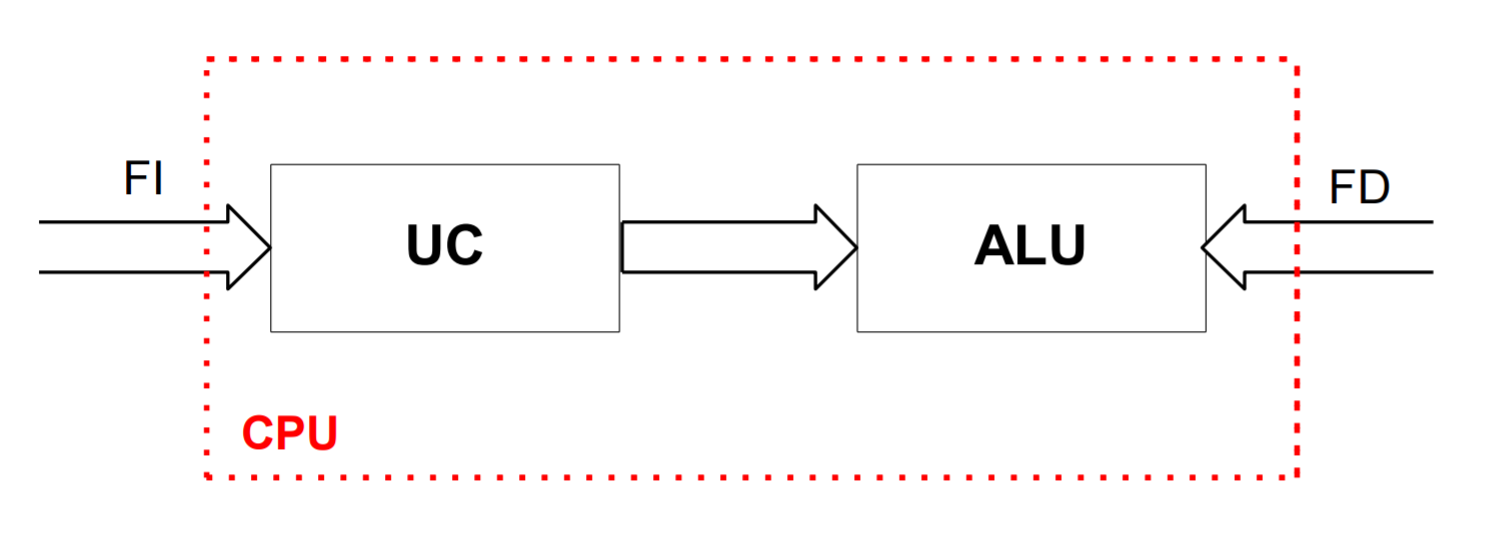
Schema 2: Fundamental structure cu Super-operative memory si operative memory si secondary memory.



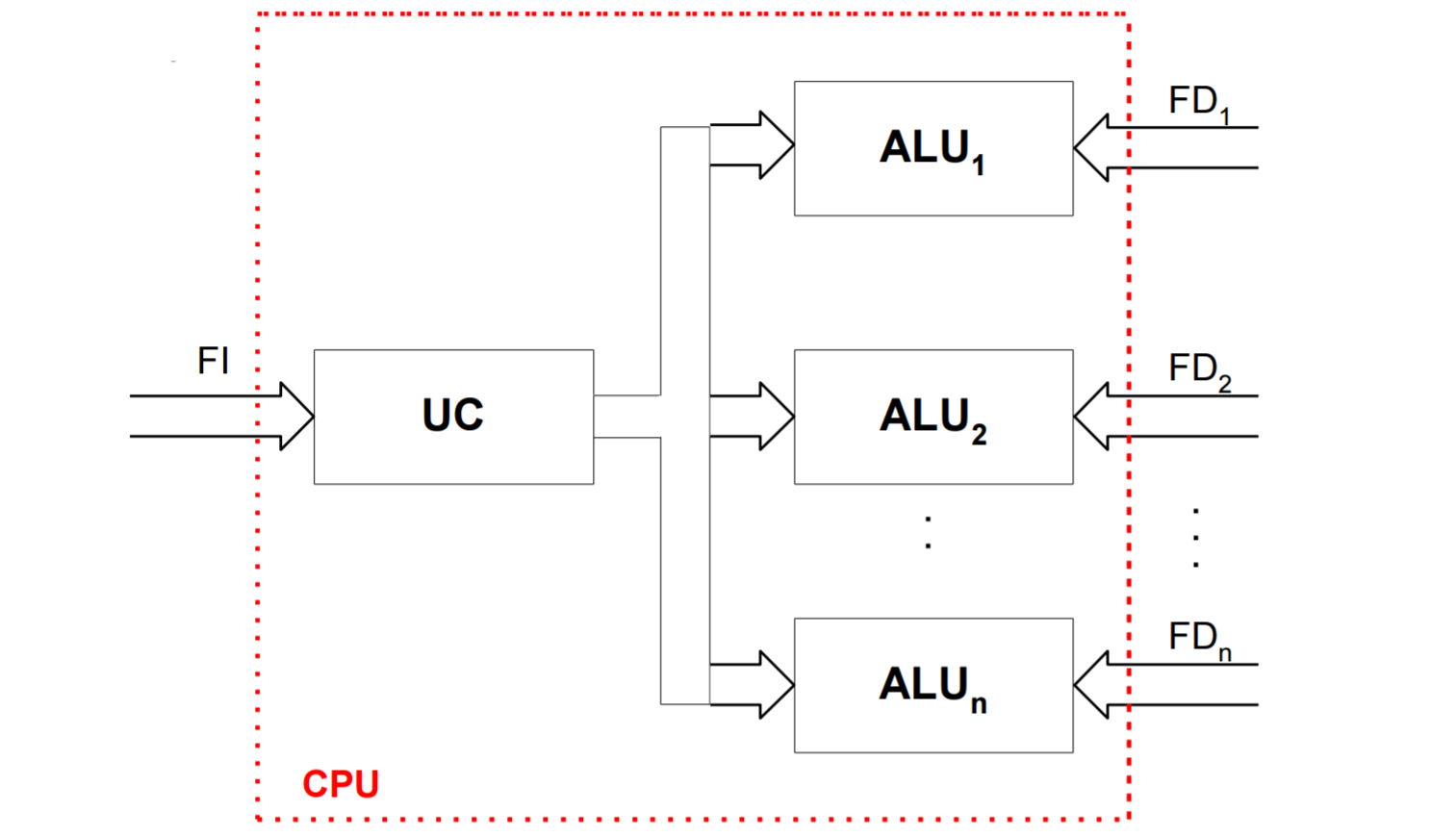
Schema 3: Fetch and Execute Instruction Cycle.



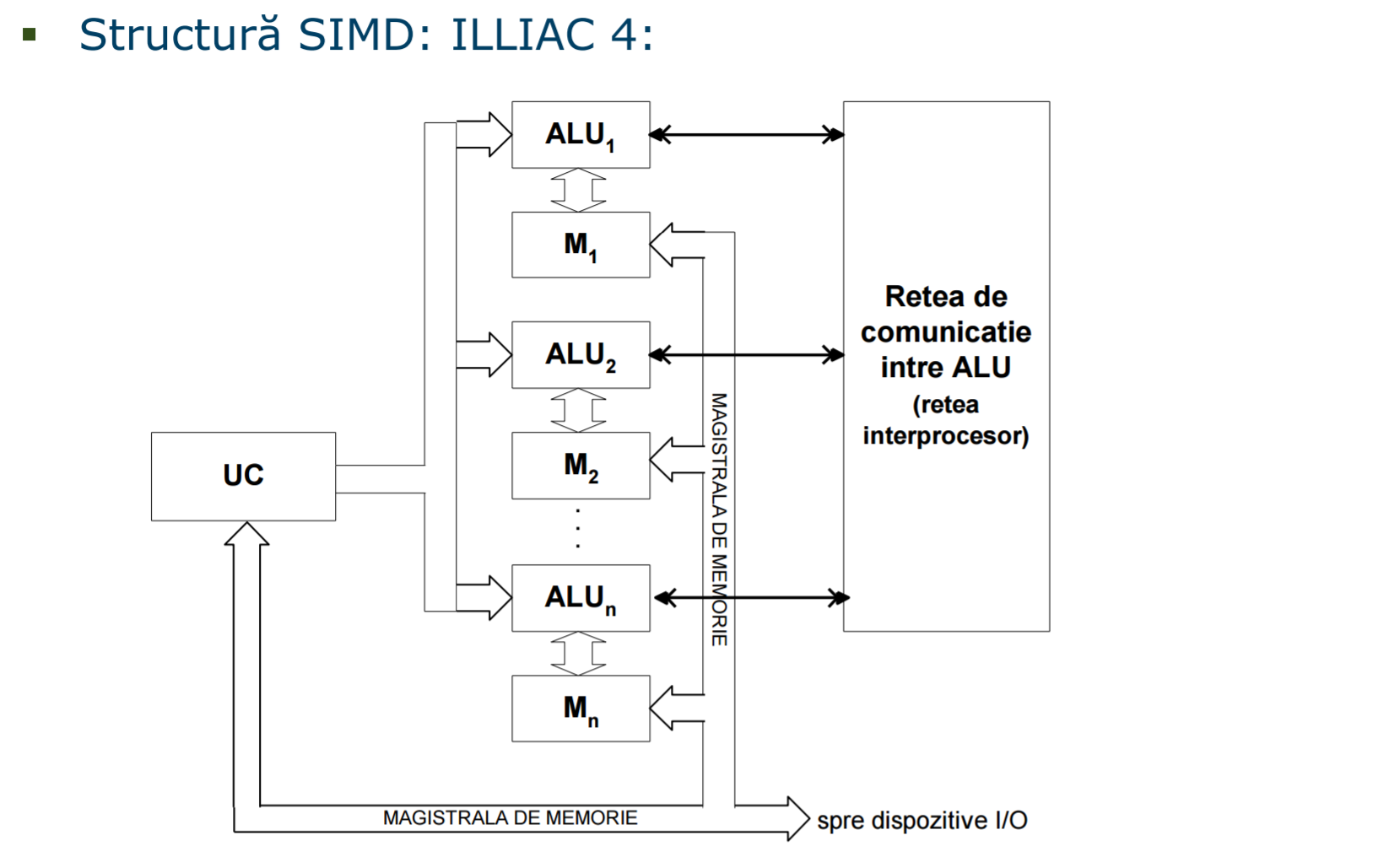
Schema 4: SISD(Single Instruction, Single Data)



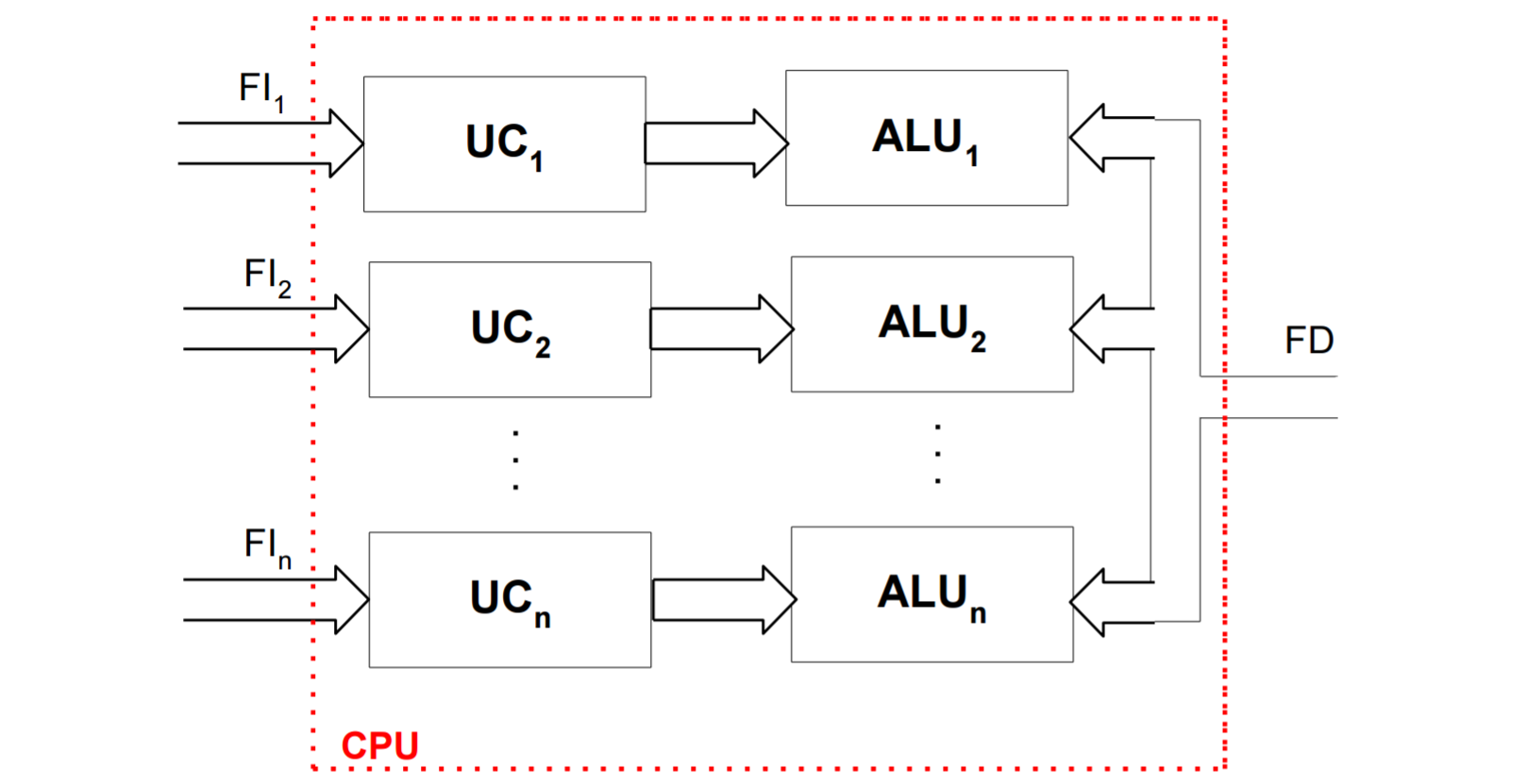
Schema 5: SIMD(Single Instruction, Multiple Data)



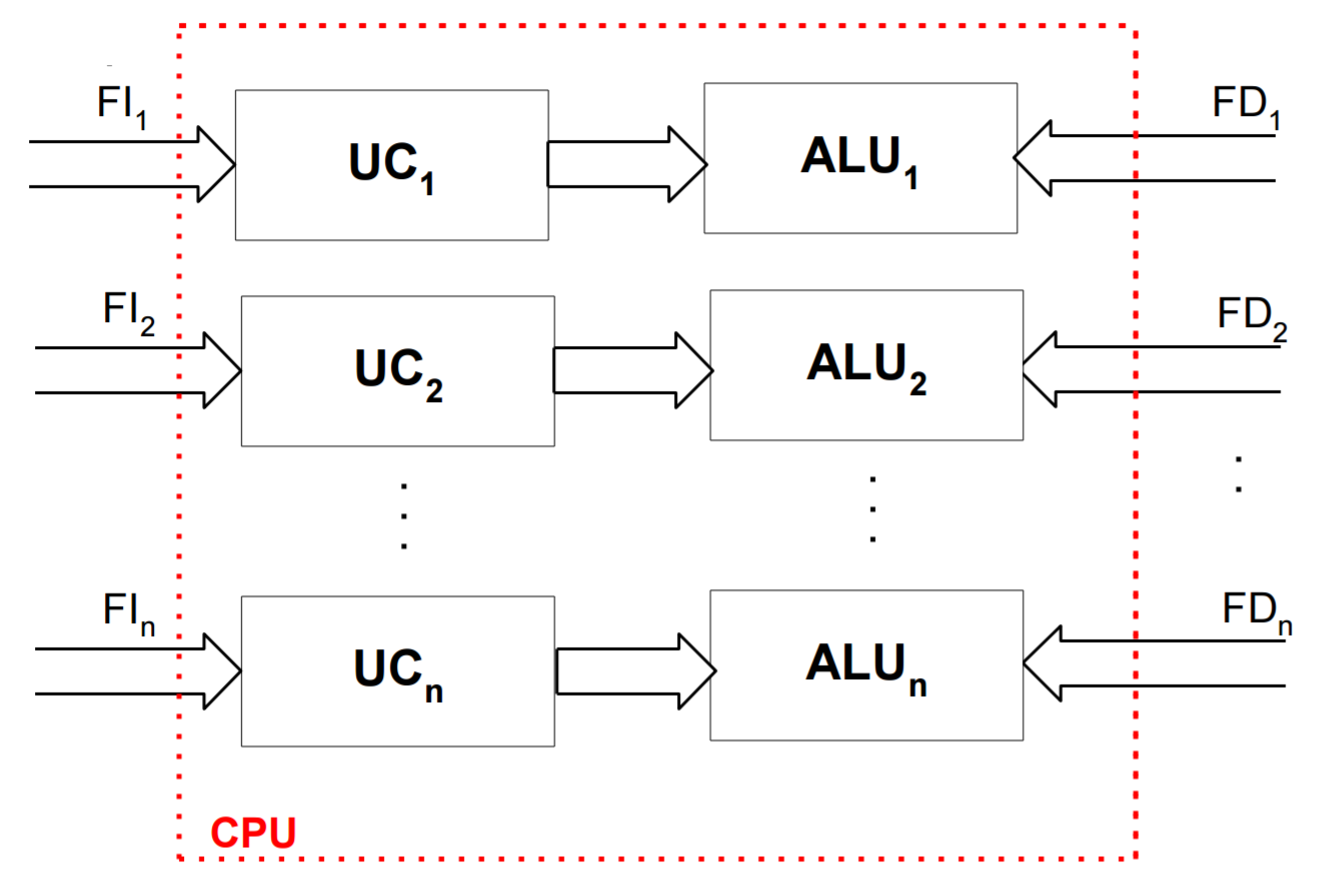
Schema 6: SIMD ILLIAC.



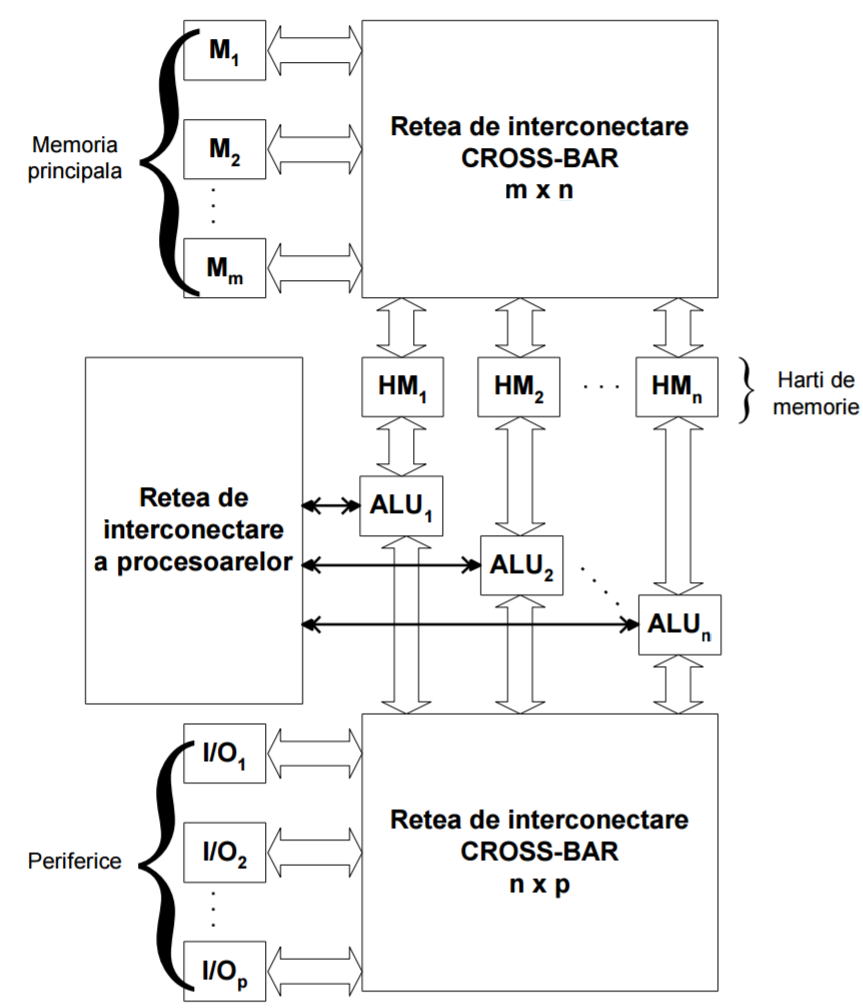
Schema 7: MISD



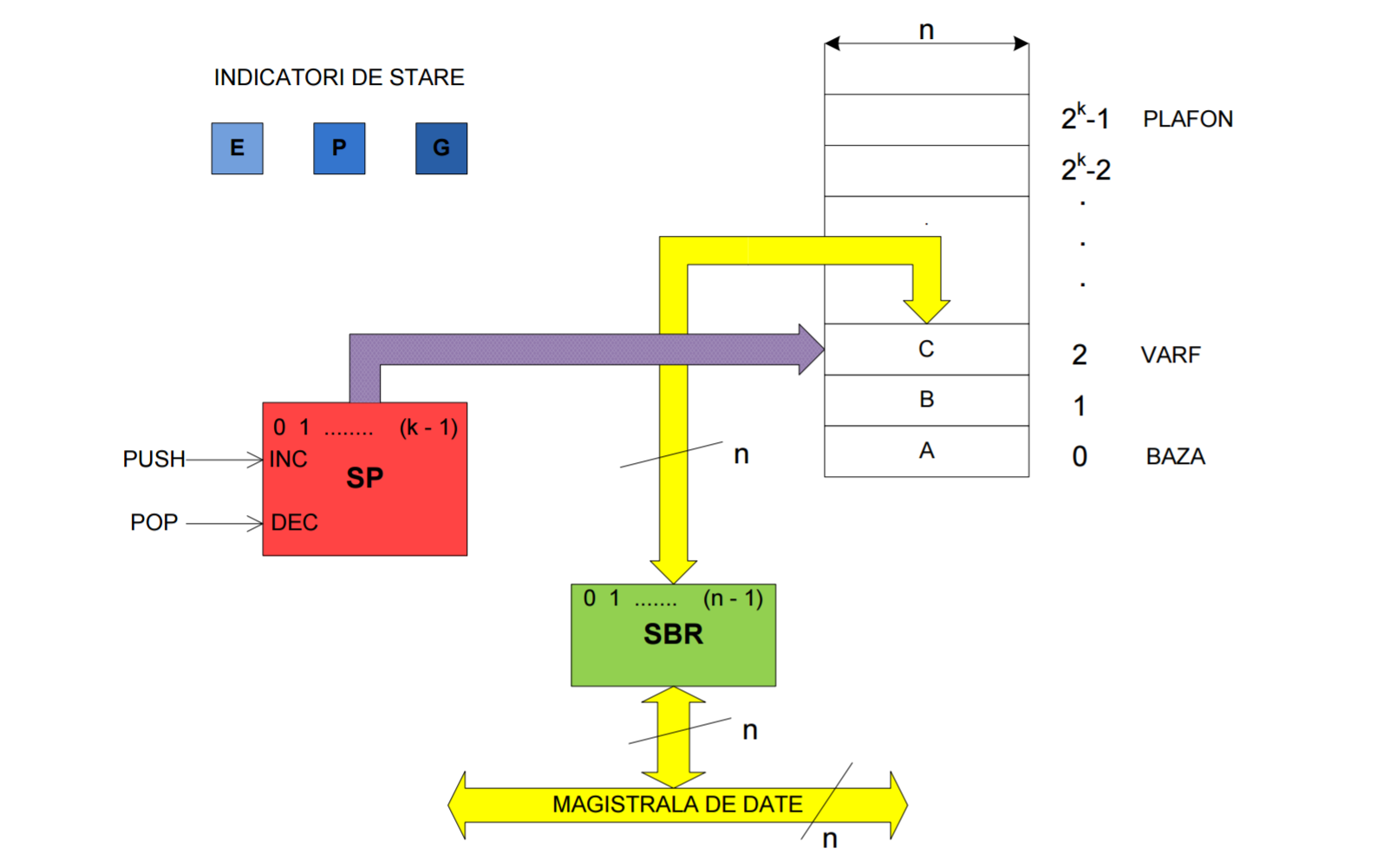
Schema 8: MIMD



Schema 9: MIMD



Schema 10: Stiva Hardware



Schema 11:Stiva Software

