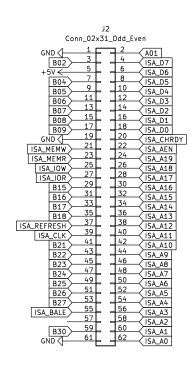


	J1
	Bus_ISA_8bit
BO2   BO2   GND   BO2   RESET   SV < BO3   VCC   BO4   BO2   RESET   SV < BO3   VCC   BO5   BO6   BO5   BO6   BO5   BO6   BO7   B	Bus_ISA_8bit
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