Table 1. F28377S LaunchPad Pin Out and Pin Mux Options - J1, J3

Mux Value				J1 Pin	J3 Pin	Mux Value			
3	2	1	0			0	1	2	3
			+3.3 V	1	21	+5 V			
	EM1D13		GPIO71	2	22	GND			
EM1DQM2	EM1A17		GPIO90	3	23	ADCIN14			
EM1DQM1	EM1A16		GPIO89	4	24	ADCINB1			
	EM1A3		GPIO41	5	25	ADCINB4			
			NC	6	26	ADCINB2			
EM2D8	EM1D24	MCLKRB	GPIO60	7	27	ADCINA0			
EM2D7	EM1D23	MFSRB	GPIO61	8	28	ADCINB0			
			GPIO43	9	29	ADCINA1			

Table 2. F28377S LaunchPad Pin Out and Pin Mux Options - J4, J2

Mux Value				J4 Pin	J2 Pin	Mux Value			
3	2	1	0			0	1	2	3
MDXB	CANTXB	EPWM7A	GPIO12	40	20	GND			
MDRB	CANRXB	EPWM7B	GPIO13	39	19	GPIO4	EPWM3A		
MCLKXB	SCITXDB	EPWM8A	GPIO14	38	18	GPIO62	SCIRXDC	EM1D22	EM2D6
MFSXB	SCIRXDB	EPWM8B	GPIO15	37	17	NC			
OUTPUTXBAR7	CANTXB	SPISIMOA	GPIO16	36	16	RESET#			
OUTPUTXBAR8	CANRXB	SPISOMIA	GPIO17	35	15	GPIO58	MCLKRA	EM1D26	EM2D10
CANTXB	MDXA	EQEP1A	GPIO20	34	14	GPIO59	MFSRA	EM1D25	EM2D9
CANRXB	MDRA	EQEP1B	GPIO21	33	13	GPIO72		EM1D12	
			DAC1	32	12	GPIO73		EM1D11	XCLKOUT
			DAC2	31	11	GPIO78		EM1D6	

Table 3. F28377S LaunchPad Pin Out and Pin Mux Options - J5, J7

Mux Value				J5 Pin	J7 Pin	Mux Value			
3	2	1	0			0	1	2	3
			+3.3V	41	61	+5V			
			NC	42	62	GND			
EM1RAS	EM1A14		GPIO87	43	63	ADCIN15			
EM1CAS	EM1A13		GPIO86	44	64	ADCINA2			
			NC	45	65	ADCINA5			
			NC	46	66	ADCINB5			
	EM1D19		GPIO65	47	67	ADCINA3			
			NC	48	68	ADCINB3			
	EM1D15		GPIO69	49	69	ADCINA4			
	EM1D18		GPIO66	50	70	NC			

Table 4. F28377S LaunchPad Pin Out and Pin Mux Options - J8, J6

Mux Value				J8 Pin	J6 Pin	Mux Value			
3	2	1	0			0	1	2	3
		EPWM1A	GPIO2	80	60	GND			
		EPWM1B	GPIO3	79	59	GPIO91		EM1A18	EEM1DQM3
ADCSOCB0	CANRXB	EPWM6A	GPIO10	78	58	NC			
OUTPUTXBAR7	SCIRXDB	EPWM6B	GPIO11	77	57	NC			
CANRXA	SCITXDB	SPICLKA	GPIO18	76	56	RESET#			
CANTXA	SCIRXDB	SPISTEA	GPIO19	75	55	GPIO63	SCITXDC	EM1D21	EM2D5
			NC	74	54	GPIO64		EM1D20	EM2D4
			NC	73	53	GPIO99			EM2A1
			DAC3	72	52	GPIO92		EM1A19	EM1BA1
			DAC4	71	51	NC			