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Note:

MicroPhase board HW FULL schematics are not available. MicroPhase has an open source code but not an open hardware schematics. Nonetheless, DEVELOPMENT schematics are available here .

This schematic will give you information about HW configuration, FPGA pin connection and similar.

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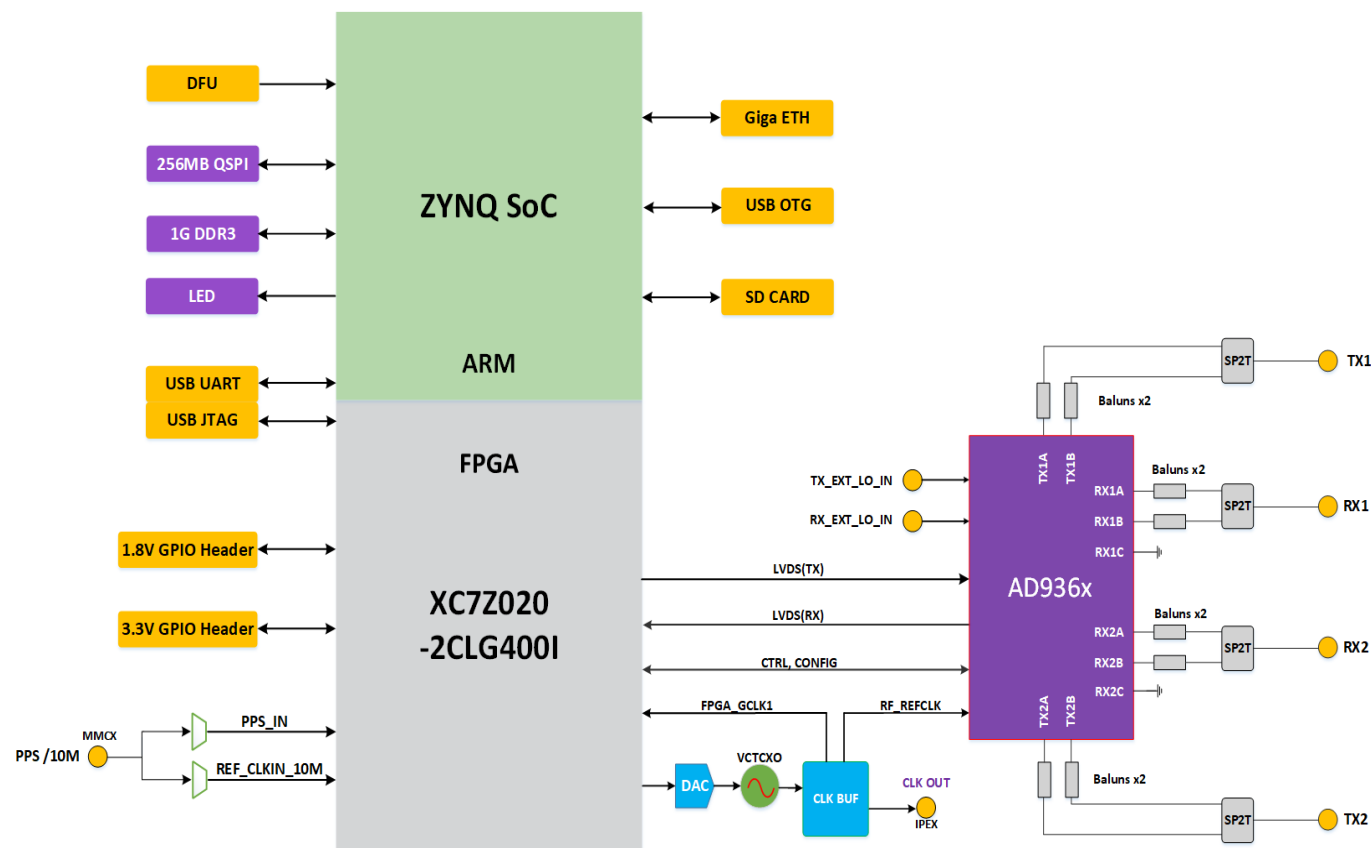
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ANTSDR E310 Block Diagram

REV	DATE	PAGES	DESCRIPTION
1.0	20/6/2023	All	Rev 1.0 Release



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13	CLOCK PPS&10M
14	USB JTAG&UART
15	Power
16	KEY&LED&GPIO
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Title			
ANTSDR E310			
Size	Document Number	Rev	
B	Block Diagram	1.0	
Date:	Tuesday, August 15, 2023	Sheet	1 of 16

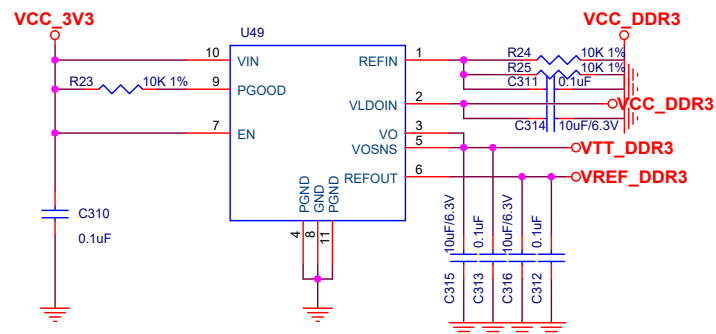
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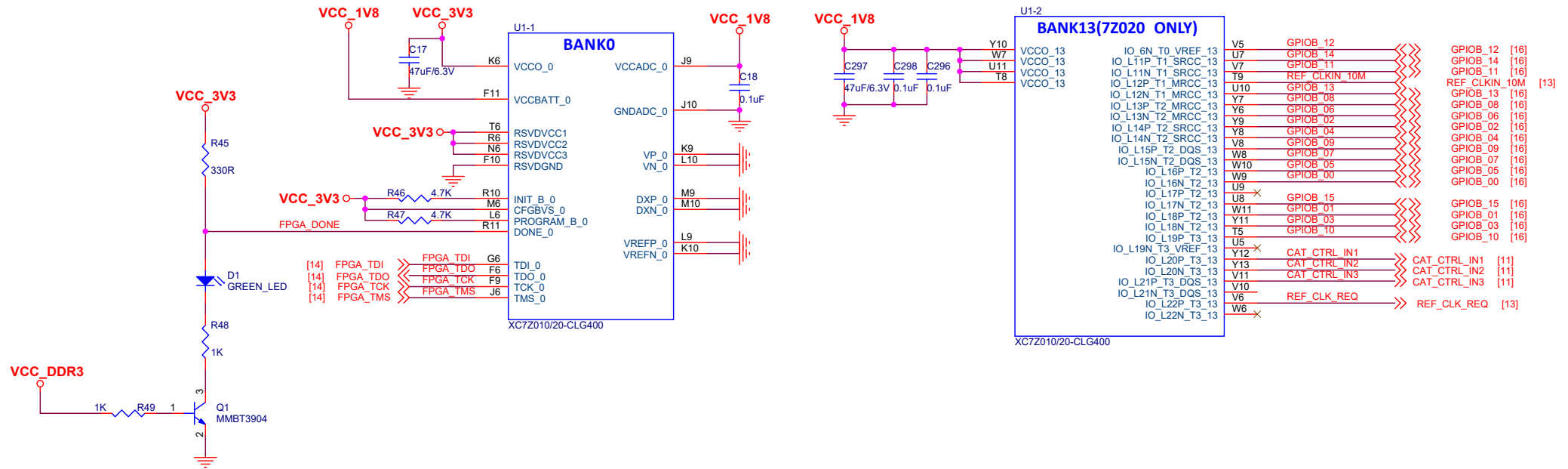
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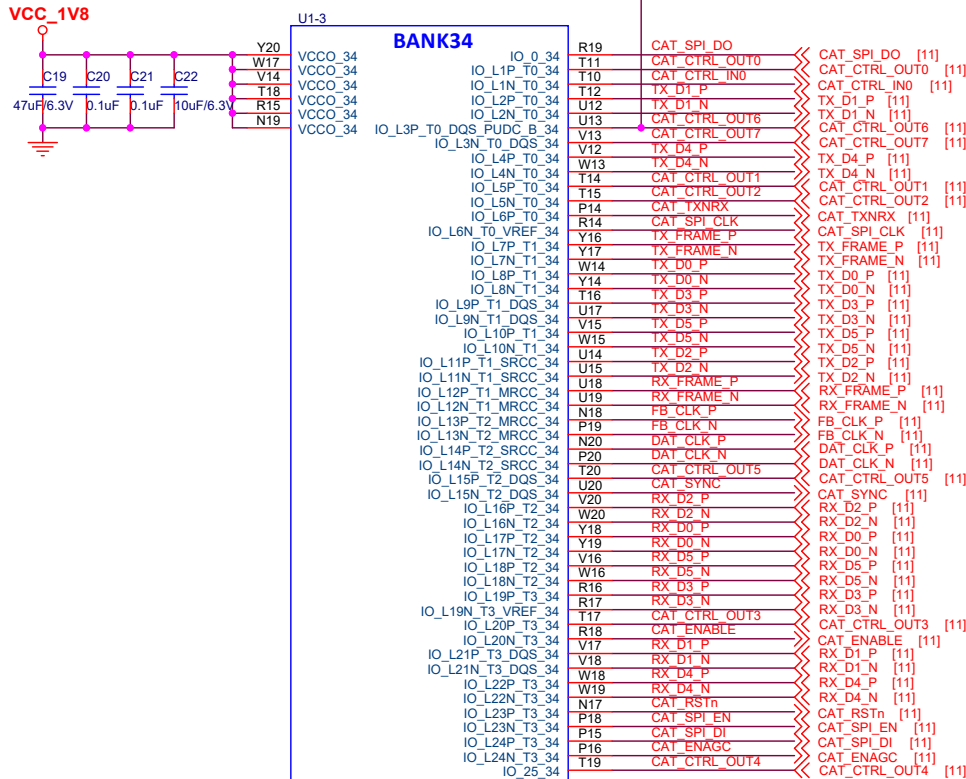
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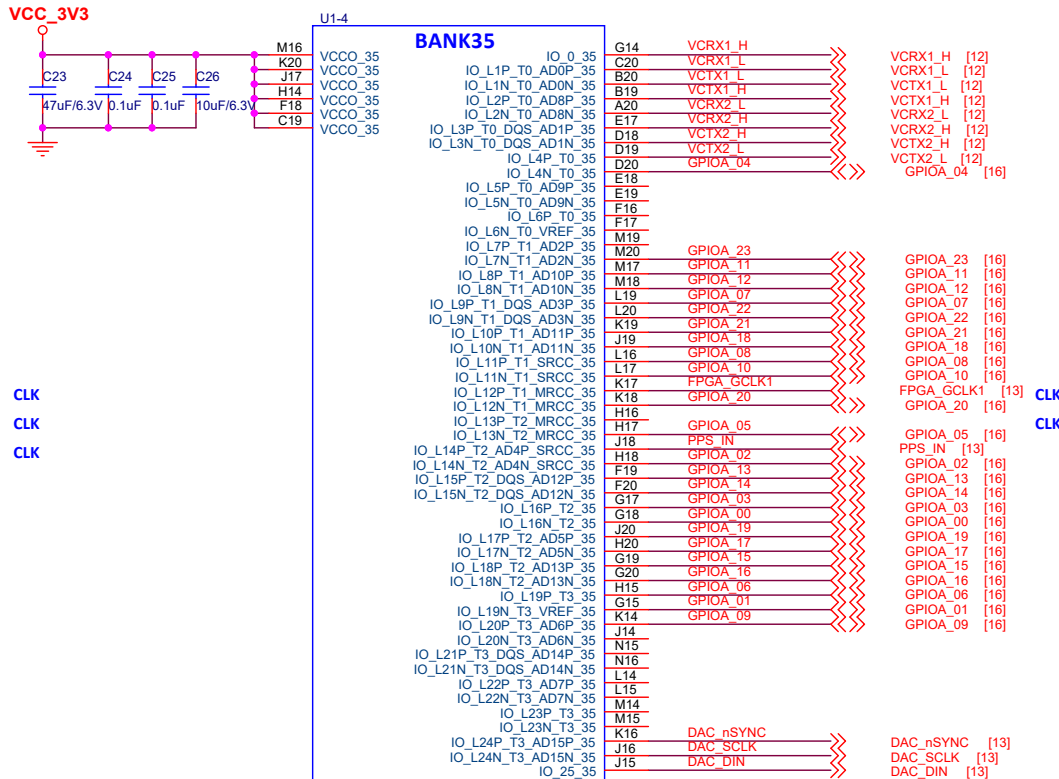
ZYNQ CONFIG



PL B34 B35

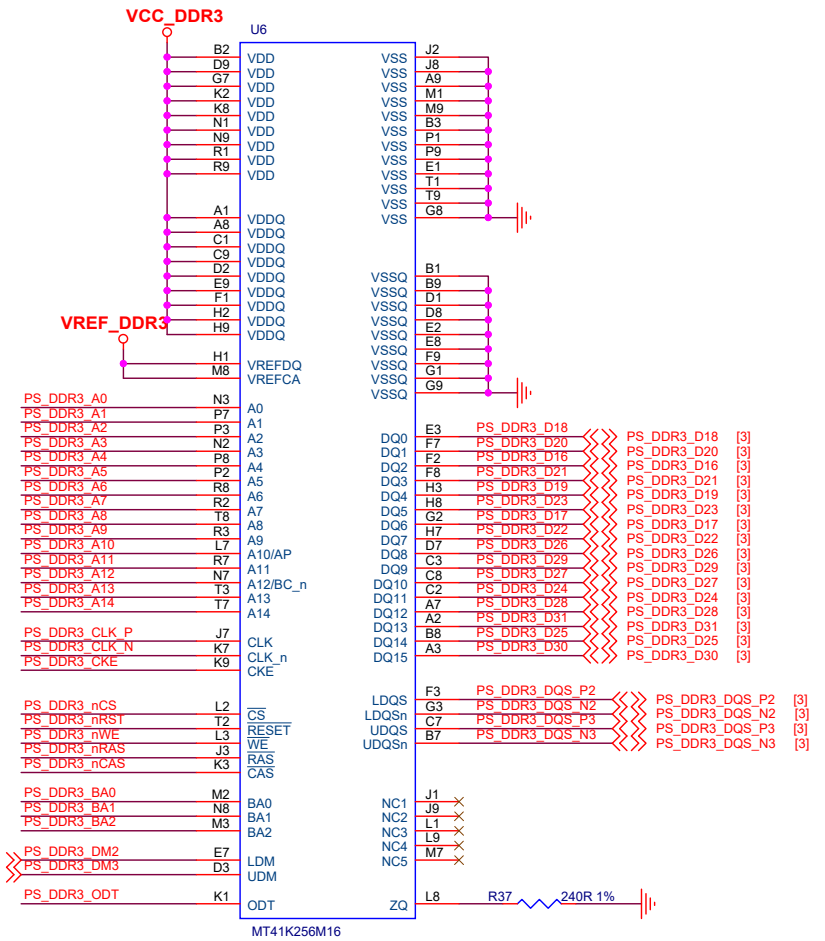


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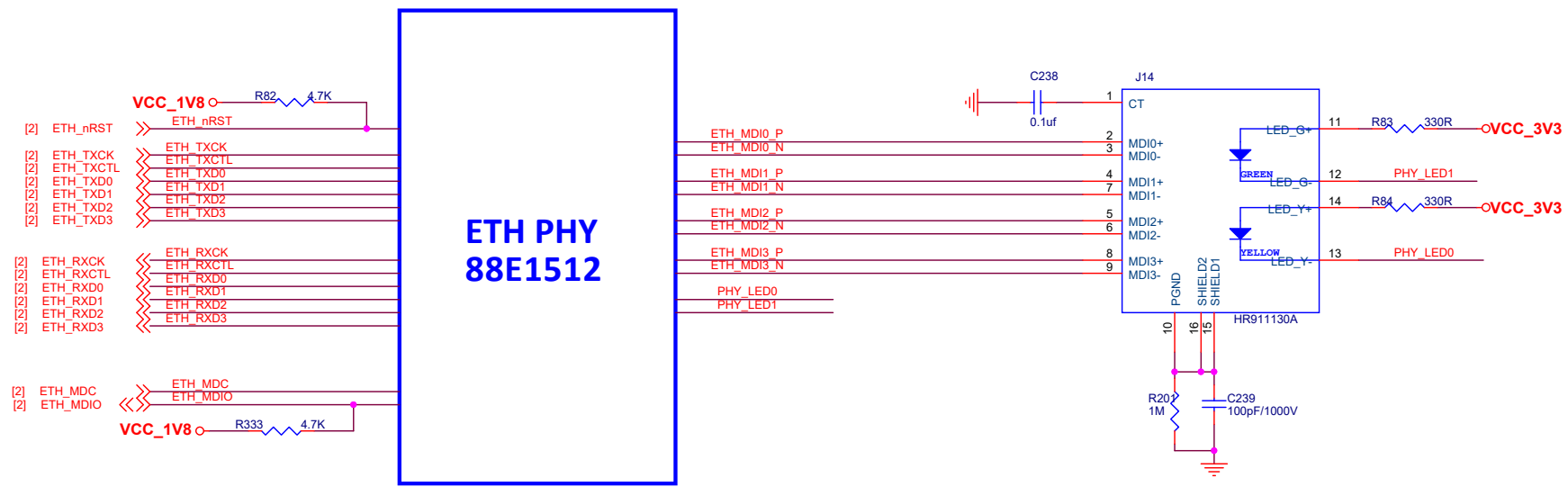


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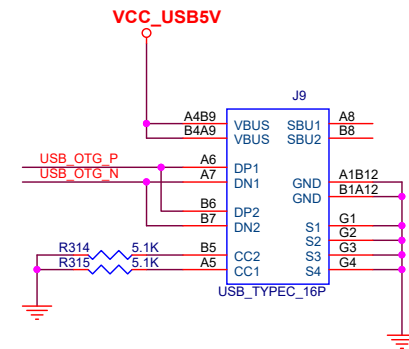
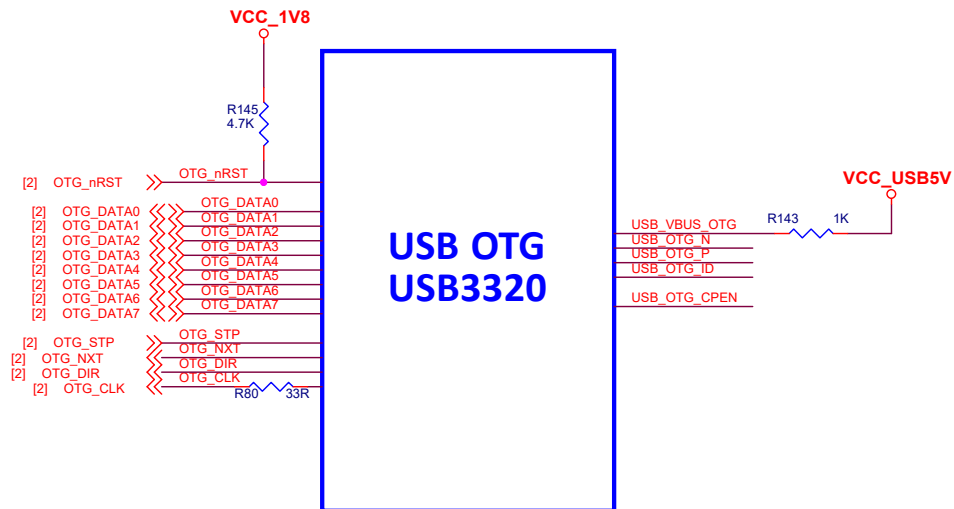
PS_DDR3



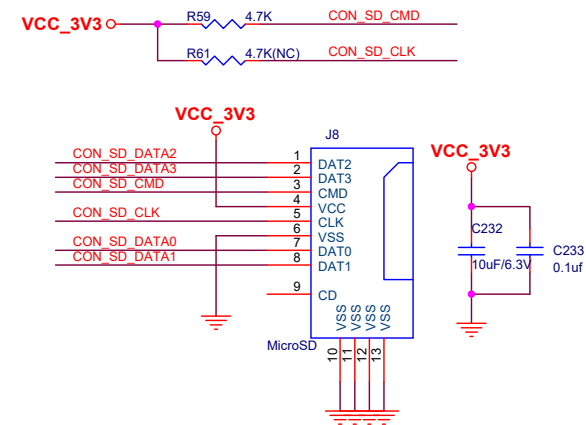
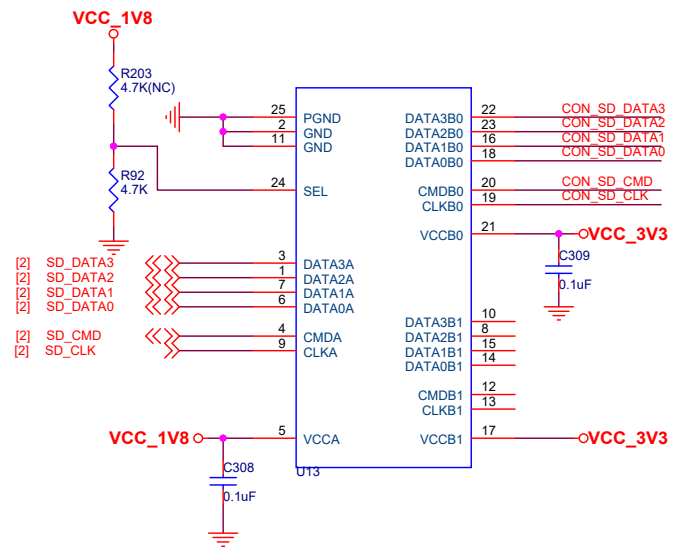
PS ETH



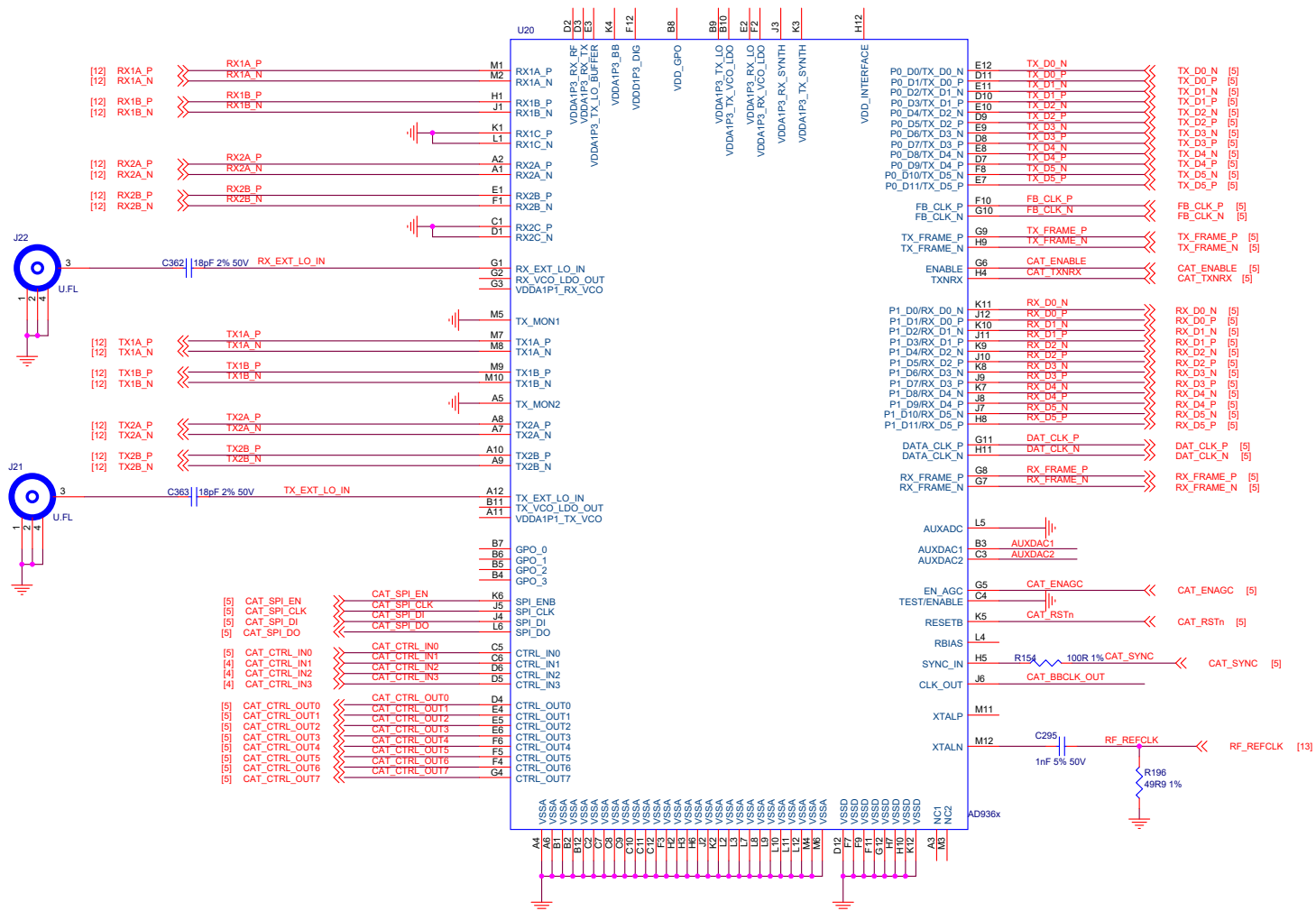
PS USB OTG



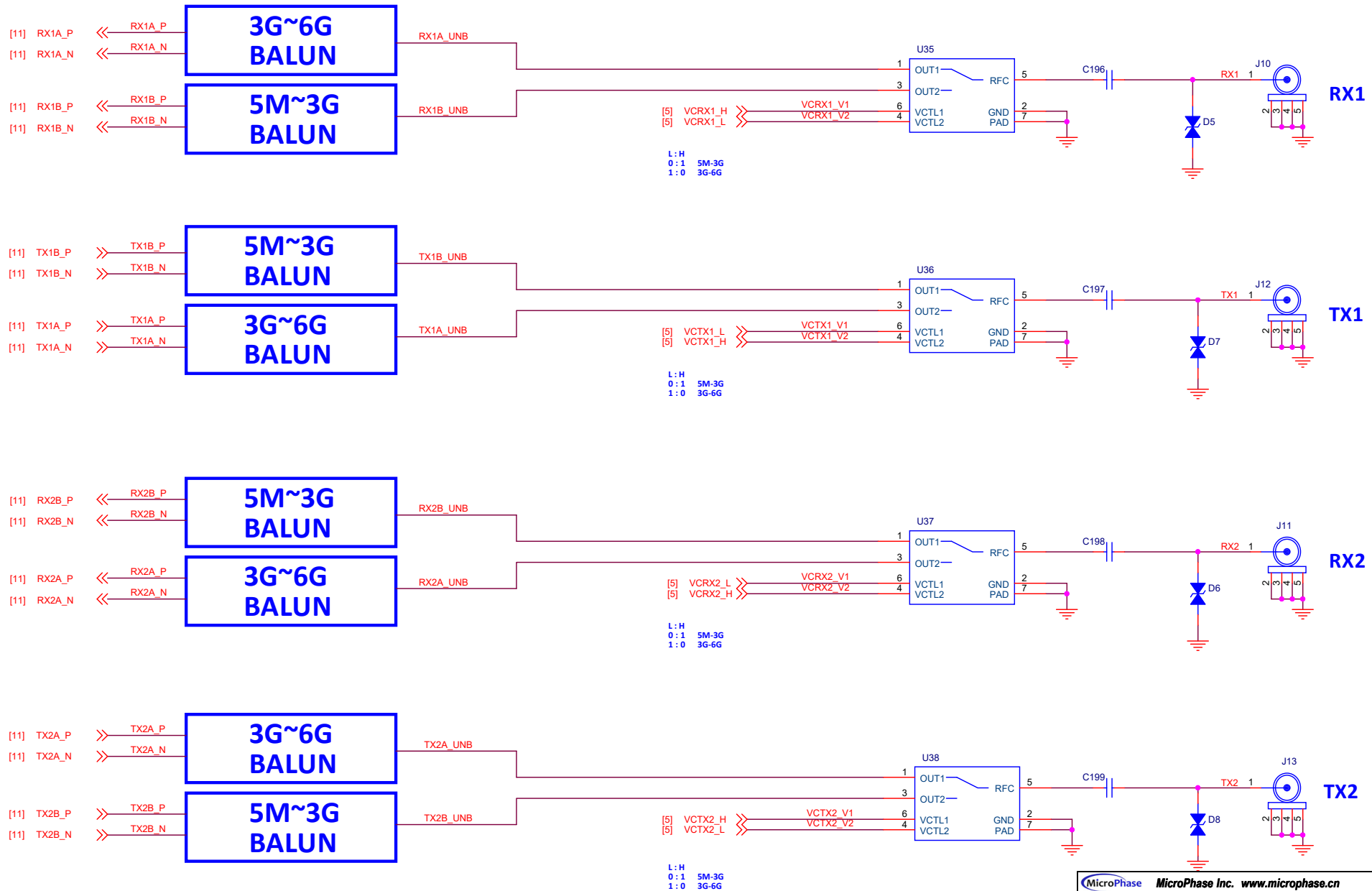
SD CARD

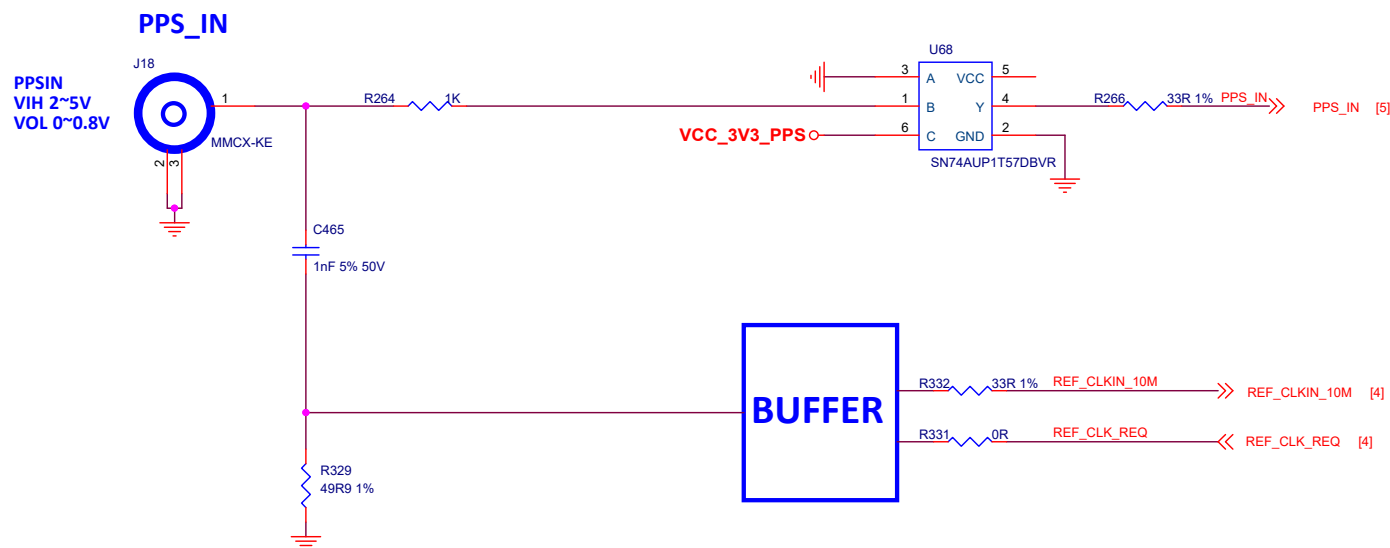
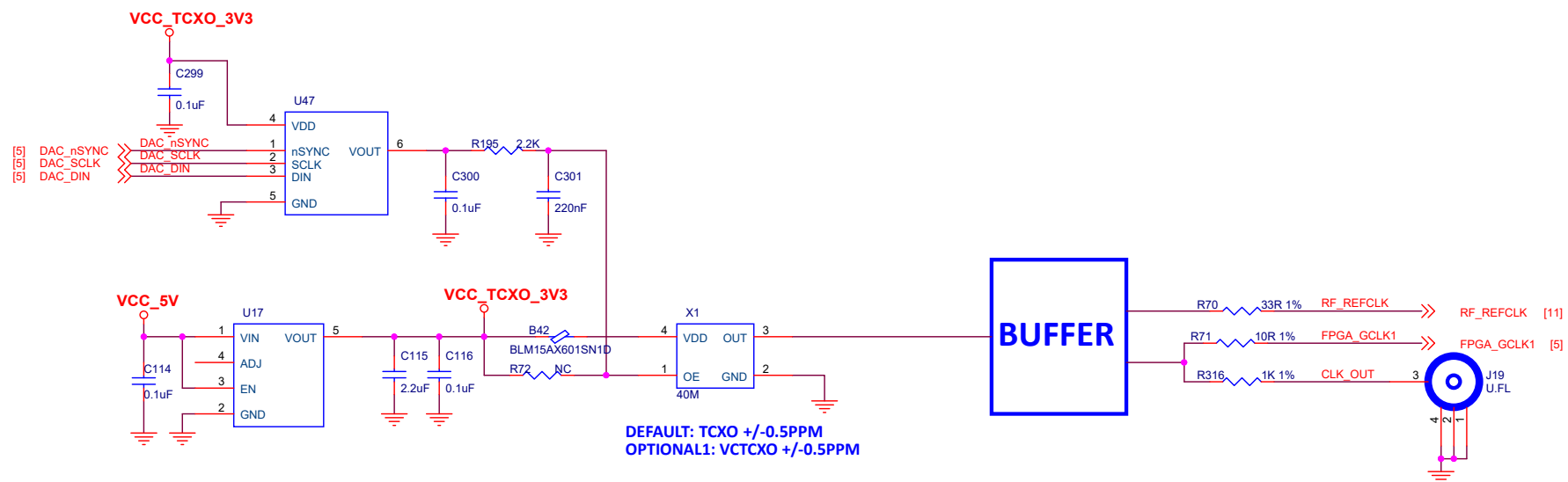


AD936x

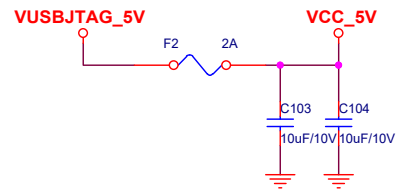


Use 2ppm crystal or lower
For GPS +/-0.5ppm





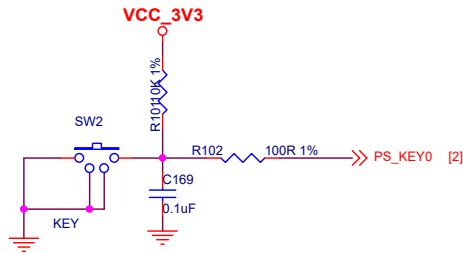




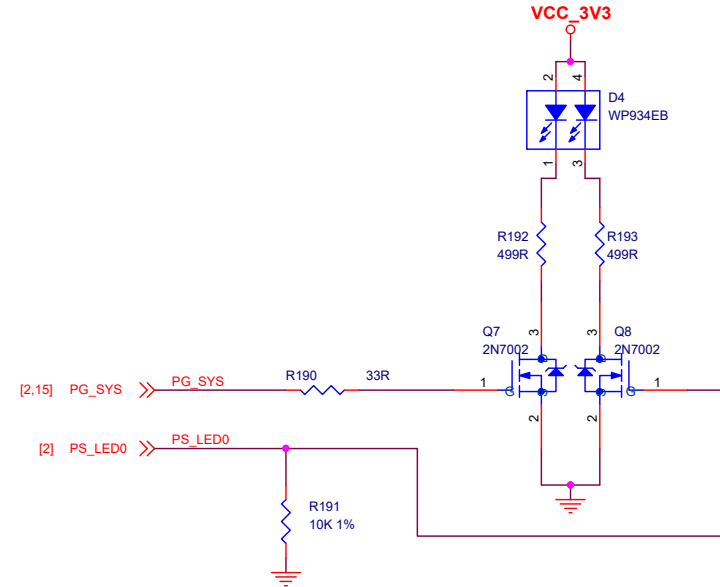
Power From USB Connector (J16)

POWER SYSTEM

Panel DFU



Panel LED



GPIO

