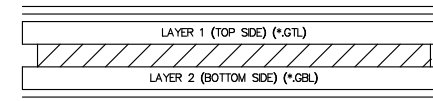


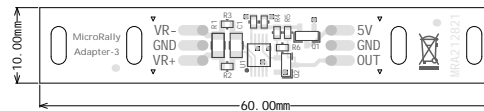
GERBER NOTES

Symbol	Count	Hole Size	Plated	Hole Type	Via/Pad
□	7	0.300mm	PTH	Round	Via
○	6	0.800mm	PTH	Round	Pad
▽	4	1.152mm	NPTH	Round	Pad
	17 Total				

1.0 OZ L1
1.50 mm Core – FR4
1.0 OZ L2



PASTE (TOP SIDE) (*GTP)
SILKSCREEN (TOP SIDE) (*GTO)
SOLDERMASK (TOP SIDE) (*GTS)
1.6 mm +/- 10%
LAYER DETAIL
2 LAYERS
SOLDERMASK (BOTTOM SIDE) (*GBS)



NOTES: (UNLESS OTHERWISE SPECIFIED)

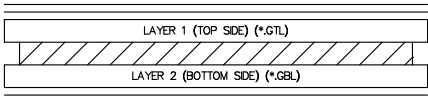
- THIS IS 2 LAYER BOARD
- MATERIAL: FR4, TG 150 DEGREE C MIN
- FR4 DIELECTRIC CONSTANT NOT SPECIFIED
- FINISHED BOARD THICKNESS TO BE 1.60MM +/- 10%
- TRACE WIDTHS IN ARTWORK ARE FINISHED SIZES
- SEE FILM FOR LAYER SEQUENCE AND COPPER THICKNESSES (SHOWN BEFORE PLATING)
- MIN TRACE/SPACE 0.25/0.20MM
- SEE DRILL CHART FOR FINISHED HOLE SIZES
- MIN DRILL 0.30MM
- HOLE TOLERANCE IS +/-0.3MIL UNLESS OTHERWISE SPECIFIED
HOLE COPPER THICKNESS MIN 0.7MIL
SLOT TOLERANCE +/-0.1MM
BORDER OUTLINE TOLERANCE +/-0.15MM
- SURFACE PLATING: HASL, Pb FREE
- SOLDERMASK: LPI, BOTH SIDES. COLOR GREEN
- SILKSCREEN: TOP SIDE. COLOR WHITE
- ALL BOARDS MUST BE ELECTRICALLY TESTED FOR ISOLATION (SHORTS) AND CONTINUITY (OPENS)

Project: MR Adapter-3	
Author: Andis Jargans	Revision: 1
Date: 13.07.2021	Size: A4
File: MR_Adapter-3_r1.PcbDoc	MicroRally

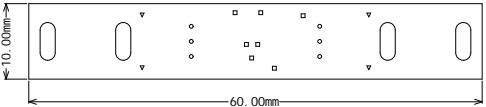
GERBER NOTES

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□	7	0.300mm	PTH	Round	Via
○	6	0.800mm	PTH	Round	Pad
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	17 Total				

1.0 OZ L1
1.50 mm Core – FR4
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PASTE (TOP SIDE) (*GTP)
SILKSCREEN (TOP SIDE) (*GTO)
SOLDERMASK (TOP SIDE) (*GTS)
1.6 mm +/- 10%
LAYER DETAIL
2 LAYERS
SOLDERMASK (BOTTOM SIDE) (*GBS)

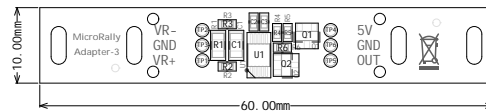


NOTES: (UNLESS OTHERWISE SPECIFIED)

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- MATERIAL: FR4, TG 150 DEGREE C MIN
- FR4 DIELECTRIC CONSTANT NOT SPECIFIED
- FINISHED BOARD THICKNESS TO BE 1.60MM +/- 10%
- TRACE WIDTHS IN ARTWORK ARE FINISHED SIZES
- SEE FILM FOR LAYER SEQUENCE AND COPPER THICKNESSES (SHOWN BEFORE PLATING)
- MIN TRACE/SPACE 0.25/0.20MM
- SEE DRILL CHART FOR FINISHED HOLE SIZES
- MIN DRILL 0.30MM
- HOLE TOLERANCE IS +/- 0.03MM UNLESS OTHERWISE SPECIFIED
HOLE COPPER THICKNESS MIN 0.075MM
SLOT TOLERANCE +/- 0.1MM
BORDER OUTLINE TOLERANCE +/- 0.15MM
- SURFACE PLATING: HASL, Pb FREE
- SOLDERMASK: LPI, BOTH SIDES. COLOR GREEN
- SILKSCREEN: TOP SIDE. COLOR WHITE
- ALL BOARDS MUST BE ELECTRICALLY TESTED FOR ISOLATION (SHORTS) AND CONTINUITY (OPENS)

Project: MR Adapter-3	
Author: Andis Jargans	Revision: 1
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File: MR_Adapter-3_r1.PcbDoc	MicroRally

ASSEMBLY NOTES



Optional VR load resistor:

Load R1 to add load resistance for VR sensor.
It can be useful if VR gives too high voltage.

Input filter:

Use R2, R3 and C1 to adjust VR input filter.

Direct IC output:

Load R6 and Not load Q2 to use direct output from U1.
U1 output is still open-drain, but max voltage is 5V.

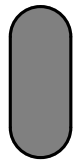
High voltage Open-drain output:

Load Q2, and not load R5 and R6 to use Q2 as open drain output.
Q2 can be connected to much higher voltage (max 60V)
If Q2 output is used, then output is inverted relative to U1 output.

5V Square wave output:

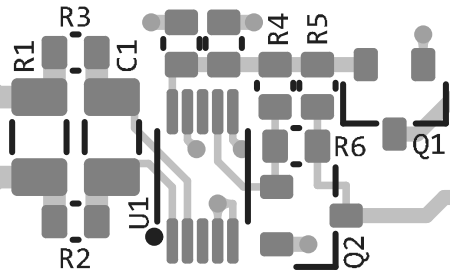
Load R4 or R5 to add 5V pull-up to output.
This will create 5V/0V square wave signal.

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**MicroRally
Adapter-3**

VR -
GND
VR +



5V
GND
OUT



MRA212821

