G55 xPro - Pins (I2S breakout)

Friday, October 05, 2018 3:15 PM

PIN MANAGER



G55 xPro - Pins (I2S) - Spreadsheet

NAME	PORT	PIN	EXT-PIN	Notes		
NAME	VDDIO		EXI-PIN	Notes		
	NRST	1				
	PB12	3				
I2SMCK0	PA04	4	I2C Conn-6(TWCK0)	12SC0 MCLK, 12C0 CK		
12SD00	PA03	5	I2C Conn-7(TWD0)	12SC0 DO, 12C0 D		
12SCK0	PA00	6	12C COIIII-7(1 WD0)	12SC0 BCLK		
12SWS0	PA01	7	EXT3-7(TIOB0-PWM+), I2C			
1254430	1701	,	Conn-2(NCHG, TIOB0)	123CO ENCK		
	PA05	8	Comi Z(Nerio, Nobo)			
	VDDCORE	9		VBUS_DET (USB)		
	TEST	10		USB_VBUS_SENSE: GPIO IN PULL-DWN		
	PA07	11				
	PA08	12				
	GND	13				
	PB15	14	EXT3-6(GPIO)			
	PB14	15	EXT4-9			
	PA31	16				
LED	PA06	17		LED_AL - Out - Low		
	PA16	18				
	PA31	19	EXT4-5			
	PA29	20				
	PA28	21				
	PA15	22	EXT3-(GPIO,WKUP13)			
I2SDO1	PA23	23	EXT1-7(TIOA1)	I2SC1 DO		
UHP_DP, I2SDI1	PA22	24		Target USB, I2SC1 DI		
UHP_DM	PA21	25		Target USB		
	VDDUSB	26				
	VDDIO	27				
	ADVREF	28				
	GND	29				
	VDDOUT	30				
	VDDIO	31				
	VDDIO	32	EVE4 2/4 DO)			
neva .	PA17	33	EXT1-3(AD0)	Incom the later to		
PCK2	PA18	34	EXT1-4(AD01)	I2SCO MCLK		
I2SCK1 I2SWS1	PA19 PA20	35 36	EXT3-3(AD2) EXT3-4(AD3)	I2SC1 BCLK		
123W31	PB00	37	EX13-4(AD3)	I2SC1 LRCK		
	PB01	38				
	PB02	39				
	PB03	40	EXT3-9(WKUP13, IRQ/GPIO)			
	1 1003	40	EX13-3(WKOF13, INQ/GFIO)			
	PA14	41				
	PA13	42				
	PA12	43				
	PA11	44				
	VDDCORE	45				
	PB10	46	EXT1,EXT4-11 (TWD4)	EDBG		
	PB11	47	EXT1,EXT4-12(TWCK4)	EDBG		
	PA10	48	. (
	PA09	49				
	PB05	50				
	PA27	51				
I2SMCK1	PA26	52	EXT1-(GPIO UARTO RTS)	I2SC1 MCLK		
	GND	53				
	PB06	54				
	PB07	55				
	PA25	56				
	PB13	57	EXT3-9(GPIO, PWM-)			
	PA24	58				
	PB08	59	EXT3- 11(TWD6)	TW6_SDA (I2C6_DA)		
	PB09	60	EXT3-12(TWCK6)	TW6_CK (I2C6_CK)		

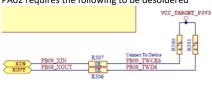
LDTO	31	LATS-S(GFIO, FVVIVI-)				
PA24	58					
PB08	59	EXT3- 11(TWD6)	TW6_SDA (I2C6_DA)			
PB09	60	EXT3-12(TWCK6)	TW6_CK (I2C6_CK)			
PA02	61		SWITCH_AL - In - Low			
PB04	62		GPIO - In - PullDown			
JTAGSEL	63					
PB09	64		VBUS_DET (USB)			
	PA24 PB08 PB09 PA02 PB04 JTAGSEL	PA24 58 PB08 59 PB09 60 PA02 61 PB04 62 JTAGSEL 63	PA24 58 PB08 59 EXT3- 11(TWD6) PB09 60 EXT3-12(TWCK6) PA02 61 PB04 62 JTAGSEL 63	PA24 58 FB08 FB08 FB08 FB09 F	PA24 58 FB08 FB08 FB08 FB09 F	PA24 58 S9 EXT3- 11(TWD6) TW6_SDA (I2C6_DA) SMITCH_AL - In - Low TW6_CK (I2C6_CK) SWITCH_AL - In - PullDown SWITCH_AL - In - PullDown SMITCH_AL - PULLDOWN SMITCH_AL - PULLDOWN SMITCH_AL - PULLDOWN

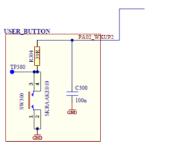
X32 AK4954 SIGNALS - I2SCO/I2C6

SIGNAL	Connector/Pi n	G55 Signal	G55 Port(Pin)	EXT - Pin	CONFLICTS
GND	J1-1	GND	-	EXT3-1	-
GND	J1-2	GND	-	EXT3-2	-
	J1-3				
	J1-4				
	J1-5				
	J1-6				
I2C SCL1	J1-7	TWCK6	PB09(60)	EXT3-12	XIN (NOTE 2)
STBY/RS T	J1-8	GPIO	PA26()	EXT1-5	I
I2C SDA1	J1-9	TWD6	PB08(59)	EXT3-11	XOUT (NOTE 2)
I2S LRCL	J1-10	12SWS	PA01(7)	12CC-2	HOST_I2C (TIOBO-NCHG) (NOTE 1)
I2S SDI	J1-11	12SDI0	PA02(61)	Solder to R304	SWITCH (NOTE 3)
I2S BCLK	J1-12	I2SCK1	PA00	EXT3-7	PWM+ (unused)
I2S SDO	J1-13	I2SDO	PA03(5)	12CC-7	HOST_I2C (I2C_SDA) (NOTE 1)
I2S MCLK	J1-14	I2SMCK0	PA04(4)	12CC-6	HOST_I2C (I2C_SCL) (NOTE 1)
	J1-15				
	J1-16				
+9V	J1-17	-	-	-	-
+3.3V	J1-18		VCCP3V3	EXT1-20	-
+9V	J1-19	-	-	-	1
+5V_PD	J1-20	-	-	-	-

• NOTE(s):

- I2CC is the connector for the high speed HOST_I2C, which is unused.
 PA02 requires the following to be desoldered





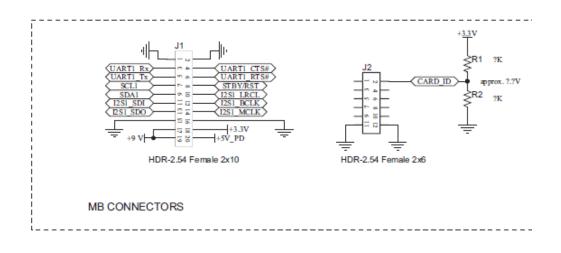
3. The Switch is required to mute and control volume.

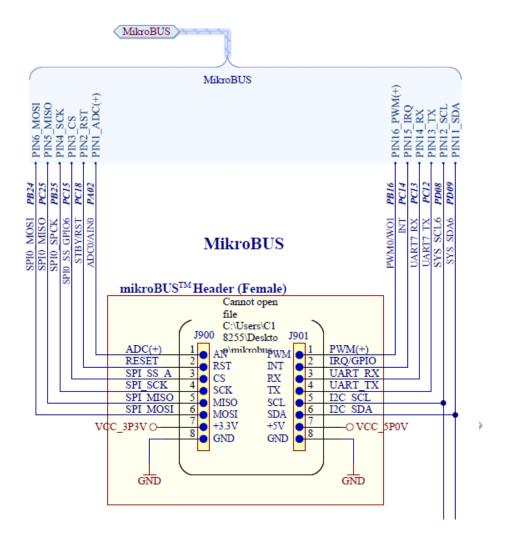
TEST SIGNALS (OUT)

J506-1 - TEST1 - PA02 (93) TEST2 - PC19 (117)

Signal	Connector/Pin	E70 Port	E70 Pin
TEST1			
TEST2			
TEST3			
TEST4			
TEST5			

Clock Manager

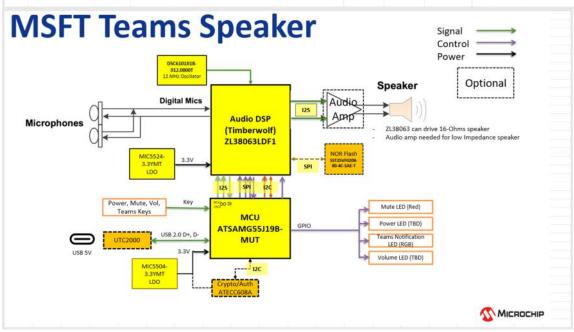




XΞ

USB Headset Audio Demo Feature Requests - 2020_CAL_...

USB Headset Audio Demo Feature List											
No.	Feature Name	Description	Req Type (Mandatory/ Optional)	Plan2Support (Yes/No)	Status (Done/Devlopin g/Progressing) *	Comment					
1	USB DEVICE auido	Can connect to a USB Host (such as a personal computer, Android phone,	Mandatory		g/Frogressing)	Only tested on Windows 10					
2	USB Audio 1.0/2.0/3.0 compliant	etc} UAC 3.0: https://www.usb.org/document-library/usb-audio-devices-rev-30-	Mandatory	Yes							
3	Auido sample rate	and-adopters-agreement 48K audio sample rates by default	Mandatory	Yes Yes		USB Audio 1.0					
		44.1K/32K/16K(configurable or build option); 16K has higher priority than 32 or 44.1.	Optional	No		16Khz					
4	Audio data width	16bit 24bit (configurable or build option)	Mandatory Optional	Yes							
5	Dual Channel audio stream	stereo audio: recording can be single channel, but playback MUST dual channel	Mandatory	Yes		Mono Record					
6	bi-directional audio stream at the same time	Conf-call/skype/teams etc need both audio in and auido out at the same time	Mandatory	Yes							
7	HID device with buttons	Audio control and sync: volume +/-, pause & play, Next & Back, etc	Mandatory	Yes		Using 4 button interface Volume+ (mute)/Volume-(mute)/Next/Prev(rewind)					
	Microsoft Teams ASP compability	Device shall full compatible with Teams Accessory Signaling Protocol(ASP). It use the HID profile to pass ASP commands. Refer to Teams specification for detail information.	Mandatory	No		Only standard HID Audio Control protocol, but could be extended later					
9	Microsoft Teams notification	Device should support Teams notification (Upcoming scheduled meeting, missing income call, etc)	Mandatory	No		Don't know about this					
	Firmware upgrade via USB HID	a.Anti-spoofing The image to be updated will be authenticated. The images without valid signature will be rejected. b.Silent update through Windows Update (WU) The driver and bundled firmware image are pushed to the hosts via WU automatically. There shall be no interaction with user to have the device updated. c.fail-safe The device shall support fail-safe mechanism, e.g., dual bank. It shall never brick the device in case the firmware update is interrupted by any reason. d.No service impacts When the device is being updated, it shall not interrupt the function/service.	Optional	No							
11	USB composite device support with build option	USB audio class only(best to have individual build option for below options): - USB speaker only - USB microphone only - USB microphone + speaker USB audio class + HID	Mandatory	Most Yes		Did not plan to add HID to USB mic, or implement on G55					
		USB audio class + HID USB audio class + CDC	Optional	No		CDC is used for codec/dsp tuning/configurations/fw upgrading, etc					
12	Q-Touch buttions	SW based Q-Touch for buttonsn	Optional	No		and in control and turning configurations in approximation.					



USB Audio SSOs - Requirements

Friday, July 31, 2020 11:52 AM



USB Audio(H3) SSOs-4

USB Aud	dio													
Region	City	Customer	Арі	plication	First CPN		Alternative CPN	EAU (K/Yr •	Funnel S	tage		Status	DFAE	- 4
S.China	Shenzhen	A&D (Tymphany)	conference speaker	for MS-Teams	SAMG55		SAMD21		3 - system defini	tion	need USB Audio and HID			Simon Hu
TW	Taibei	Microsoft/Foxlin	teams conference s	peakers	SAMG55/D51			300	3 - system defini	tion	USB audio lib and bootloader	,		
S.China	Shenzhen	A&D (Philips)	Conference Call Spe	eaker	SAMG55		SAMD21	20	3 - system defini	tion	Need USB-Audio lib.			Simon Hu
S.China	Shenzhen	Grandsun	conference speaker	for MS-Teams	SAMD51/E5x		SAMG55	100	3 - system defini	tion	need USB Audio and HID			Simon Hu
Taiwan	Taipei	Jazz Hipster	Conference Call Sys	tem	PIC32MX230F128		SAM deivice could be accepted	20	1 - define needs		Need H3 USB Audio, I2S librar	'y	Burn Keiffer	
Taiwan	Taipei	Meiloon	BT Speaker System		PIC32MX2, PIC32MX4				3 - system defini		Timberwolf.	ete new platfrom such as with BT,	WFE Spring	
Japan	Tokyo	Ricoh	Conference Call Sys	tem	ZL38090/SAMG55			30	3 - system defini	tion	A proposal with SAMG55+ZL3			
S.China	Shenzhen	Tenveo	Conference Call Sys	tem	ZL38090/SAMG55			60	1 - define needs		ZL38090 for USB audio I/F and	sst it due to two chips solition is not cost effective. We need 190 for USB audio I/F and ZL38063 for voice processing. Jetitot use one chip for USB and voice processor. And it nee		Diffin
Name	:	Disty nam	e Role	Region	Covered are	e Wh	at he/she good at	? F	late	recom	mended tech di	rection/trainings		
Hans	Yao	Infortech	FAE	E. China	E.China						MPU 板			
JW C	nen	Burnon	FAE	E. China	E.China	MP	U Linux				MPU 板			
Eddie	peng	Weikeng	FAE	E. China	E.China						MCUboard			
Wool	f Zhou	Promaste	r FAE	N. China	N. China	MC	U32 (PIC32+MCT3	2)			MCUboard			
Viviar	n Xiao	Promaste	r FAE	S. China	ShenZhen						MCUboard			
Xuelia	ang Wu	CEAC	FAE	S. China	S.China	MP	U + MCU32		6		MCUboard	, MPU, Linux, Grapl	nics, drive	er
Perry	Peng	CECP	FAE	W. China	WH, ZZ	MP	U + MCU32		7		MCUboard			
Oliver	Lin	WFE	DFAE	Taiwan	Taiwan, Sou	PIC	32				MPU 板			