G55 xPro

Tuesday, August 11, 2020

4:29 PM

Machine generated alternative text:
VCC TARGET P3V3 
R320 
PB07 SWCLK 
PB06 SWD10 R319 
PB05 TRACESWO 
VDDCORE PI V2 
VCc 
P3V3 
P3V3 
VDDCORE Plu 
VCC_TARGET p3V3 
PB10 TiVD4 
PBII TWCK4 
TP301 
PB09 XLN 
PA06 GPIO 
PAIS GPIO 
PA26 GPIO 
PA29 GPIO 
PA30 GPIO 
12 
TST 10 
VCC 
XOUT32/PA08 
XIN32/PA07 
TEST 
RESETN 
PAD o 
PAOI 
PA02 
PA03 
PAD 4 
PA05 
PAD 6 
PA09 
ggg 
GND TARGET RESE 
PAOO TIOAO 
PAOI 
TIOBO 
PA02 
PA03 TIVD3 
PA04 TWCK3 
PA05 SPIS NPCSI 
PA09 VART RXDO 
PAIO CART TXDO 
PAIT 
SPF NPcso 
PA12 
SPF MISO 
PA13 
SPF MOSI 
PA14 
SP15 CLK 
PA16 WKUP7 
PA17 ADO 
PA18 ADI 
PA19 
AD2 
PA20 AD3 
TARGET USB N 
TARGET USB 
PA23 TIOAI 
PAN ',VKUPII 
PA25 GPIO 
PA27 CART? R-XD 
PA2S CART? TXD 
PA31 
GPIO 
4 
8 
PBOO 
PBOI 
PB02 
PB03 
PB08 
PB09 
PEI o 
PBI 
PB12 
PEI 3 
FBI 4 
PEI 5 
JTAGSEL 
TCK/ SWCLK/PBO 
TMs/ SWDIO/PB06 
TDO/TRACESWo/pB05 
TD1/PB04 
7 
59 
46 
7 
7 
4 
63 
5 
62 
5 
2 
PAI 1 
PA12 
PAI 3 
PA14 
PAI 5 
PAI 6 
PAI 7 
PA18 
PAI 9 
PA20 
PA21 
PA22 
PA23 
PA24 
PA25 
PA26 
PA27 
PA28 
PA29 
PA30 
PA31 
PBOO CART TXD6 
PBOI CART &XD6 
PB02 AD6 
PB03 
PB08 xoc-r 
PBIO TWL)4 
PBII 
TWCK4 
PB12 
PB13 USART6 CLK 
PB14 
GPIO 
PB15 
GPIO 
JTAGSEL 
PB07 SWCLK 
PB06 
PB05 TRACESWO 
PB04 GPIO 
VCC TARGET P3V3 

I2SC

Machine generated alternative text:
Table 33-2. 
Instance 
12SCO 
12SCO 
12SCO 
12SCO 
12SCO 
12SCO 
12SCO 
12SC1 
12SC1 
12SC1 
12SC1 
12SC1 
12SC1 
12SC1 
Lines 
Signal 
12SCO CK 
12SCO DIO 
12SCO 
DOO 
12SCO 
DOO 
12SCO MCK 
12SCO MCK 
12SCO ws 
12SC1 
CK 
12SC1 
DIO 
12SC1 
DOO 
12SC1 
DOO 
12SC1 
MCK 
12SC1 
MCK 
12SC1 
ws 
I/O Line 
PAO 
PA2 
PA3 
PA17 
PA4 
PA18 
PA19 
PA22 
PA23 
PA25 
PA24 
PA26 
PA20 
Peripheral 

I2C - TWI

Machine generated alternative text:
Table 32-3. 
Instance 
TWIO 
TWIO 
TWII 
TWII 
TW12 
TW12 
TW13 
TW13 
TW14 
TW14 
TW14 
TW14 
TW15 
TW15 
TW16 
TW16 
TW16 
TW16 
TW17 
TW17 
Lines 
Signal 
RXDO/SPIO MISO/TWCKO 
TXDO/SPIO MOSI/TWDO 
RXDI/SPII 
MISO/TWCKI 
TXDI/SPII 
MOSI/TWDI 
RXD2/SP12 MISO/TWCK2 
TXD2/SP12 MOSI/TWD2 
RXD3/SP13 MISO/TWCK3 
TXD3/SP13 MOSI/TWD3 
RXD4/SP14 MISO/TWCK4 
RXD4/SP14 MISO/TWCK4 
TXD4/SP14 MOSI/TWD4 
TXD4/SP14 MOSI/TWD4 
RXD5/SP15 MISO/TWCK5 
TXD5/SP15 MOSI/TWD5 
RXD6/SP16 MISO/TWCK6 
RXD6/SP16 MISO/TWCK6 
TXD6/SP16 MOSI/TWD6 
TXD6/SP16 MOSI/TWD6 
RXD7/SP17 MISO/TWCK7 
TXD7/SP17 MOSI/TWD7 
I/O Line 
PAIO 
PB2 
PB3 
PA5 
PA 6 
PA3 
PB9 
PBII 
PB8 
PBIO 
PA12 
PA13 
PBII 
PBO 
PBIO 
PA27 
PA28 
Peripheral 

FLEXCOM

Machine generated alternative text:
Table 29-1. 
I/O Line Description 
RXD 102 
SCK 103 
Name 
TXD 
CTS 
RTS 
USART/UART 
TXD 
RXD 
SCK 
CTS 
RTS 
Description 
SPI 
MOSI 
MISO 
SPCK 
NPCSO/NSS 
NPCSI 
TWI 
TWD 
TWCK 
SMBALERT 
Type 
I/O 
I/O 
I/O 
I/O 
O 
Note: 
101 
104 
105 
FLEXCOM3 only 

Machine generated alternative text:
Table 29-2. 
Function 
FLEXCOM Configuration 
Main Function 
TWI 
TWI 
IJART 
USART 
USART 
SPI 
Subfunction 
High-speed SMBUS 
High-speed Alternate Function 
High-speed Sleepwalking 
Sleepwalking 
IS07816 
IrDA 
LIN Mode 
SPI Mode 
2 Chip Selects 
FLEXO 
FLEXI 
FLEX2 
FLEX3 
FLEX4 
FLEX5 
FLEX6 
FLEX7 

Machine generated alternative text:
29.5.1 
29.5.2 
29.5.3 
I/O Lines 
The pins used for interfacing the FLEXCOM are multiplexed with the PIO lines. The programmer must first 
program the PIO controller to assign the desired FLEXCOM pins to their peripheral function. If I/O lines of the 
FLEXCOM are not used by the application, they can be used for other purposes by the PIO Controller. 
Power Management 
The peripheral clock is not continuously provided to the FLEXCOM. The programmer must first enable the 
FLEXCOM Clock in the Power Management Controller (PMC) before using the FLEXCOM. However, if the 
application does not require FLEXCOM operations, the FLEXCOM clock can be stopped and restarted later. 
In SleepWalking mode (asynchronous partial wakeup), the PMC must be configured to enable SleepWalking for 
the FL EXCOM in the Sleepwalking Enable Register (PMC SLPWK ER). The peripheral clock can be 
automatically provided to the FLEXCOM, depending on the instructions (requests) provided by the FLEXCOM to 
the PMC. 
Interrupt Sources 
The FLEXCOM interrupt line is connected on one of the internal sources of the interrupt controller. Using the 
FLEXCOM interrupt requires the interrupt controller to be programmed first. 