



Known Limitations/Issues

This document lists all the known limitations/issues related to this release.

WBZ451 Curiosity Board

1. WBZ451 Curiosity Boards with serial no: 599 and below may need an update to configuration stored on PKOB4
An update is required if user fails to program the board with MPLAB IDE returning – “Failed to get Device ID” message
Workaround: Follow Instructions mentioned in /Documentation/Updating WBZ451 Curiosity board Kit Info to work with MPLAB x IDE.pdf document of the package
2. Leakage on SWDCLK/PB8, additional 25 to 175 uA current consumption observed on certain boards. The reason for additional current consumption is wrong level translator was populated on these boards. This issue is not seen on every board and will only be seen on some boards.
Workaround: Remove resistor R53 to achieve expected current consumption
Note: upon removal of R53 resistor, external debugger will be required to program/debug the Development kit as R53 resistor is connected to SWDCLK/PB8 pin of the Soc

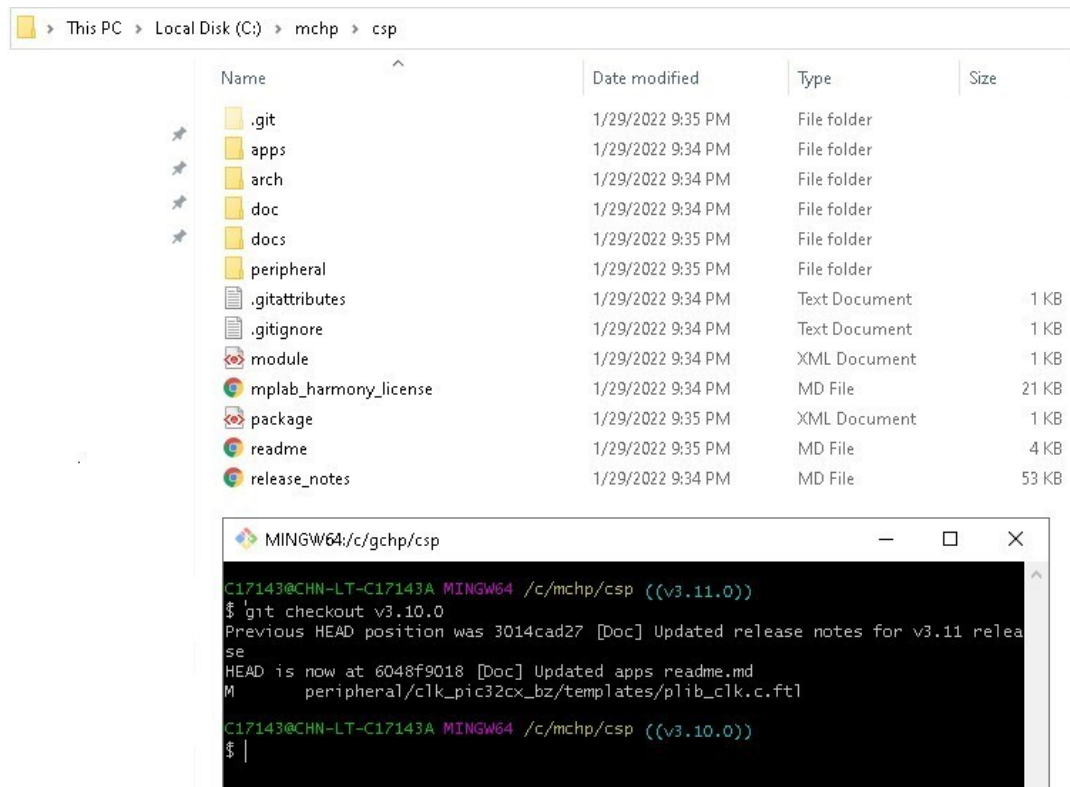
MPLAB Code Configurator (MCC)

3. **Content Manager:** Downgrading the versions of Harmony Components like csp, bsp, dev_packs etc. is not working within the Content Manager



Type to Search Globally...			
Component	Version	Status	
Device Supported Content Only ↑↓			
> Harmony Chip Support Package		⚙	
> Harmony Bootloader		⚙	
> Harmony Networking Stack and Solutions		⚙	
> Harmony USB solutions		⚙	
> Harmony Core		⚙	
> Harmony Motor Control solutions		⚙	
> Harmony Class B solutions		⚙	
> Harmony Audio solutions		⚙	
> Harmony Wireless solutions		⚙	
> Harmony Graphics solutions		⚙	
> Harmony Mbed OS Port		⚙	
> Harmony Tools		⚙	
bsp	3.11.0	Active Version	
> Harmony Amazon FreeRTOS solutions	✓ 3.11.0 - [local]		
> Harmony Micrium u	3.10.0 - [local]		
> Harmony reference material	3.9.0 - [local]		
> Harmony Cryptography solutions	3.8.2 - [local]		
ⓘ harmony-services	3.8.1 - [local]		
> Harmony Aerospace solutions	3.8.0 - [local]		
> Harmony Graphics (Aria) solutions	3.7.0 - [local]		

Workaround: User can checkout the individual component with version number mentioned in /Documentation/PIC32CX-BZWBZ45x SDK Setup.pdf



BLE Sleep/Standby Mode Limitations

- Unexpected disconnection might be observed if SOSC is chosen as a low power clock source in the following conditions
 - BLE central role.
 - The connection interval is smaller than 30ms.
- System cannot enter sleep mode if there is no BLE activity (such as advertisement or no connection), in other words when developing a low power application using BLE stack, application needs to be Transmitting/Receiving packets periodically by means of Adv/Scan/Connection to be able to enter "sleep" mode.
- 100 uA of additional current is measured in "sleep" mode because of a known calibration issue which will be resolved at final production release
- Peripherals cannot be enabled to "run in standby" when entering sleep mode, future releases will give user control to enable their choice of peripheral to run in standby

BLE & Zigbee Stack Support Limitation

- Flash Error Correcting Code (ECC) feature cannot be used with current BLE/Zigbee Stacks