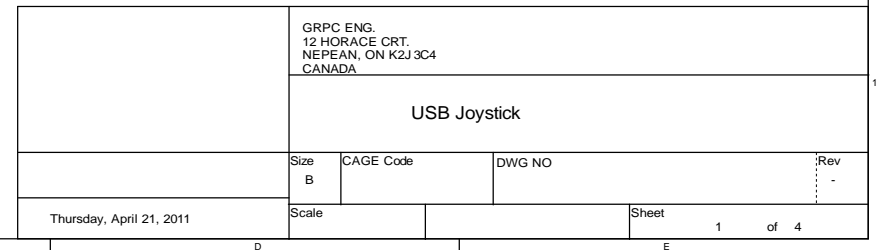
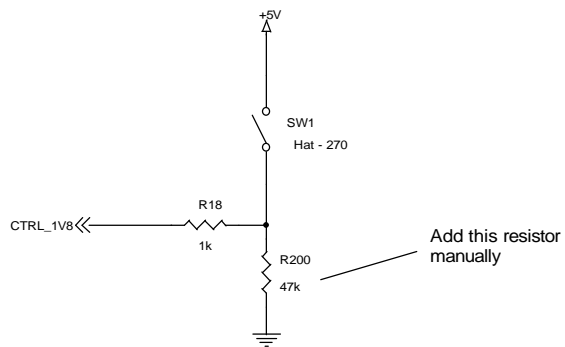
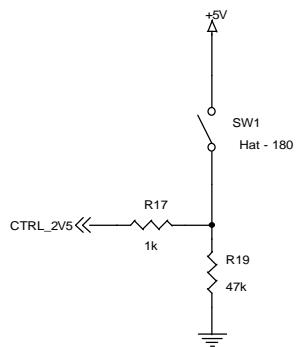
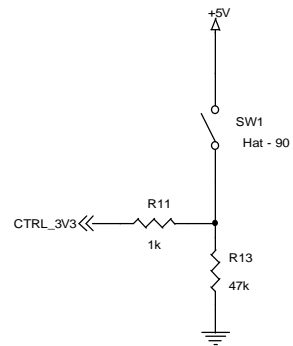
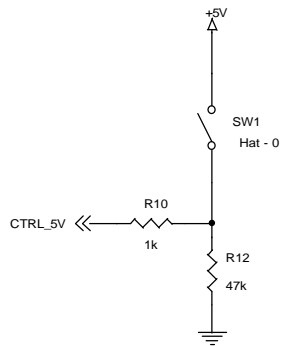
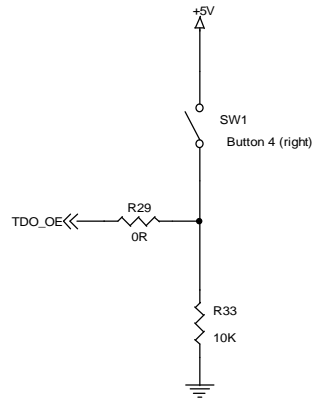
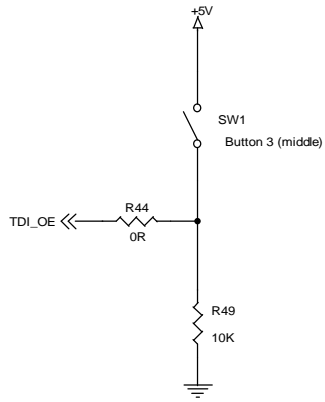
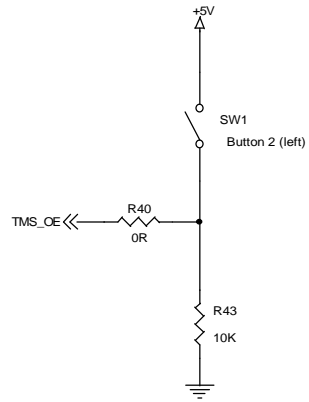
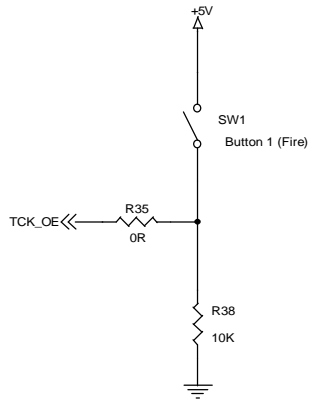


This design is based on the JTAG Programmer design. Some elements have been removed, but the reference designators have not been changed, in order to be able to reference the components on the JTAG Programmer PCB.





		GRPC ENG. 12 HORACE CRT. NEPEAN, ON K2J 3C4 CANADA		
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