ATMEL FUSE BIT WORKSHEET

DEFAULTS

7	6	5	4	3	2	1	0	
0	1	1	0	0	0	1	0	0x62

LOW FUSE BYTE

CKDIV8 / CKOUT / SUT1 / SUT0 / CKSEL3 / CKSEL2 / CKSEL1 / CKSEL0

7	6	5	4	3	2	1	0	
1	0	0	1	1	0	0	1	0x99

HIGH FUSE BYTE

OCDEN / JTAGEN / SPIEN / WDTON / EESAVE / BOOTSZ1 / BOOTSZ0 / BOOTRST

7	6	5	4	3	2	1	0	
1	1	1	1	1	1	1	1	0xFF

EXTENDED FUSE BYTE

N/A / N/A / N/A / N/A / N/A / BODLEVEL1 / BODLEVEL0 / RSTDISBL

PROJECT

7	6	5	4	3	2	1	0

CKDIV8 / CKOUT / SUT1 / SUT0 / CKSEL3 / CKSEL2 / CKSEL1 / CKSEL0

7	6	5	4	3	2	1	0

OCDEN / JTAGEN / SPIEN / WDTON / EESAVE / BOOTSZ1 / BOOTSZ0 / BOOTRST

7	6	5	4	3	2	1	0

N/A / N/A / N/A / N/A / N/A / BODLEVEL1 / BODLEVEL0 / RSTDISBL

NOTE: "1" = UNPROGRAMMED, "0" = PROGRAMMED

ATmega3290/6490

PROGRAM:_____

DATE:_____