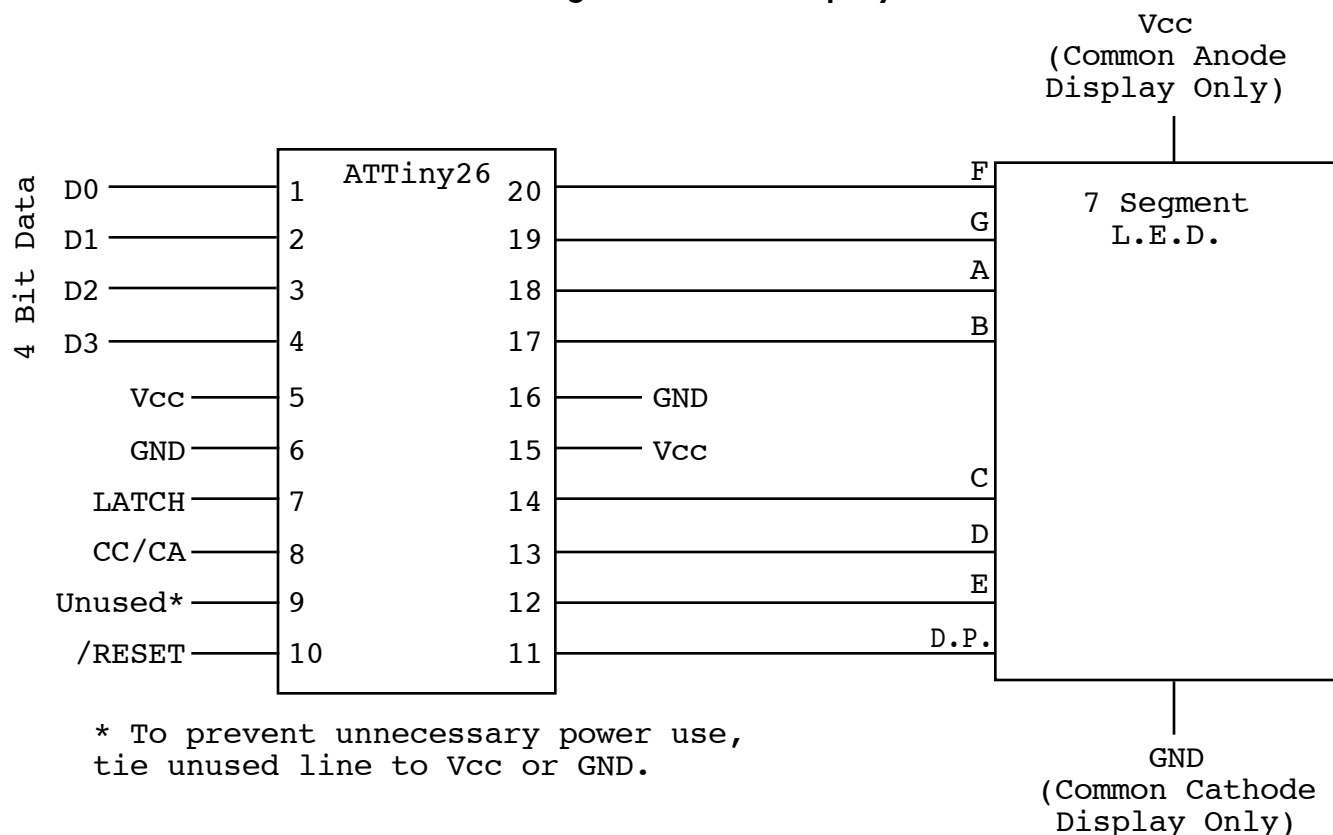


ATTiny26 Hexadecimal Display Decoder/Driver/Latch for 7 Segment L.E.D Displays



Input Signal Descriptions:

D0-D3: Latched Data Inputs. D0=LSb, D3=MSb.

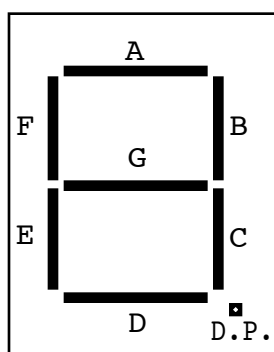
LATCH: When low, outputs follow inputs. When high, last data value obtained when low is retained on outputs.

CC/CA: Tie line to Vcc for common cathode displays (outputs are active high) or to GND for common anode displays (outputs active low.)

Unused: Tie this line to Vcc or BND to prevent noise and unnecessary power utilization of circuit.

/RESET: Tie high for most applications, can be used for display blanking if uC startup time is not a problem for the application.

Segment Designations



Display Patterns for Digits

