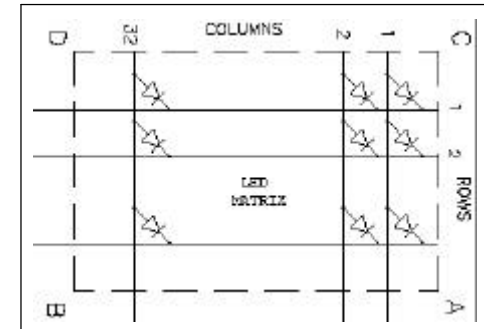


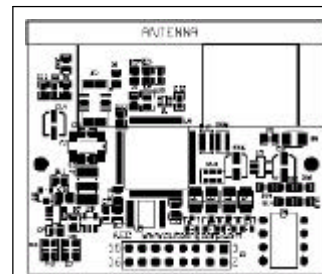
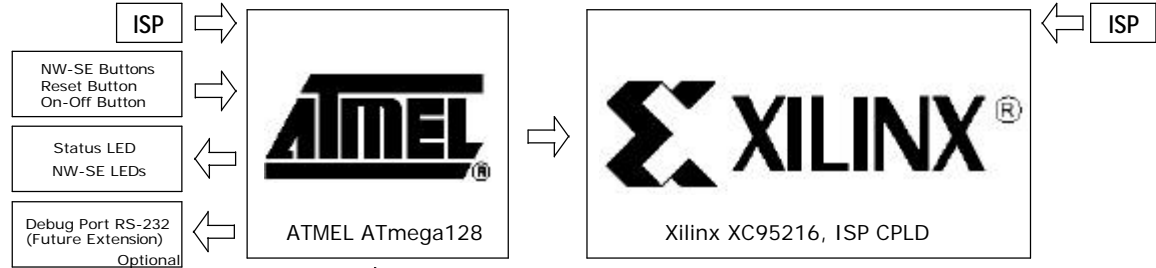
The Traffic Data Display Project

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Top Level Schematic, 001-TOPP.SCH	2
Custom Logic, 001-CPLD.SCH	3
CPU, 001-CPU.SCH	4
LED Matrix, 001-MTRX.SCH	5
LED Matrix, Cluster 1, 001-CLST1.SCH	6
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LED Matrix, Cluster 5, 001-CLST5.SCH	10
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LED Matrix, Cluster 7, 001-CLST7.SCH	12
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REV	SHEET	DESCRIPTION	Initial Date
A	ALL	Initial Release: Preliminary Block Diagram and Complete Schematics	03/02/2004



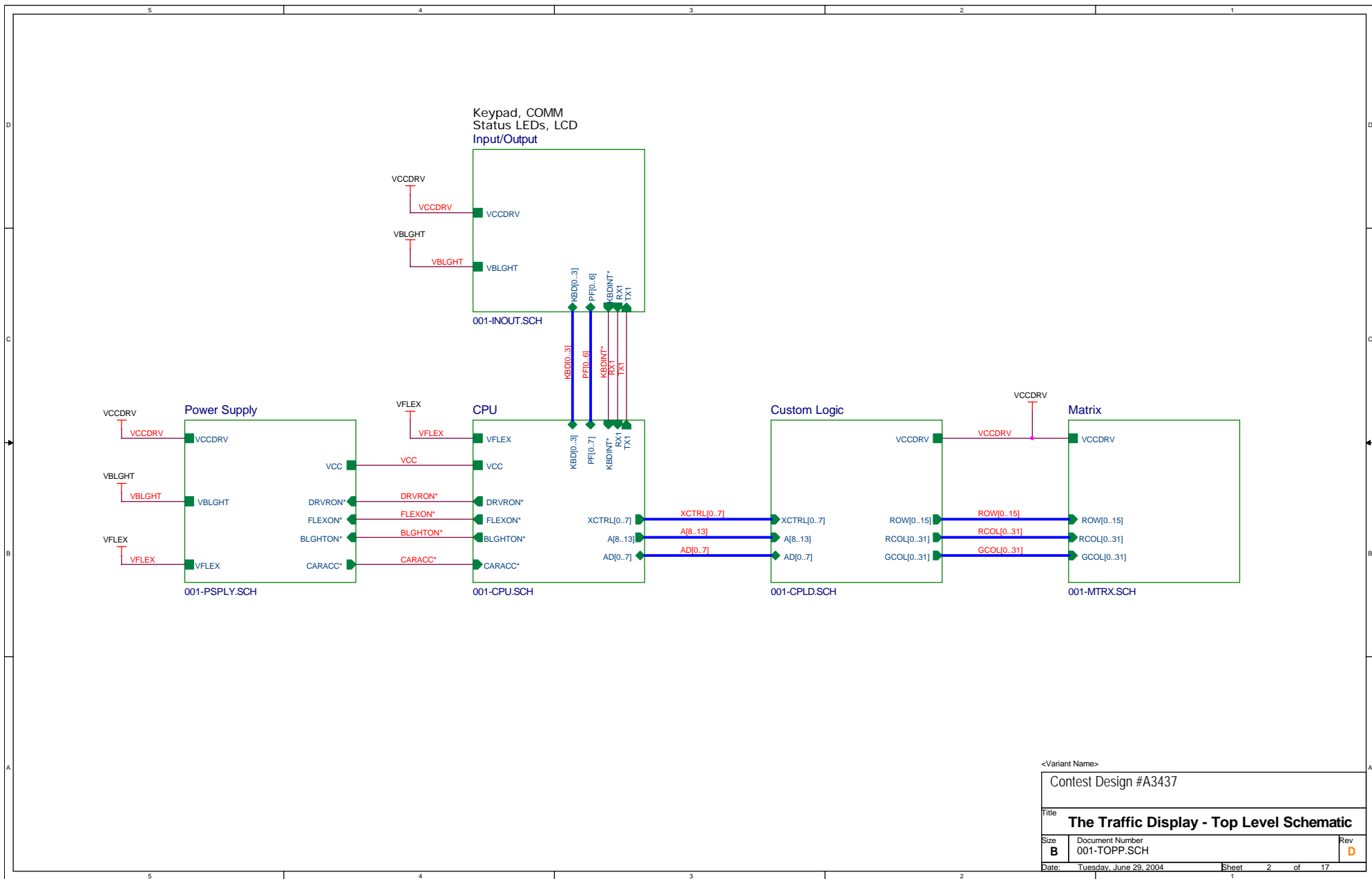
LED Matrix - up to 512 LEDs

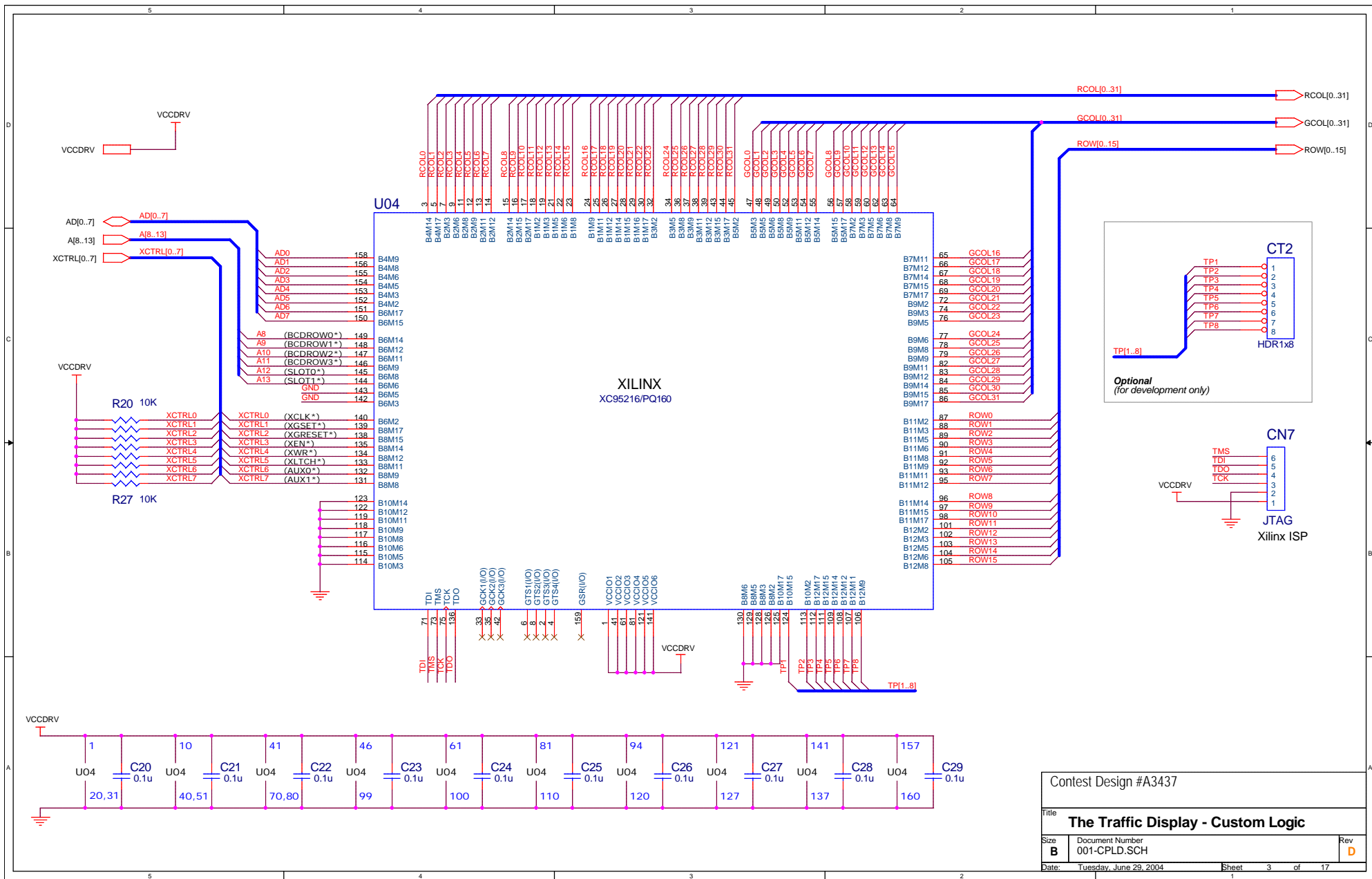


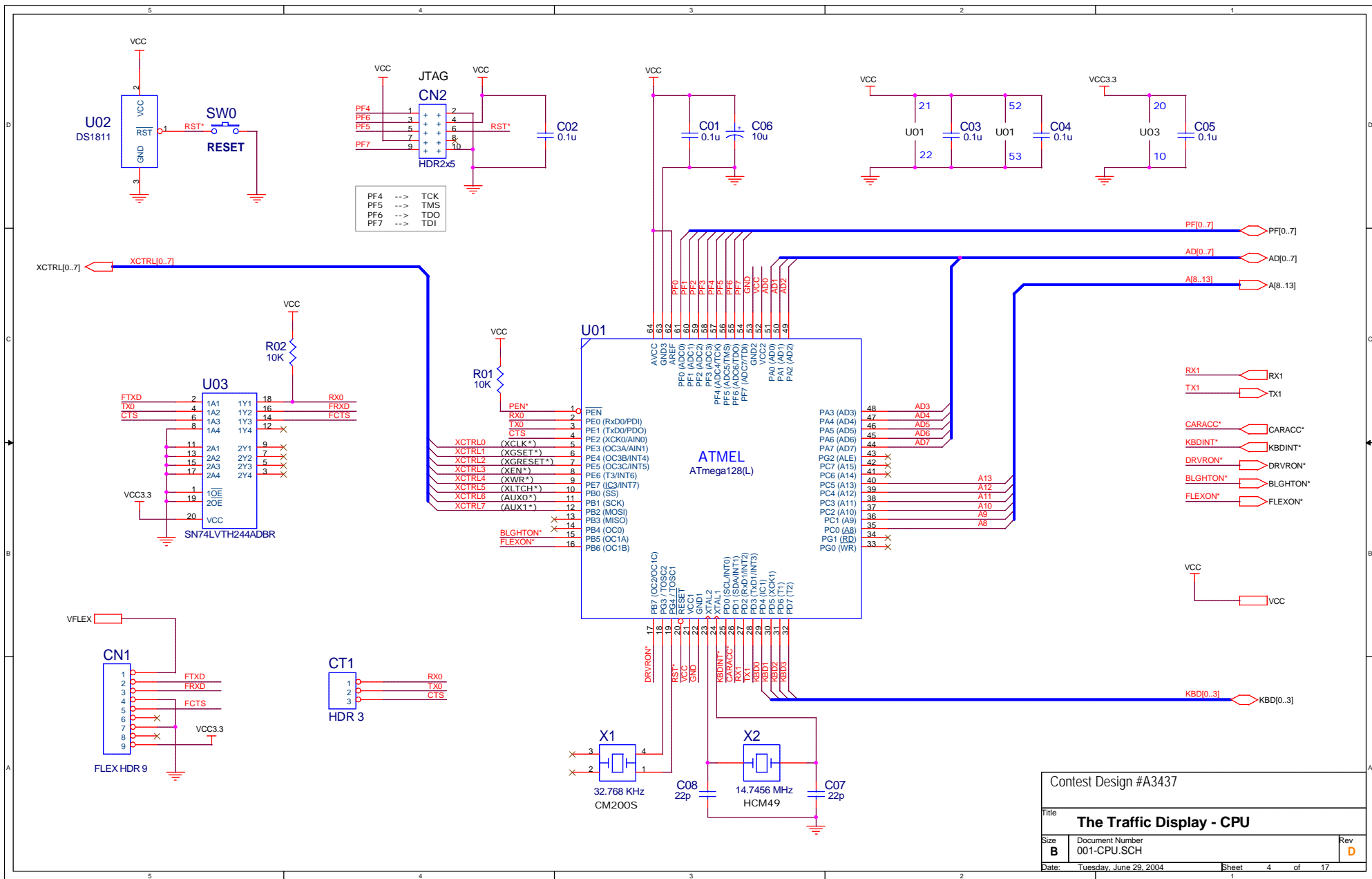
RS-232

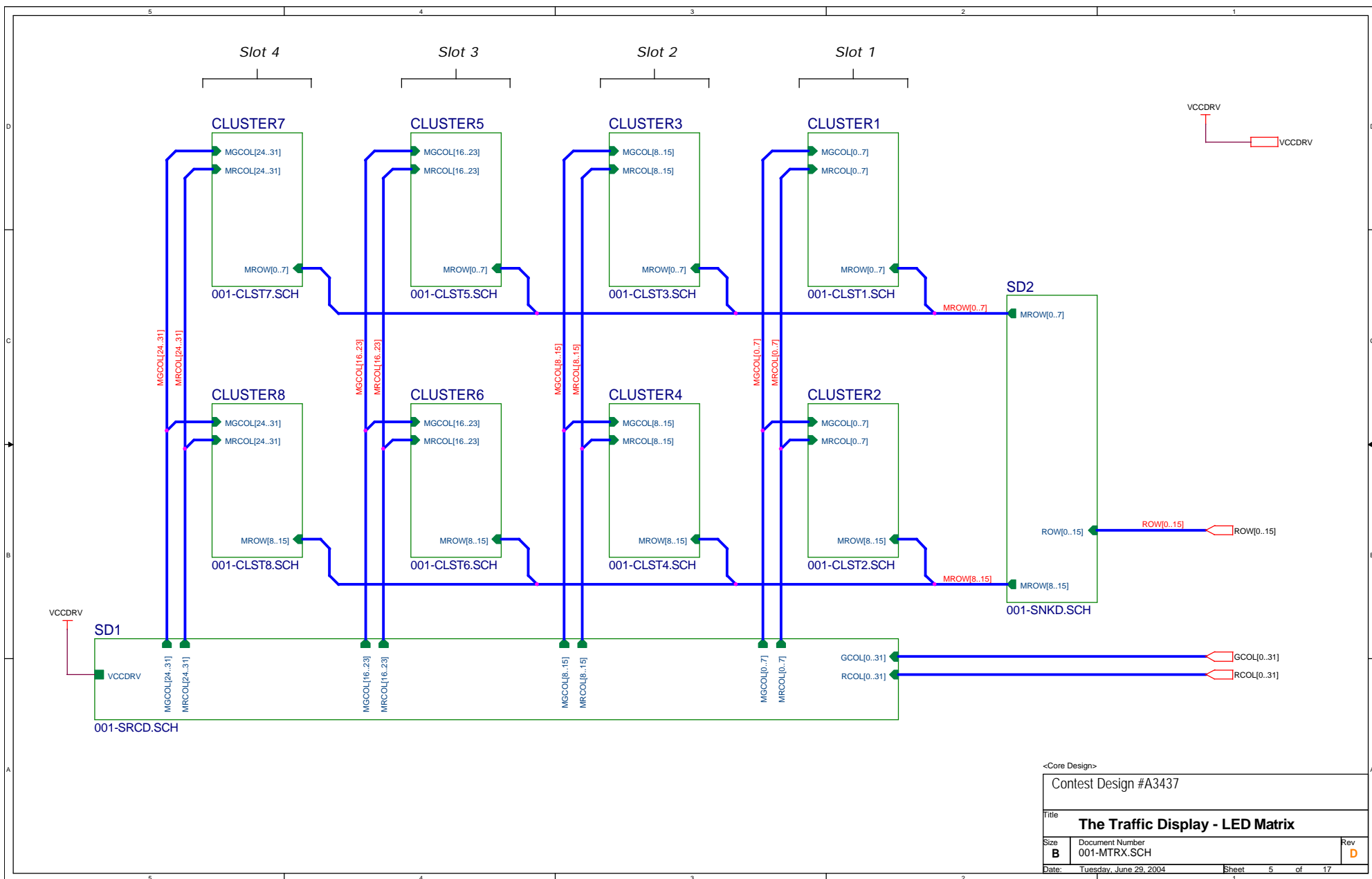
Contest Design #A3437

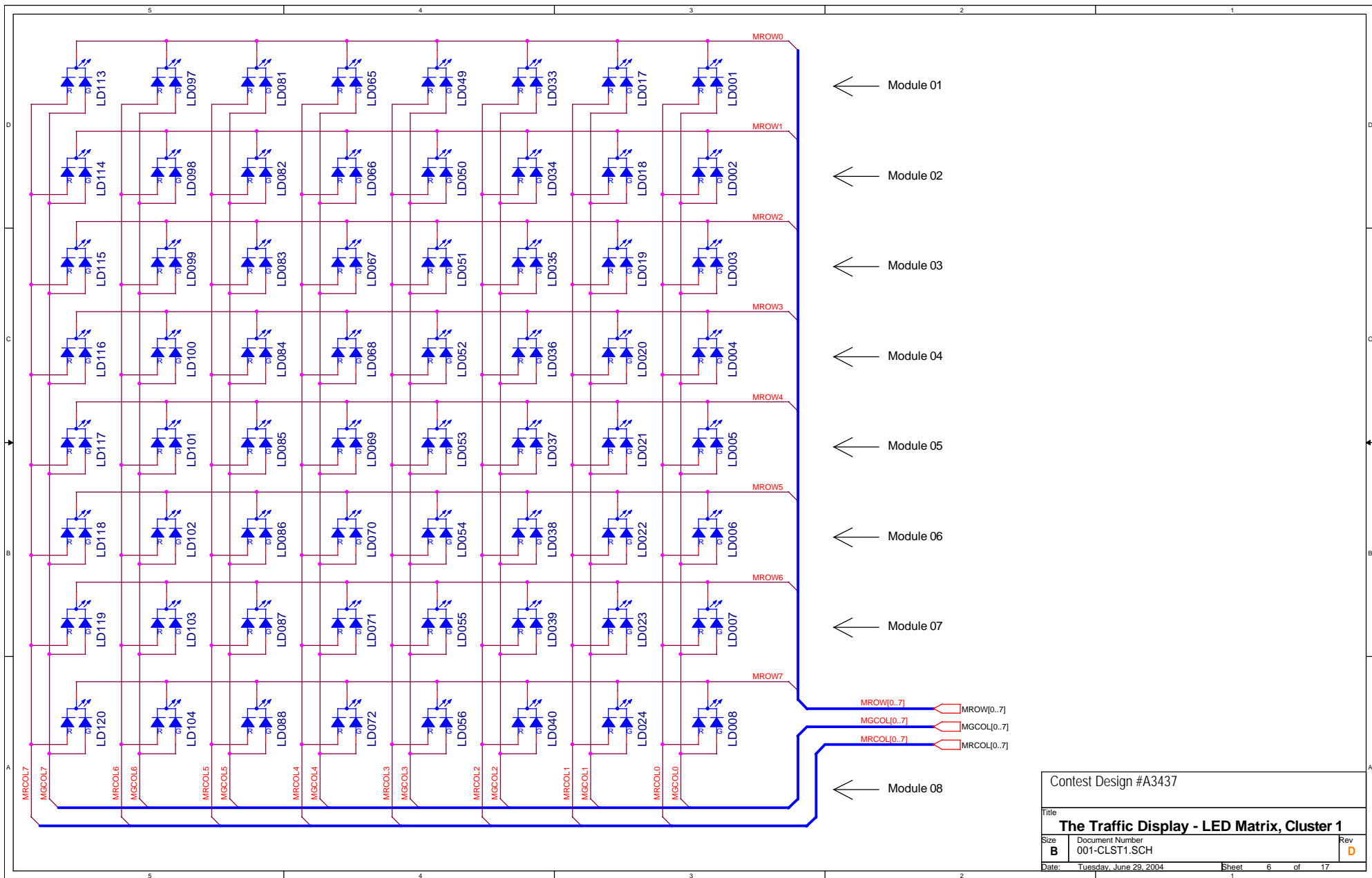
Title The Traffic Display - Title Page		
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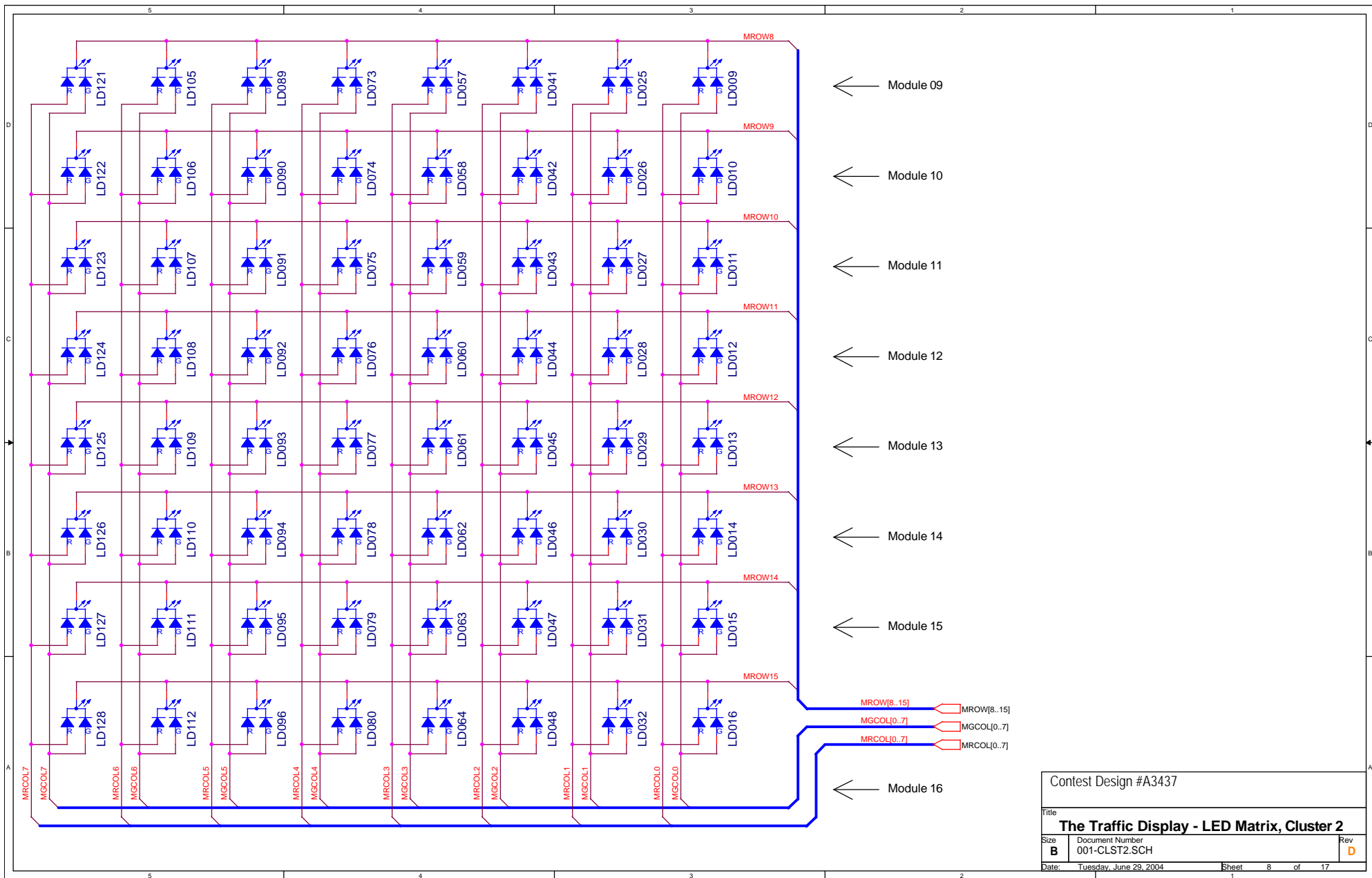


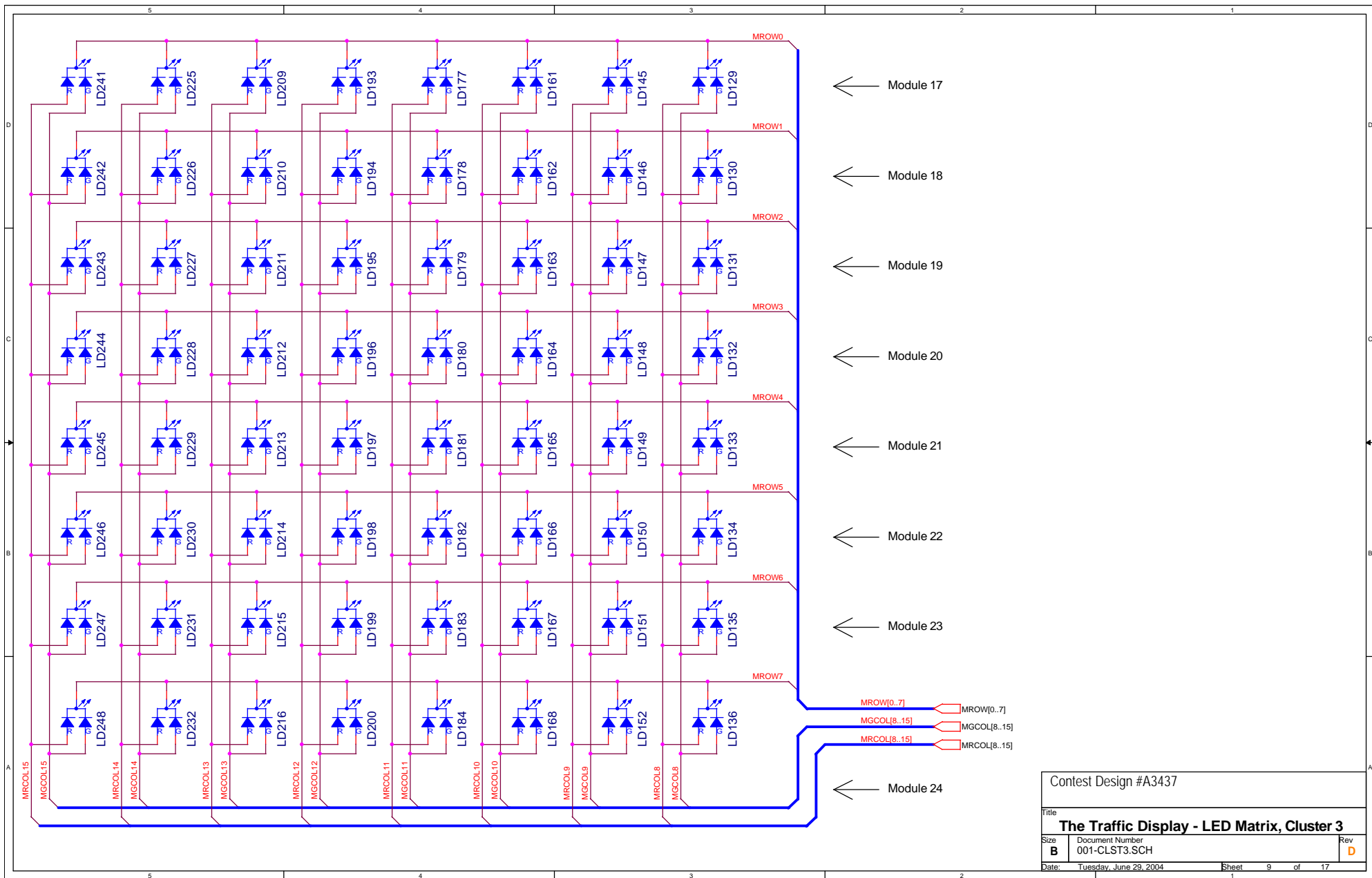
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Title
The Traffic Display - LED Matrix, Cluster 1

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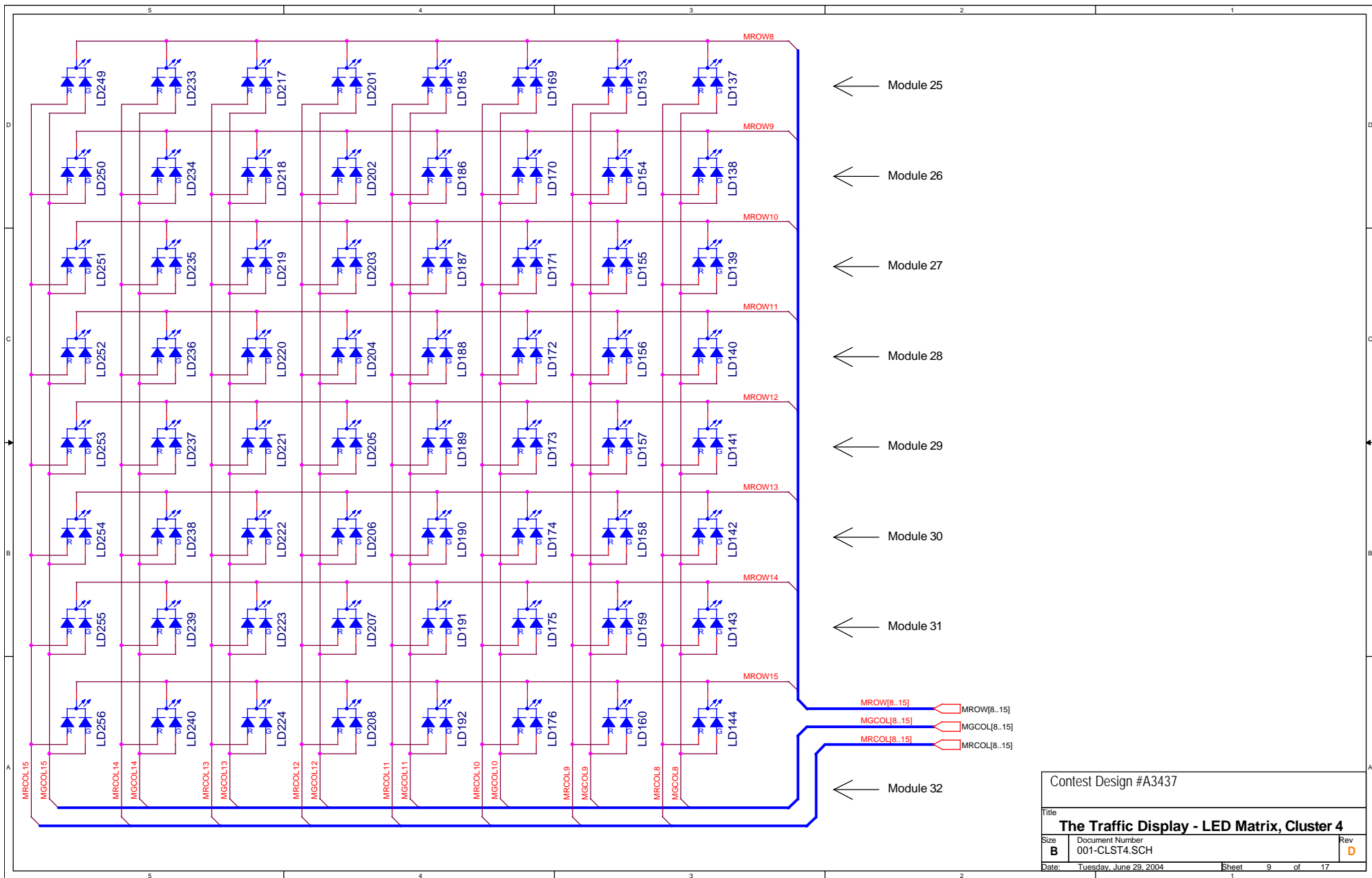


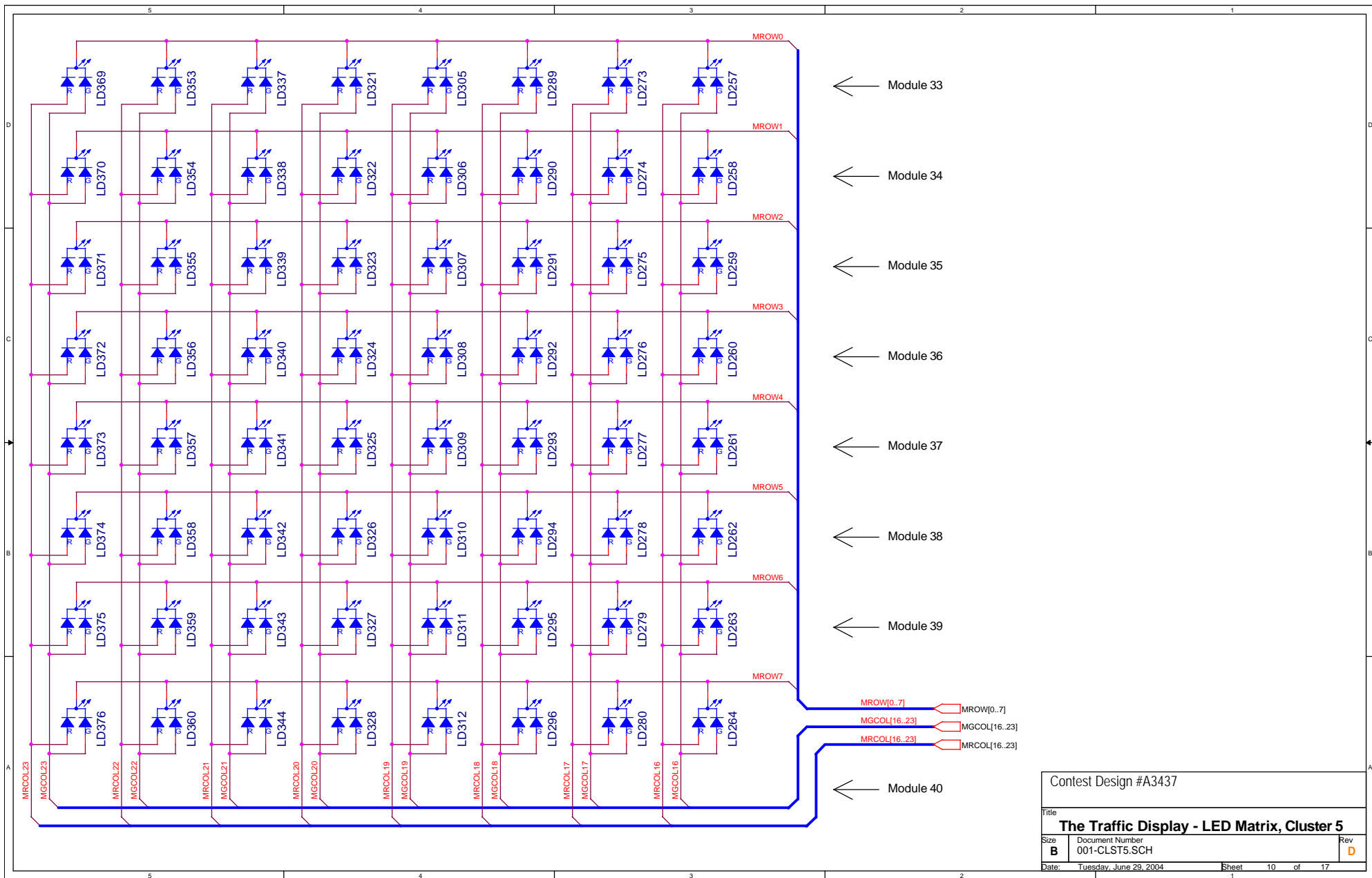
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Title
The Traffic Display - LED Matrix, Cluster 3

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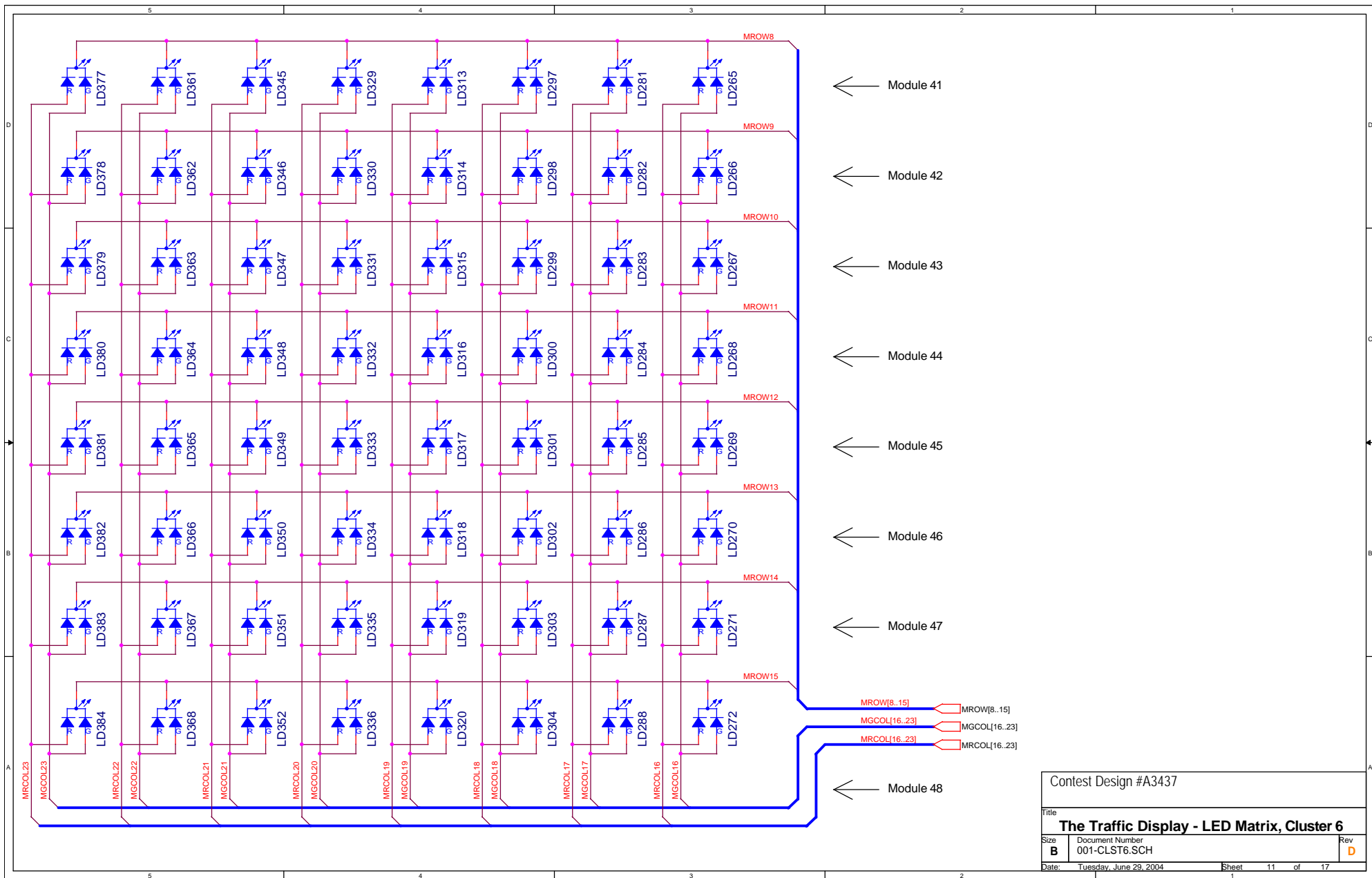
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Size B	Document Number 001-CLST5.SCH
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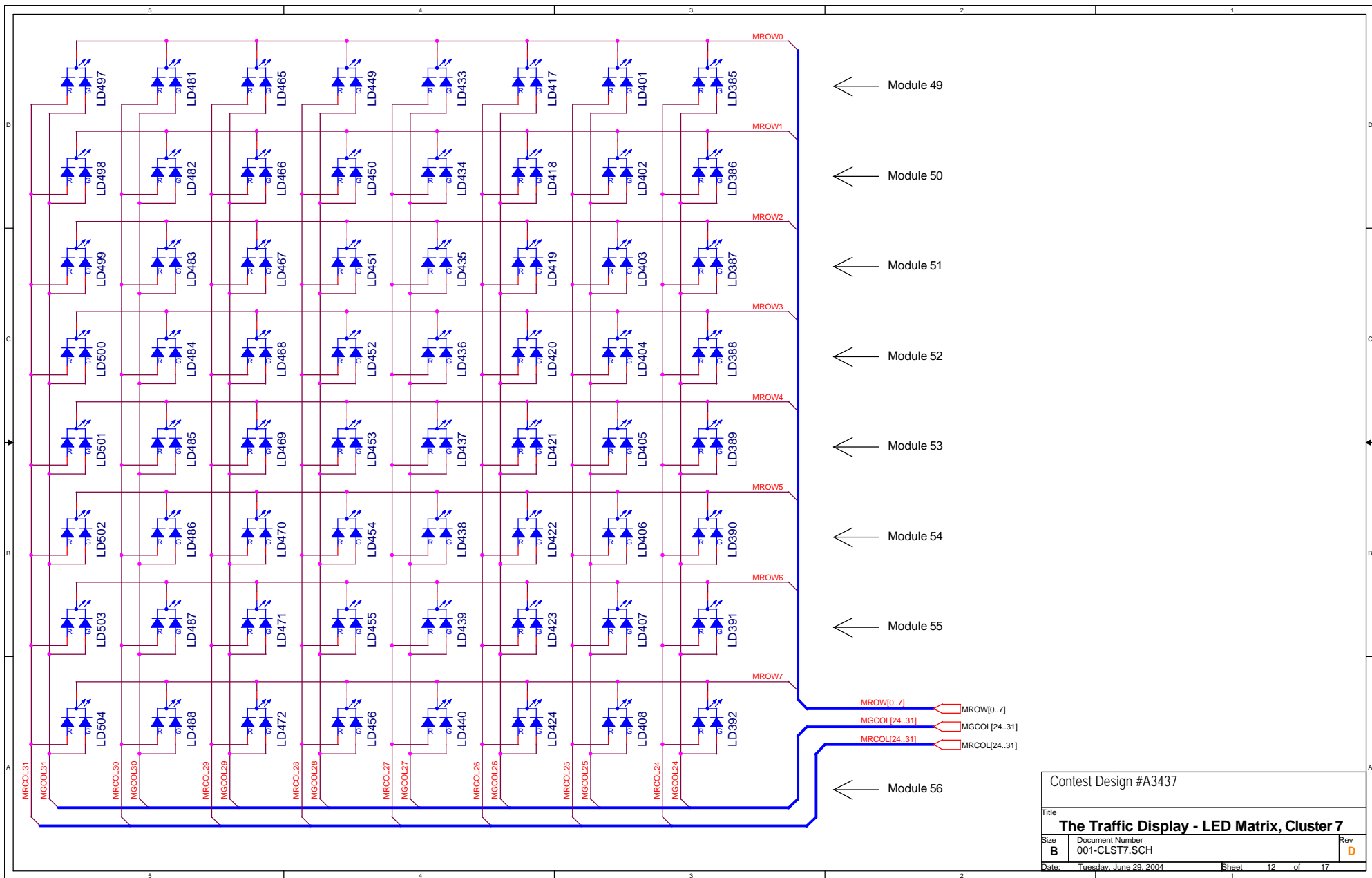
Contest Design #A3437

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Size B	Document Number 001-CLST6.SCH
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Date: Tuesday, June 29, 2004

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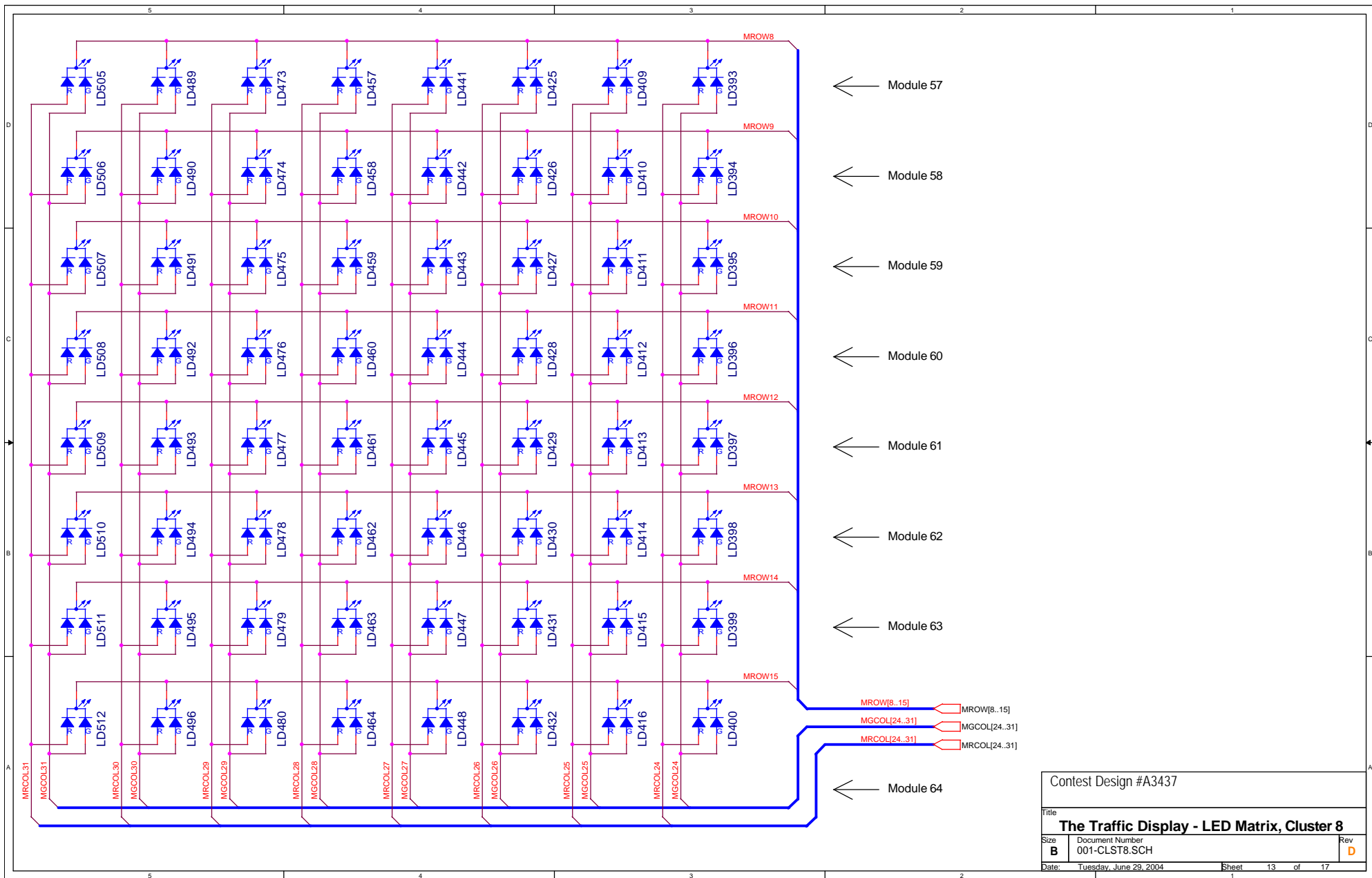


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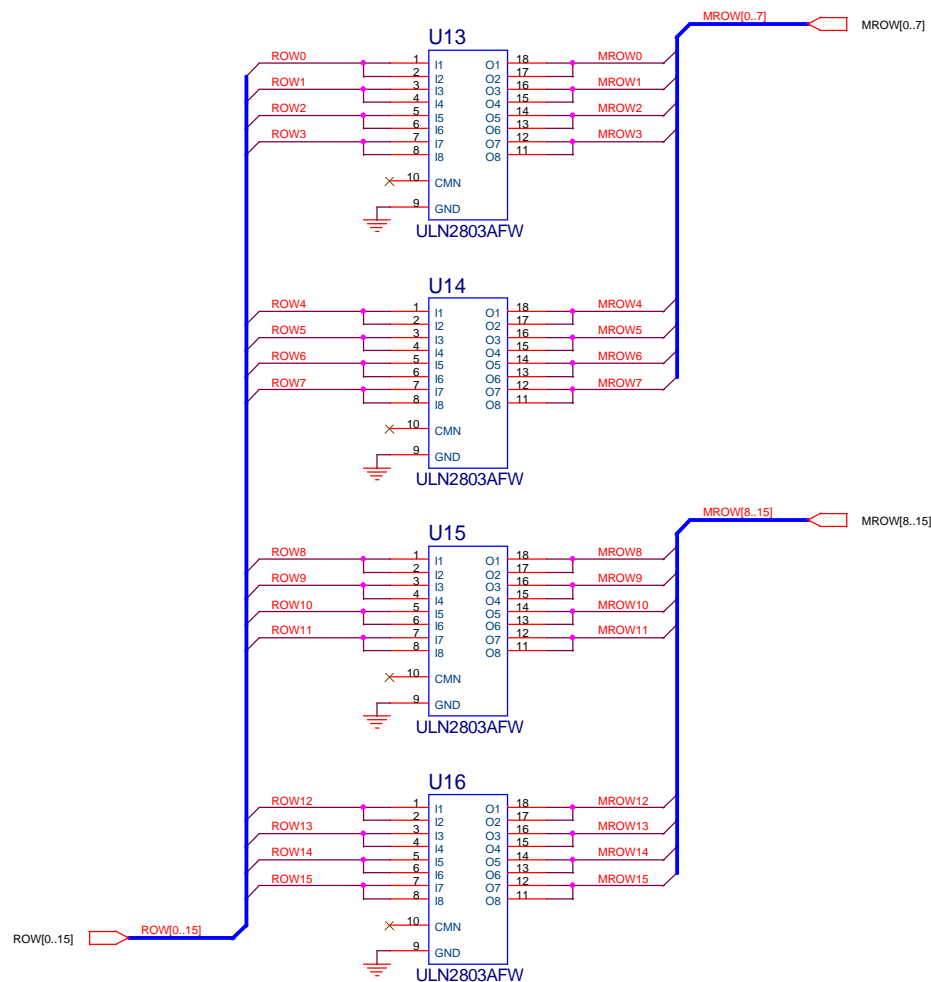
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The Traffic Display - LED Matrix, Cluster 7

Size **B** Document Number 001-CLST7.SCH Rev **D**

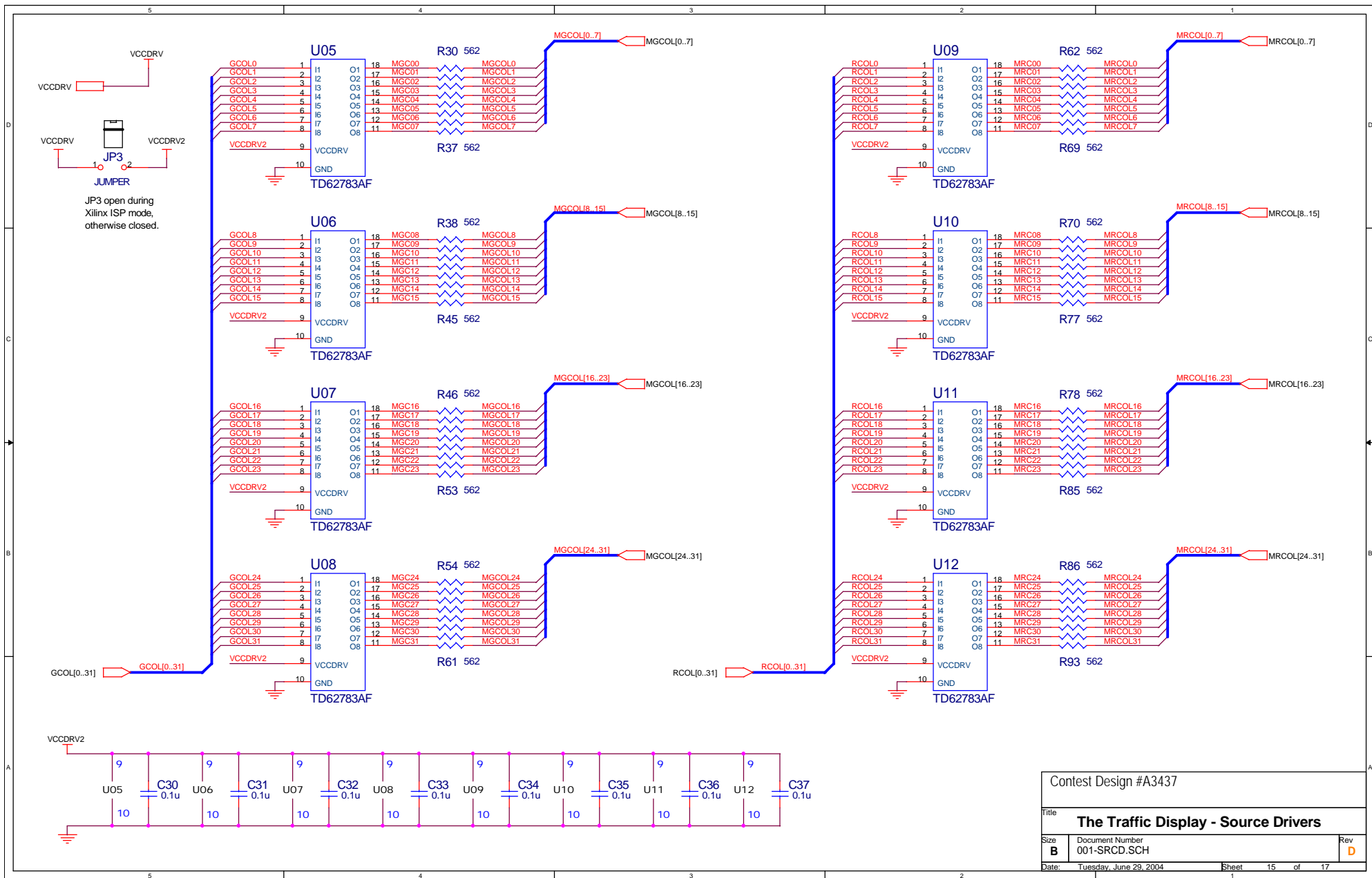
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Title		
The Traffic Display - LED Matrix, Cluster 8		
Size	Document Number	Rev
B	001-CLST8.SCH	D
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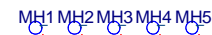
Contest Design #A3437		
Title		
The Traffic Display - Sink Drivers		
Size	Document Number	Rev
B	001-SNKD.SCH	D
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Title		
The Traffic Display - Source Drivers		
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B	001-SRCD.SCH	D
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PORT	LCD
PF0	--> D4
PF1	--> D5
PF2	--> D6
PF3	--> D7
PF4	--> RS
PF5	--> R/W
PF6	--> E
PF7	--> n/c



Contest Design #A3437

Title

The Traffic Display - Input / Output

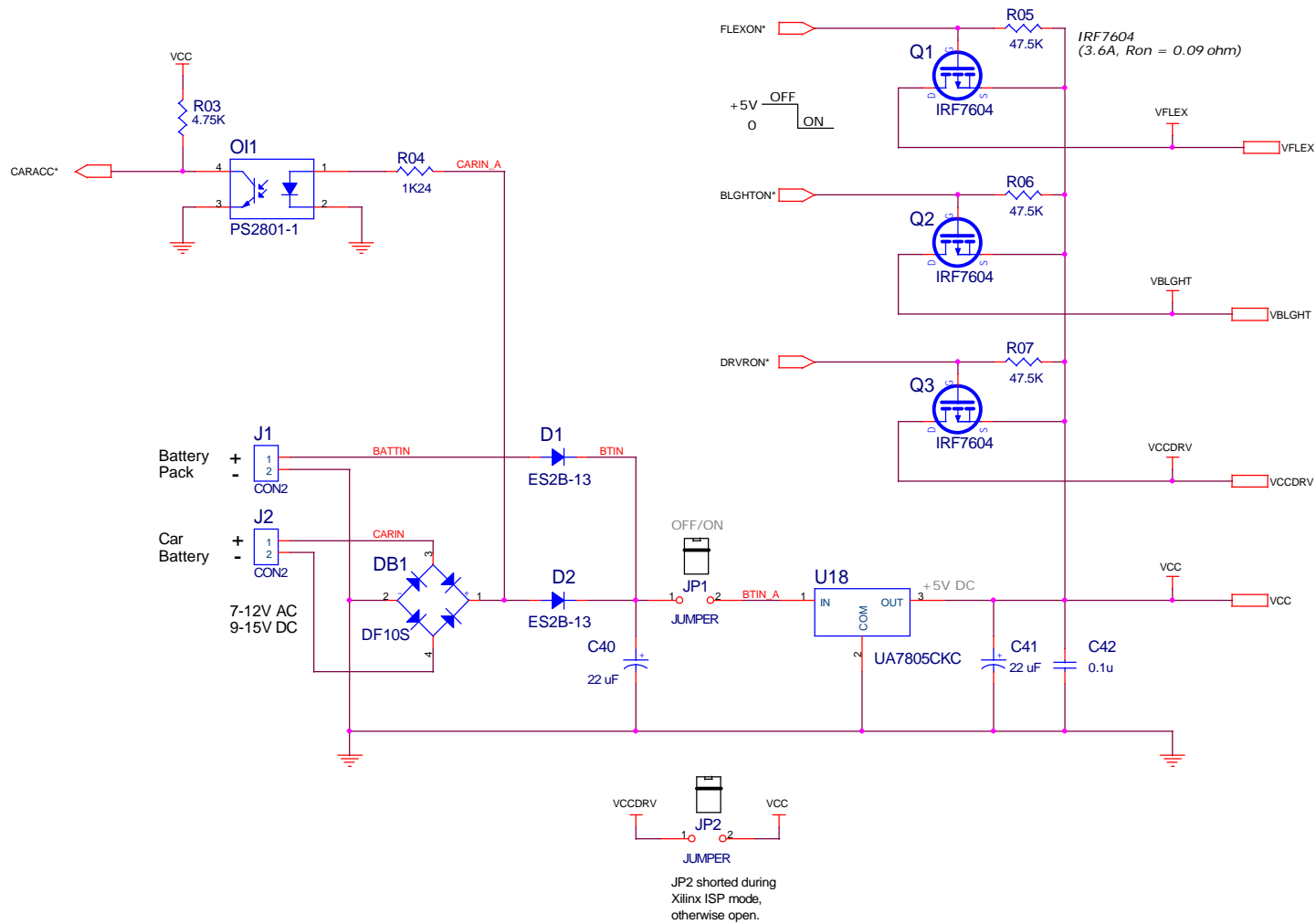
Size

Size B	Document Number 001-INOUT.SCH
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Contest Design #A3437

Title

The Traffic Display - Power Supply

Size

B Document Number

001-PSPLY.SCH

Rev

D

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