



Software Release Notes

EtherCAT Software Framework (ESF)



Microchip Technology, Incorporated
2355 W. Chandler Boulevard
Chandler, Arizona 85224
480/792-7416

| REV | DATE | DESCRIPTION OF CHANGE |
|------|--------------|---|
| 1.00 | Aug 26, 2020 | Dummy cycle support for SPI & SQI firmware with all modes Addressed code review comments and code cleanup and Bug fixes Make ESF compliant to PC-Lint 2012 standard and as error free |
| 1.01 | Oct 05, 2020 | Bug Fix -Odd address register access |
| 1.02 | Oct 09, 2020 | HBI Direct mode support with 8bit and 16bit |
| 1.10 | Oct 29, 2020 | HBI Direct mode with DC enable fix and code cleanup. |
| | | |

Table of Contents

| | | |
|-------|--|---|
| 1 | Introduction | 4 |
| 2 | Release notes | 4 |
| 2.1 | Version 1.10..... | 4 |
| 2.1.1 | Not implemented / Limited functionality requirements | 4 |
| 2.1.2 | Bug Fixes..... | 4 |
| 2.1.3 | Features added | 4 |
| 2.1.4 | Notes..... | 4 |
| 2.2 | Version 1.02..... | 5 |
| 2.2.1 | Not implemented / Limited functionality requirements | 5 |
| 2.2.2 | Bug Fixes..... | 5 |
| 2.2.3 | Features added | 5 |
| 2.2.4 | Notes..... | 5 |
| 2.3 | Version 1.01..... | 6 |
| 2.3.1 | Not implemented / Limited functionality requirements | 6 |
| 2.3.2 | Bug Fixes..... | 6 |
| 2.3.3 | Features added | 6 |
| 2.3.4 | Notes..... | 6 |
| 2.4 | Version 1.00..... | 7 |
| 2.4.1 | Not implemented / Limited functionality requirements | 7 |
| 2.4.2 | Bug Fixes..... | 7 |
| 2.4.3 | Features added | 7 |
| 2.4.4 | Notes..... | 8 |

1 Introduction

| | |
|--|---|
| Component Type | Firmware Framework |
| Target Silicon | LAN9253 |
| Where can an end user see the version number | Version number included in ESF > Source > ESC > ProjectVersion.h |

This document contains the EVB- LAN9253 EtherCAT Slave Controller SDK – SAMD51 firmware samples based on EVB-LAN9253-SAMD51 platform.

2 Release notes

2.1 Version 1.10

| | |
|--|---|
| Release date | 29-Oct-2020 |
| Release Type | Bug Fix |
| Pre-requisites (if any) | JUTLAND2-SVB3, SAME70 ADD-ON board |
| Source code path | https://bitbucket.microchip.com/scm/ung_apps/ethercat-software-framework-public-new.git |
| Source code label / branch & perforce change list number | Tag: ESF_STACK_V1.10 |

2.1.1 Not implemented / Limited functionality requirements

- HBI Indirect mode with 8bit verified with basic OP mode verification.

2.1.2 Bug Fixes

- ESF-287: HBI De-mux DC compliance issue

2.1.3 Features added

None

2.1.4 Notes

- The PDI selection along with access mode (8bit_direct_mode/16bit_direct_mode) can be selected through the project configuration.
- This Version include HBI single phase direct mode with 8bit and 16bit.

2.2 Version 1.02

| | |
|--|---|
| Release date | 08-Oct-2020 |
| Release Type | Production |
| Pre-requisites (if any) | JUTLAND2-SVB3, SAME70 ADD-ON board |
| Source code path | https://bitbucket.microchip.com/scm/ung_apps/ethercat-software-framework-public-new.git |
| Source code label / branch & perforce change list number | Tag: ESF_STACK_V1.02 |

2.2.1 Not implemented / Limited functionality requirements

- HBI Indirect mode with 8bit and 16bit

2.2.2 Bug Fixes

None

2.2.3 Features added

- ESF-209: SAME70 support for ESF
Add Demo application, which uses HBI as PD interface. Provided the Demo application with Direct mode 8bit and 16bit modes.

2.2.4 Notes

- The PDI selection along with access mode (8bit_direct_mode/16bit_direct_mode) can be selected through the project configuration.

2.3 Version 1.01

| | |
|--|---|
| Release date | 05-Oct-2020 |
| Release Type | Production |
| Pre-requisites (if any) | EVB-LAN9253-SAMD51Rev B |
| Source code path | https://bitbucket.microchip.com/scm/ung_apps/ethercat-software-framework-public-new.git |
| Source code label / branch & perforce change list number | Tag: ESF_STACK_V1.01 |

2.3.1 Not implemented / Limited functionality requirements

None

2.3.2 Bug Fixes

- ESF-278: Odd address read with any length is failing in ESF - SQI mode
- ESF-279: PDRAM write is failing in Indirect mode - ESF SQI Interface

2.3.3 Features added

None

2.3.4 Notes

- The PDI frequency can be selected through the ESF configuration file. The frequency can be given in MHz to `ESF_PDI_FREQUENCY`.
- The PDI selection along with access mode (Direct/indirect/Beckhoff) can be selected through the project configuration.

2.4 Version 1.00

| | |
|---|---|
| Release date | 26-Aug-2020 |
| Release Type | Production |
| Pre-requisites (if any) | EVB-LAN9253-SAMD51Rev B |
| Source code path | https://bitbucket.microchip.com/scm/ung_apps/ethernetcat-software-framework-public-new.git |
| Source code label / branch & performance change list number | Tag: ESF_STACK_V1.00 |

2.4.1 Not implemented / Limited functionality requirements

None

2.4.2 Bug Fixes

- ESF-226: ETG compliance failed with minimum cycle time response test
The minimum cycle time is a device parameter which will be read by the master. In our setup, TwinCAT master has the provision to read the device configured parameters. the configured parameters will be listed in SSC configuration file. The configured parameter should be compliant to the device. Currently the "Minimum cycle time" parameter configured with 100 Microseconds, whereas the device is not compliant to that. So updated the "minimum cycle time" parameter, so that the device is compliant to that value. The value updated through "SSC configuration file".
- ESF-214: ERROR LED test in Compliance is failing
ECAT_LED has access to UC instead of ESC. Updated the ESC config files and ESI files, which provides ECAT_LED access to ESC. Also, EEPROM config data updated, which enables RUN and ERROR LED operation.
- ESF-189: LAN9253 - Direct mode SPI unable to enter OP mode
The Issue observed only in the case of SPI Direct mode. Debug the Read and write API's by enabling the SW break points and its observed that the API works fine and able to read proper values. Whenever there is a state change from PreOP to SafeOP, then there is an interrupt, and which needs to be served by reading proper AL event register. In Direct mode, we missed to add the Read piece of code, So AL event register gets loaded with old value, which results the error in state change. Added the read part in HW_EventReadIsr.

2.4.3 Features added

- ESF-243: PDI Frequency as a configurable parameter through ESF Config file
Enable the PDI frequency selection as a configurable parameter through ESF configuration file.
- ESF-250: Dummy cycle support for SPI modes
Provide the Dummy cycle support for SPI demo application with all the modes, such as Direct, Indirect and Beckhoff modes.
- ESF-237: PCLint setup with MISRA 2012 standard
Make the ESF to be compliant to MISRA 2012 standard and lint error-free code
- ESF-245: LAN9253- Some ESF source files do not have Microchip license disclaimer
All the files under the ESF code base should be having Microchip license disclaimer note.
- ESF-233: SQI support for SAMD51 Indirect mode
Enable the QSPI support for SAMD51 indirect mode and setup with 20Mhz and 60Mhz and the device should be able to enter OP mode.

- **ESF-234: ESF - Integration of SQI and SPI into a single demo**
We have separate Demo for SQI sample application. The SPI demo should be single and configuration option should be provided through IDE configuration. So that separate build happens based on the selected configuration.
- **ESF-229: ESF - SQI support for SAMD51 Direct mode**
Enable the QSPI support for SAMD51 direct mode and setup with 20Mhz and 60Mhz and the device should be able to enter OP mode.
- **ESF-227: SPI access times xls updated - FIRMWARE code needs to be modified accordingly**
The dummy cycle must be integrated to the code and should be dynamic. The dummy cycle code should use the latest SPI access times.

2.4.4 Notes

- The PDI frequency can be selected through the ESF configuration file. The frequency can be given in MHz to `ESF_PDI_FREQUENCY`.
- The PDI selection along with access mode (Direct/indirect/Beckhoff) can be selected through the project configuration.