



PSF DRP Demo Read Me

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1.07	08-Sep-20	Updated document version to align with v1.07 release
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1.12	22-Sep-21	Updated document version to align with v1.12 release

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2 Terms and Abbreviations

Term	Definition
PSF	Universal Serial Bus Power Delivery Software Framework
EVB	Evaluation Board
PD	Power Delivery
IDE	Integrated Development Environment
PDO	Power Data Object
PM-PD	Power Module-USB Power Delivery
LED	Light Emitting Diode
GPIO	General Purpose Input Output
DRP	Dual Role Power

3 Introduction

Microchip's USB Power Delivery Software Framework (PSF) is a configurable USB PD solution that is compliant to USB-PD 3.0 specification.

PSF DRP application includes PD DRP functionality with the support for Boot time configuration parameters, Data Role Swap, VCONN Swap, Power Role Swap, Alternate Mode, Hot Plug Detect and GPIO based DC-DC controller for port power control. This document is intended to guide a user on setting up the [USB Power Delivery Software Framework Evaluation kit with part number EV65D44A](#) to work properly with PSF DRP application.

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EV65D44A consists of the following:

- 1 x USB Power Delivery Software Framework EVB (EVB-PSF)
- 2 x One-Hot Vertical Mount PM-PDs (PMPD-VM-HOT)

4 Prerequisites

Hardware:

- 1) USB Power Delivery Software Framework Evaluation kit with part number EV65D44A (EVB-PSF)

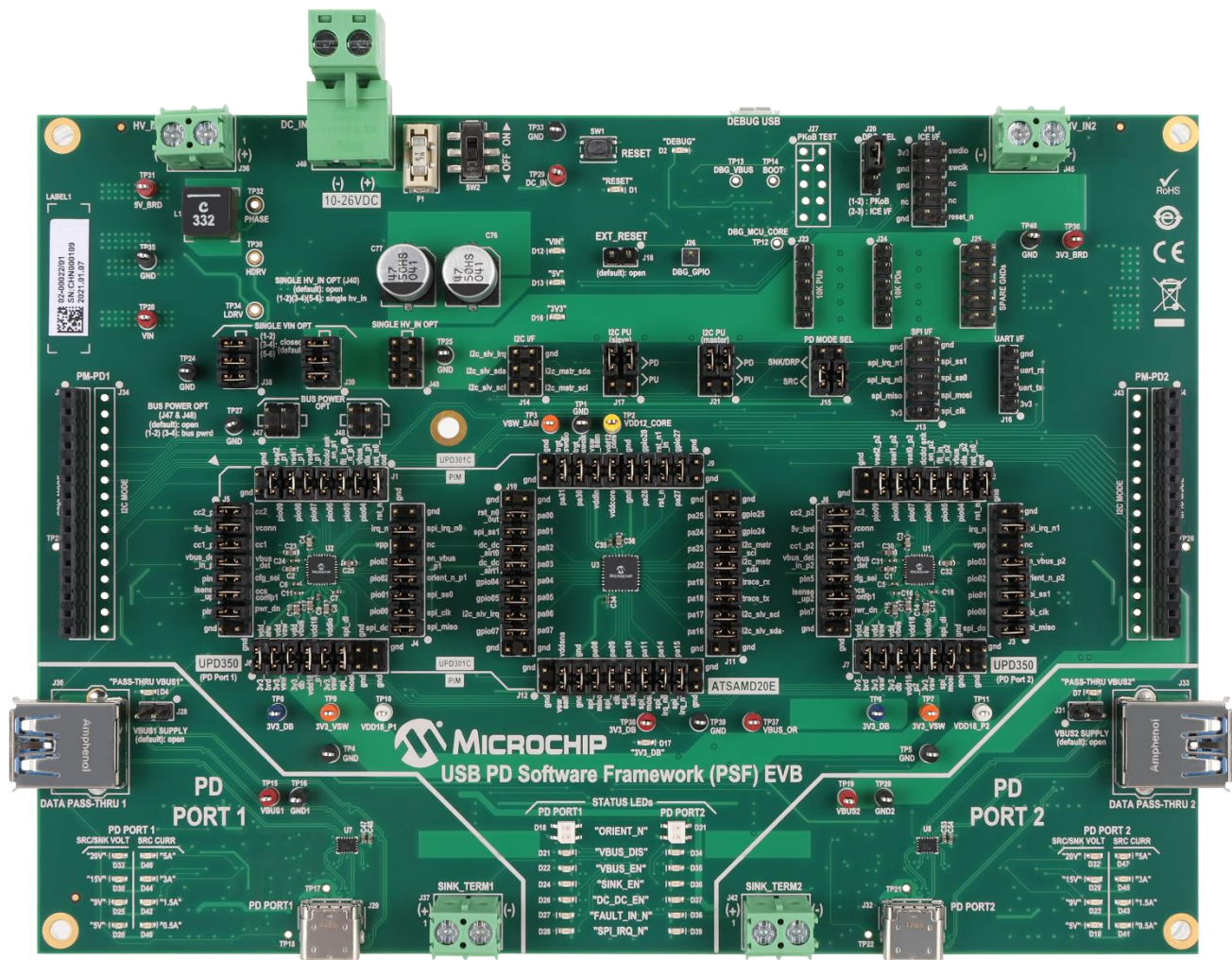


Figure 4.1 USB Power Delivery Software Framework EVB (EVB-PSF)

- 2) Two vertical mount One-Hot PM-PDs (PMPD-VM-HOT).

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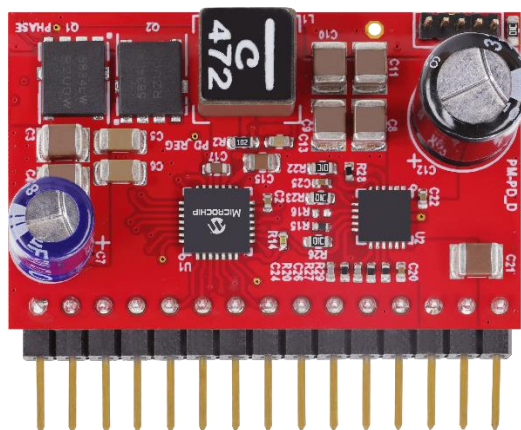


Figure 4.2 One-Hot Vertical Mount PM-PD (PMPD-VM-HOT)

3) 150W Power Adapter with 24V, 6.25A output/ Bench Power Supply



Figure 4.3 Power Adapter with 24V, 6.25A output capacity

4) USB-C to USB-C cable

5) [Atmel ICE Debugger kit](#) (Optional).



Figure 4.4 Atmel-ICE Debugger Kit

6) USB Power Delivery capable Phones or Laptops

5 Setting up the EVB-PSF board for “EVB_PSF_DRP”

1. Connect PM-PD modules to J35 and J44 of the EVB-PSF in correct orientation as shown in Figure 5.1

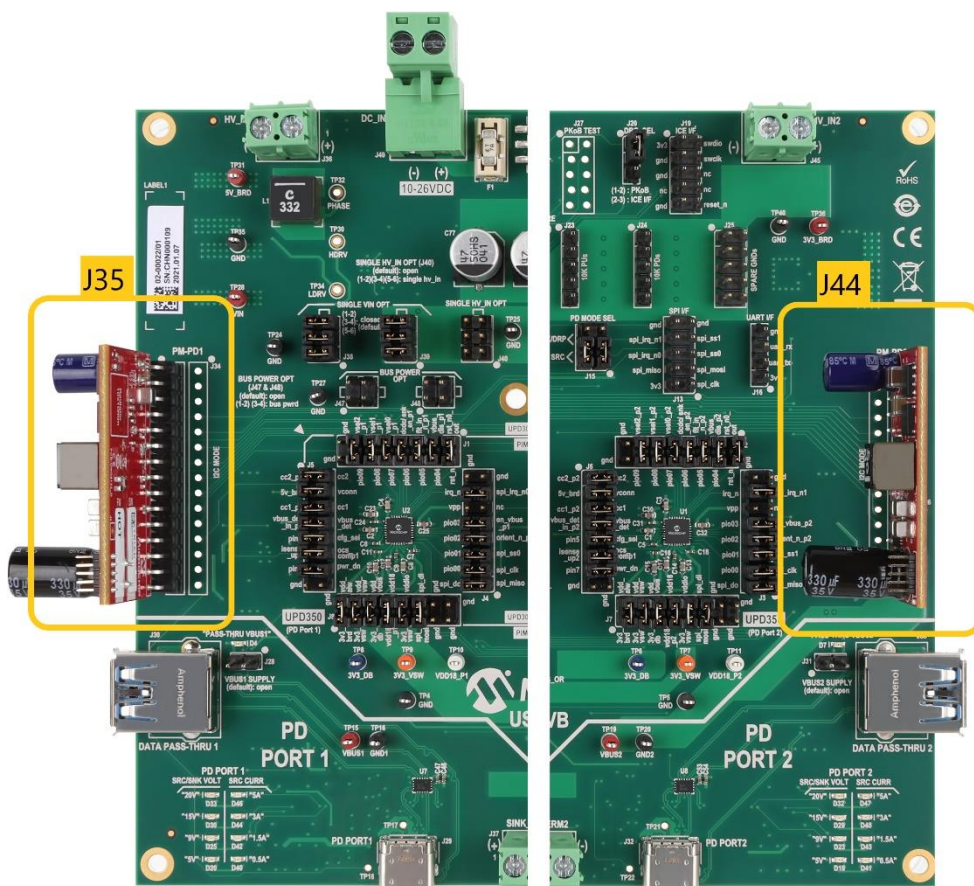


Figure 5.1 PM-PD Orientation

2. Ensure the following jumpers are connected in EVB-PSF before proceeding to next step.

Jumper	Pins
J38	1-2, 3-4, 5-6
J39	1-2, 3-4, 5-6
J17	3-5, 4-6
J21	3-5, 4-6
J15	3-5, 4-6
J1	1-2 3-4 5-6 9-10 11-12 13-14
J4	1-2 3-4 5-6 7-8 9-10 13-14
J5	1-2 3-4 5-6 7-8 9-10 13-14
J8	1-2 3-4 5-6 7-8 9-10 11-12
J9	3-4 5-6 9-10 11-12 13-14 15-16 17-18
J10	3-4 5-6 7-8 9-10 11-12 15-16

	17-18
J11	3-4 5-6 7-8 9-10 11-12 13-14 15-16 17-18
J12	3-4 5-6 7-8 9-10 11-12 13-14 15-16 17-18
J2	1-2 3-4 5-6 9-10 11-12 13-14
J3	1-2 3-4 5-6 7-8 9-10 13-14
J6	1-2 3-4 5-6 7-8 9-10 13-14
J7	1-2 3-4 5-6 7-8 9-10 11-12

Table 5.1 EVB-PSF Jumper Connections



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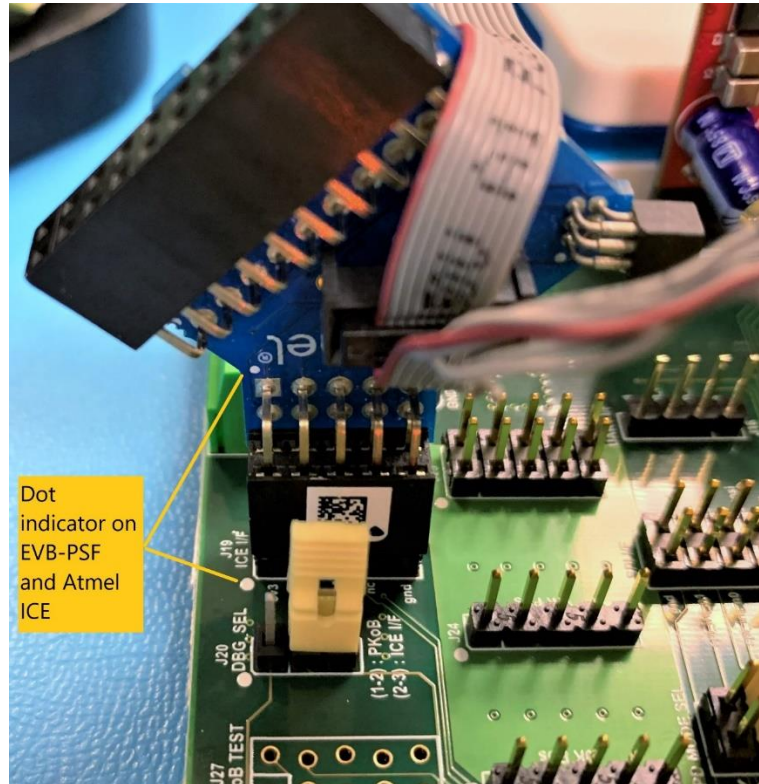


Figure 5.3 Connecting Atmel-ICE Debugger to J19

6. In Case if you are using Onboard Debugger ,Connect a USB Micro-B cable to “DEBUG USB” which on the top of the board and connect the other end USB Type-A to the laptop as shown in the Fig 5.4.

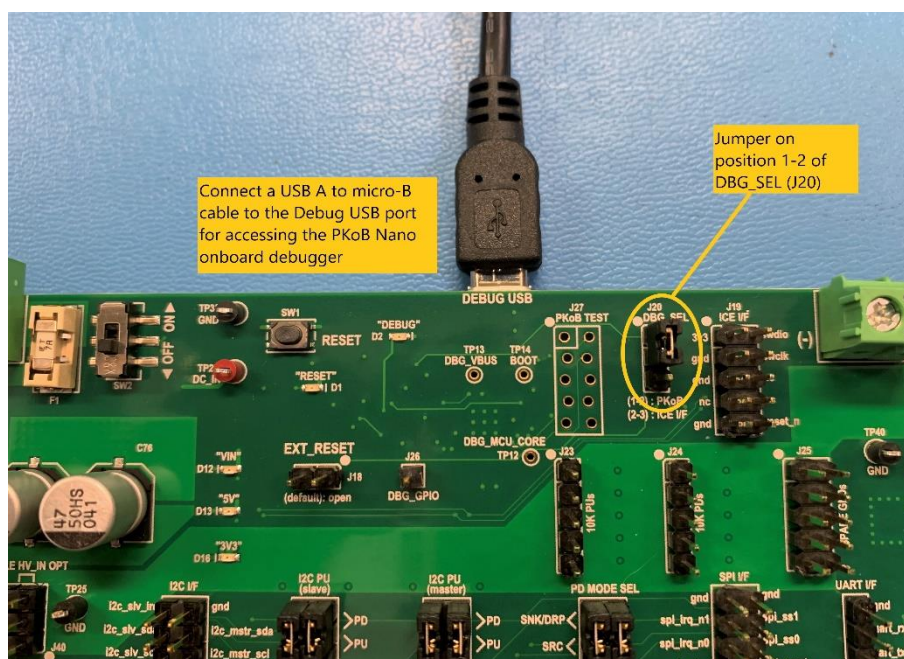


Fig 5.4 Connecting On Board Debugger

7. The whole connection looks like,

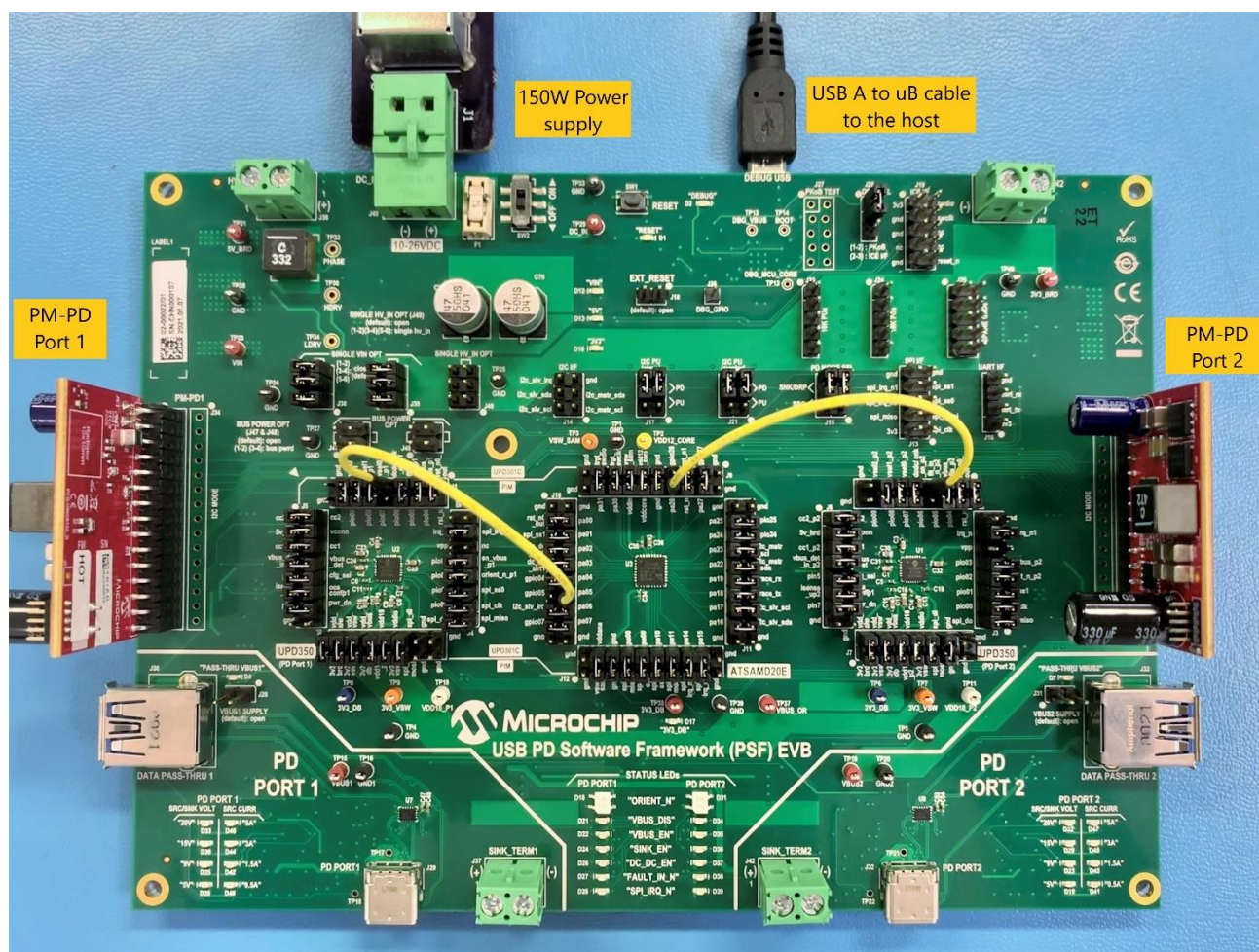


Figure 5.5 EVB-PSF Full Setup

6 Running the demo

Refer [Getting Started with PSF](#) document for the detailed steps on setting up the build environment, building the DRP PSF project and programming the EVB-PSF.

Refer Appendix 8.2 of [Getting Started with PSF](#) to change any SAMD20 Harmony configuration. Refer 'Boot time Configuration' section of [PSF User Guide](#) to change any configuration parameters.

1. Ensure all the jumpers are in place and Power on the EVB-PSF.
2. Program the EVB-PSF by following the steps mentioned in section 7 of [Getting Started with PSF](#)
3. Connect a PD device to Port 1 of EVB-PSF using a USB-C to USB-C cable.
4. Connect another PD device to Port 2 of EVB-PSF using a USB-C to USB-C cable.

5. This document demonstrates two scenarios with PSF DRP Firmware.

Scenario 1: USB PD Capable laptop (DRP capable partner) connected to PSF DRP Port 2 using a USB-C to USB-C cable

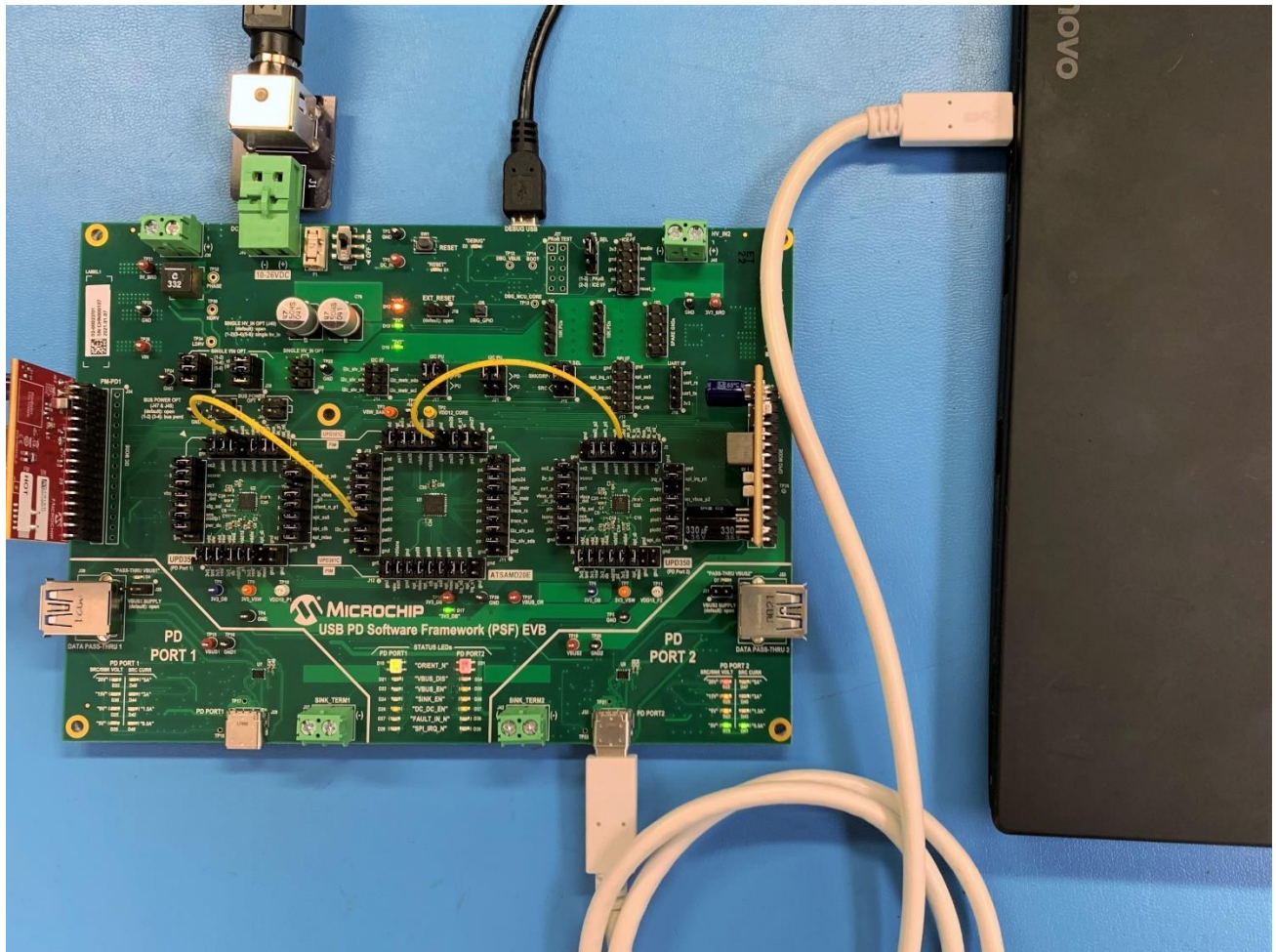


Figure 6.1 USB PD Capable Laptop connected to PSF DRP Port 2

Scenario 2: Two DRP ports of EVB-PSF connected using a USB-C to USB-C cable

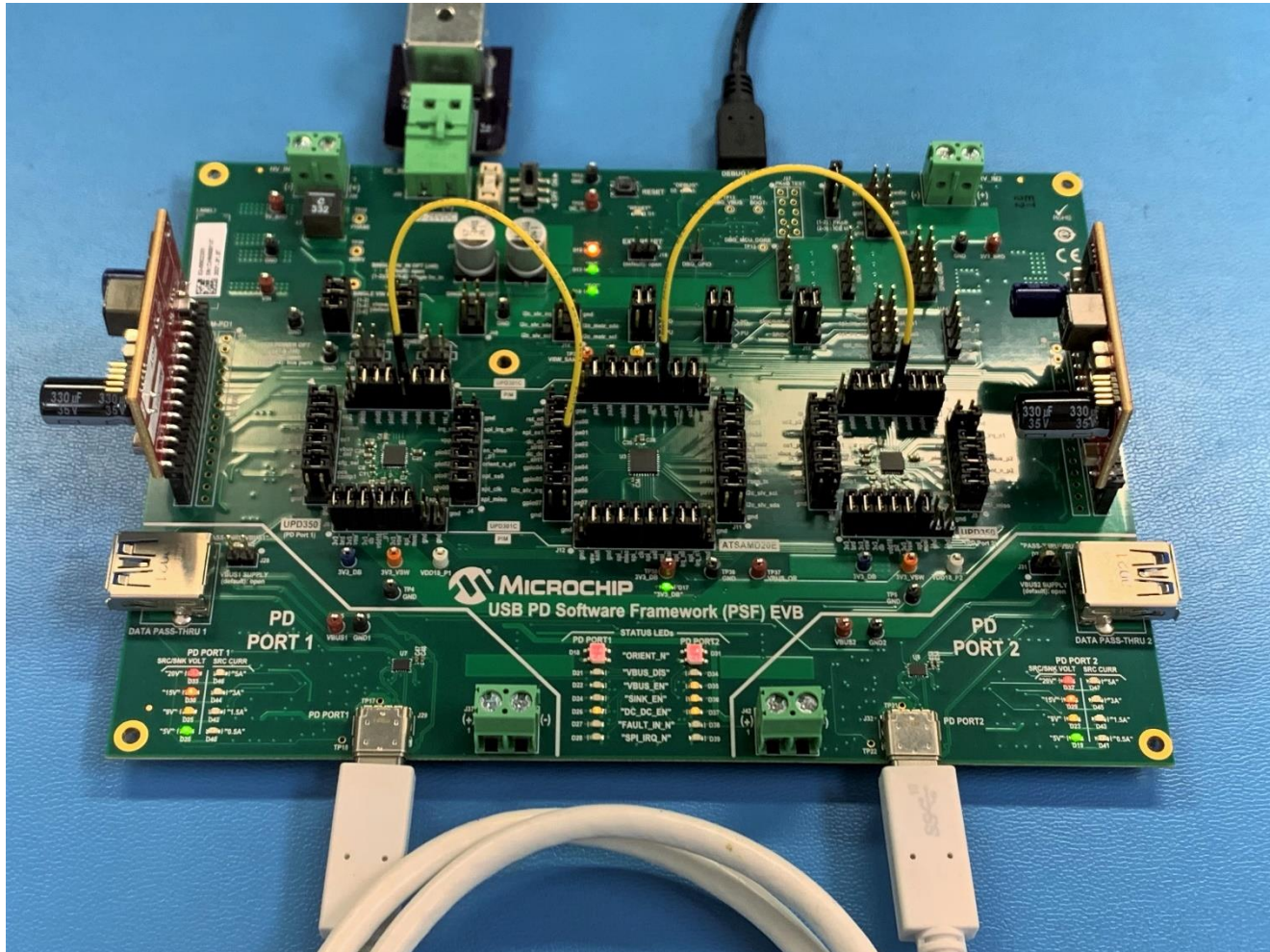


Figure 6.2 PSF DRP Port 1 connected to PSF DRP Port 2

7 Expected Results

1. Once the hex file is programmed, the SPI_IRQ_N LEDs in both the ports flash and then turn off. DC_DC_EN LEDs will be ON when the ports are in unattached state.

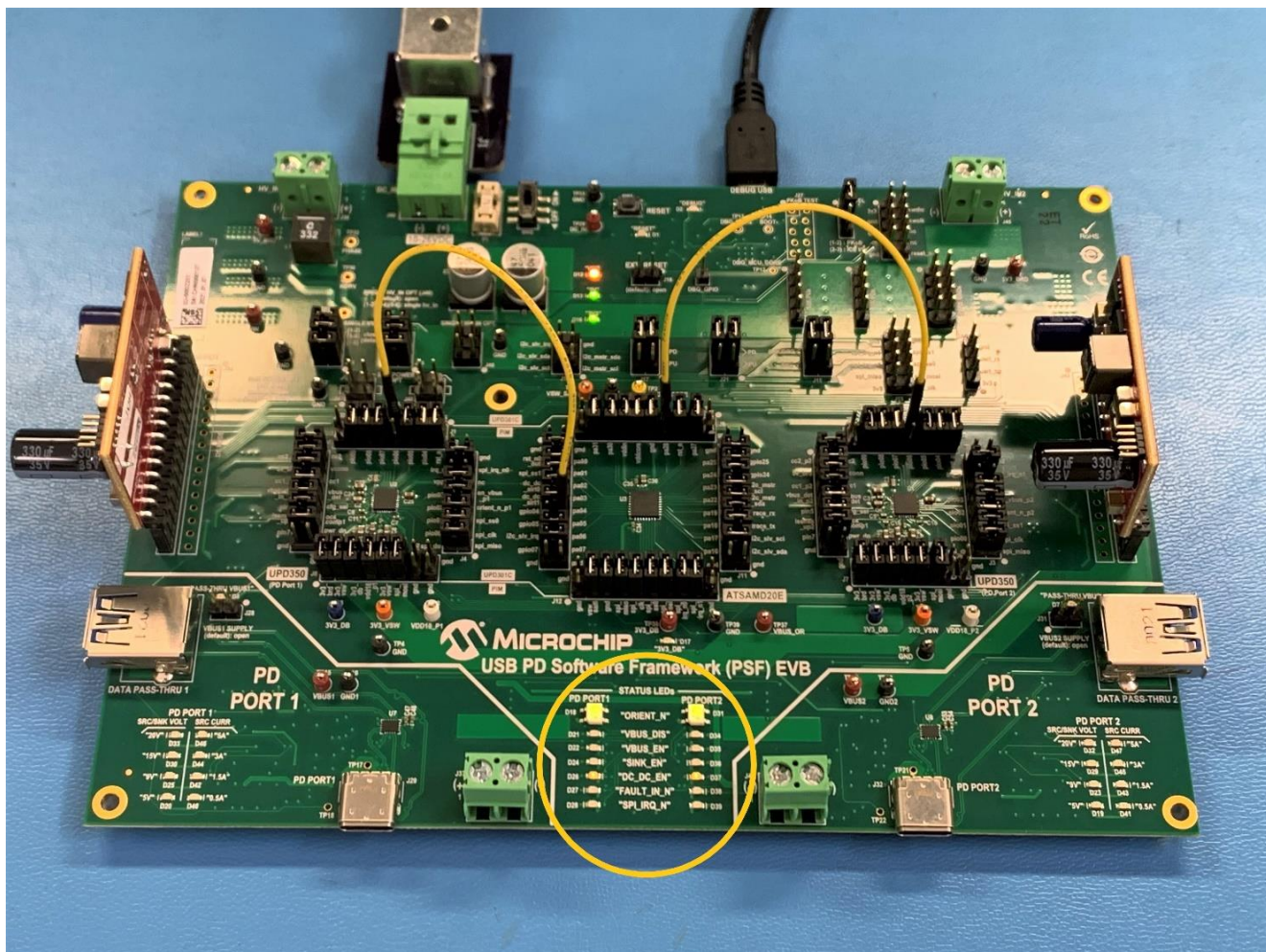


Figure 7.1 Status of LEDs after power on

2. If a Source only PD port partner is attached, PSF would become the Sink. If a Sink only PD port partner is attached, PSF would become the Source. If a DRP port partner is attached, there are chances that PSF would either attach as Source or as Sink depending on partner's power role.
3. The behaviour of PSF DRP port when attached as Source and Sink is described below.

PSF DRP port attached as Source:

1. EN_VBUS and DC_DC_EN LEDs of the port will turn on.
2. Source capabilities will be advertised by the PSF, followed by a PDO request from the device.
3. PSF checks if the PDO requested by the device is within the range of its capabilities. If so, it accepts the request and starts driving the requested voltage in the VBUS.
4. Once an explicit power contract negotiation is in place, the device starts charging. PDO status LEDs will turn on indicating the negotiated voltage as shown in Figure 7.2

- The following image depicts the status of Port 2 when attached as Source. Here the negotiated voltage is 20V

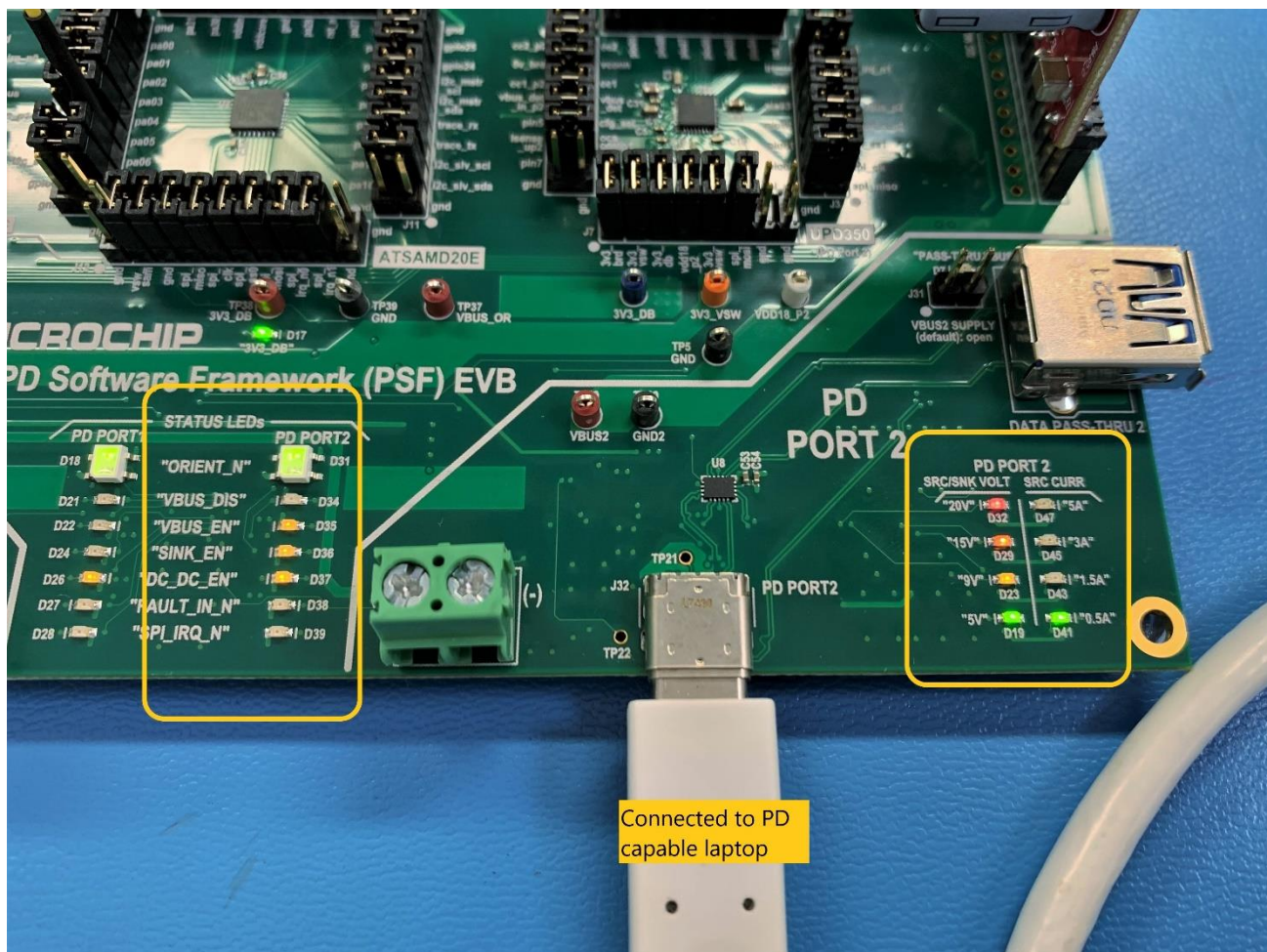


Figure 7.2 VBUS_EN, DC_DC_EN and PDO Status LEDs when attached as Source

PSF DRP port attached as Sink:

- Once the Source only or a PD DRP Device is attached to a PD port, the device gives out 5V followed by Source capabilities.
- PSF requests for suitable PDO from source capability based on the configuration. PD negotiation take place if the source accepts the request and sources the requested power.
- Once an explicit power contract negotiation is in place, the PSF gets enough power for charging. PDO status LEDs will turn ON indicating the negotiated voltage as shown in Figure 7.3
- The following image depicts the status of Port 1 when attached as Sink. Here the negotiated voltage is 5V.

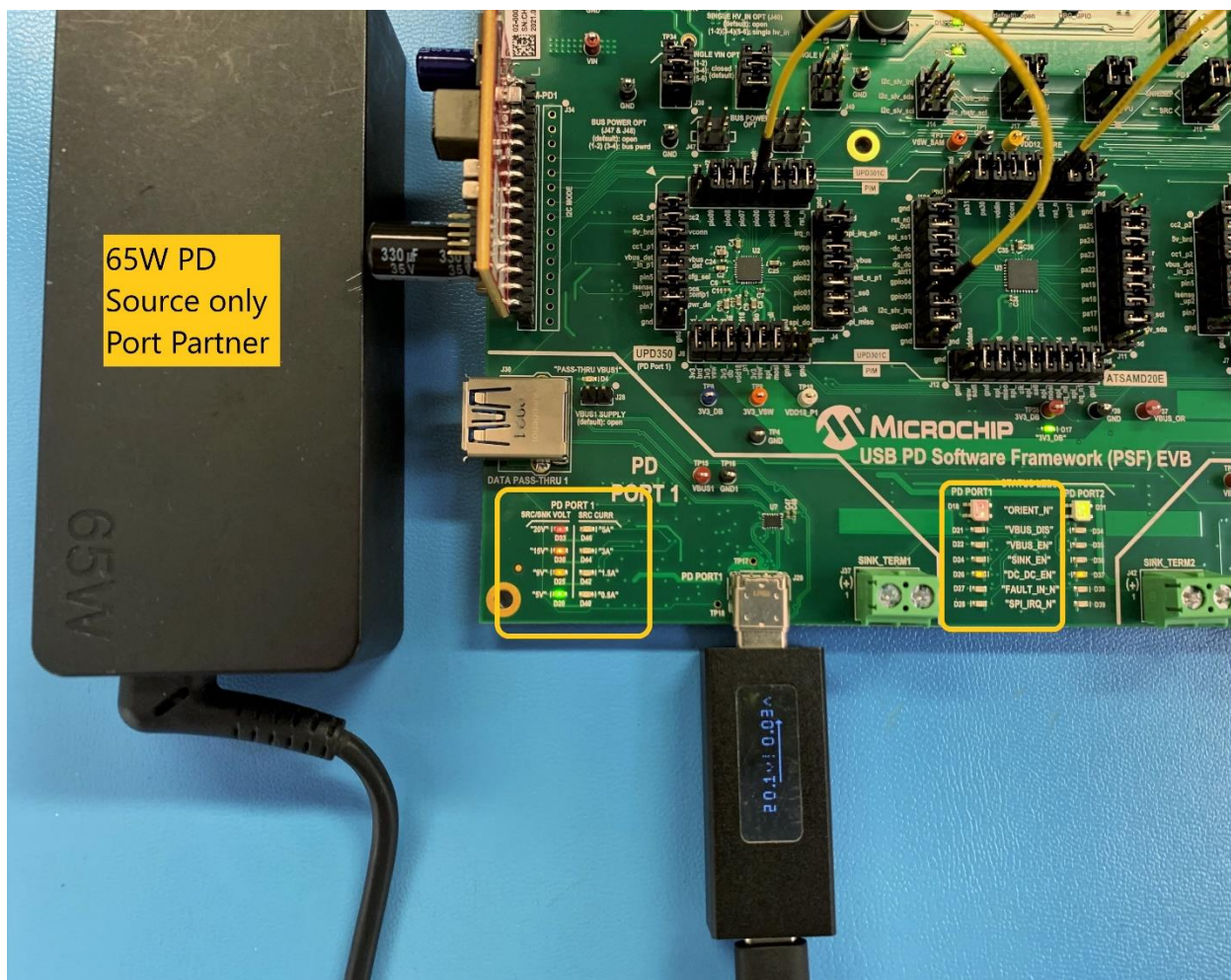


Figure 7.3 PDO Status LEDs when attached as Sink

PDO Status LEDs:

Example with Port 1: If 5V is negotiated by the device, LED in the D20 of Port 1 PDO will glow. If 9V is negotiated, then LEDs in D20 and D25 will glow. In case of 15V, LEDs in D20, D25 and D30 will glow. If 20V is negotiated, all the LEDs will glow.

PDO	Status LED
5V	Port 1 – D20, Port 2 – D19
9V	Port 1 – D25, Port 2 – D23
15V	Port 1 – D30, Port 2 – D29
20V	Port 1 – D33, Port 2 – D32

Table 7.1 PDO Status LEDs

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