



Getting Started with PSF

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2 Terms and Abbreviations

Term	Definition
USB	Universal Serial Bus
PSF	USB Power Delivery Software Framework
EVB	Evaluation Board
PD	Power Delivery
MCU	Micro Controller Unit
MHC	MPLAB Harmony Configurator
IDE	Integrated Development Environment
IPE	Integrated Programming Environment
PDO	Power Data Object

3 Introduction

USB Power Delivery Software Framework (PSF) is an open source Power Delivery stack designed to be integrated into any suitably powerful MCU and is used to control one or more UPD350 PD controllers within a USB Type-C Power Delivery System.

This document gives an overview of the software and hardware tools required for the PSF Firmware to work properly with PSF-EVB. It also provides information on how to build the Firmware and program the hex file in the PSF-EVB.

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4 PSF Overview

- The USB Power Delivery Software Framework (PSF) with USB-PD Port Controller UPD350 is an effective USB-PD solution which offers the user the ability to customize functionality to meet individual application needs.
- The PSF stack is highly portable and designed to run on different MCU Hardware platforms. Versatility towards different HW platforms is achieved through the flexibility and configurability of the PSF stack.
- The PSF stack is compliant to USB Power Delivery 3.0 & Type-C specification v1.3
- PSF supports single and multi-port solutions.

PSF User Guide

- The ‘PSF User Guide’ gives a detailed overview of the PSF stack architecture, directory structure, Supported and Not Supported messages and the various configuration options that are available in PSF.
- The User Guide also provides information about the requirements and steps for integration of PSF into a new platform.
- PSF User Guide is available at <https://github.com/MicrochipTech/usb-pd-software-framework/tree/master/Docs>

5 Prerequisites for PSF

The following are the prerequisites for PSF to work with the PSF-EVB or a Microchip MCU. To port PSF to any non-Microchip MCU the appropriate IDE, compiler and other software tools shall be used. Regardless of MCU chosen one UPD350 PD Controller is required per port.

- [MPLAB X IDE v5.30](#) or later
- [MPLAB XC32 compiler](#)
- [Atmel ICE Debugger](#)

Follow these steps for setting up the build environment needed for PSF Firmware.

1. MPLAB X IDE can be downloaded and installed from *Downloads* tab located at the bottom of <https://www.microchip.com/mplab/mplab-x-ide>
For any IDE specific information, refer the MPLAB X IDE Release Notes and User Guide of the installed version listed in the same page.
2. MPLAB XC32/32++ Compiler can be downloaded and installed from *Downloads* section located at the bottom of <https://www.microchip.com/mplab/compilers>

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For any compiler specific information, refer the *Compiler User's Guide for PIC32C/SAM MCUs* listed in the *XC32 Documents* tab of the same page.

6 Building the Project

Follow these steps for building the PSF Project and generating the hex file. Building the project is demonstrated here by having “PSF_EVB_Source_Lite” project as an example. The similar approach can be followed for Source Pro, Sink, DRP and FRS projects.

1. Download PSF Firmware from <https://github.com/MicrochipTech/usb-pd-software-framework> using *Clone or Download* option. All the sub folders must be retrieved for the project to build successfully.
2. Extract the downloaded zip file usb-pd-software-framework-master.zip.
3. The entire local folder path containing the PSF source code shall have no space in the folder names.
For ex: Instead of *C:\Users\\Desktop\PSF FW\PSF*, use *C:\Users\Desktop\PSF_FW\PSF*.
4. Remove Read-Only option for the folder by right click -> Properties -> Uncheck Read Only -> Apply -> OK
5. Open MPLAB X IDE and click File -> Open Project.
Enter the path {Local_Folder}\PSF\Demo\PSF_EVB_Source_Lite\firmware which contains PSF_EVB_Source_Lite.X

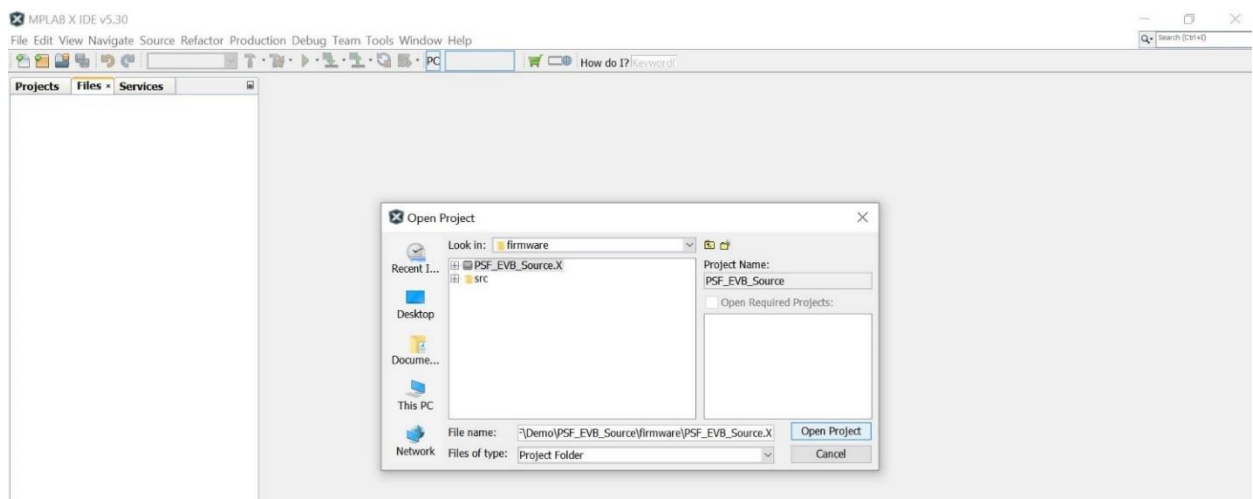


Figure 6.1 Opening project in MPLAB X IDE

6. The IDE may throw a configuration load error if the compiler toolchain is not properly linked to the project. With this error, building of the project cannot proceed.

warning: Configuration "default" builds with "XC32", but indicates no toolchain directory.
error: Configuration "default" builds with "XC32", but no toolchains of that type are installed.
Errors have occurred while loading one or more configurations.

If a specific error is not shown above, this may happen when you import a project from another computer.

- + You can add language tools in Tools->Options embedded tab.
- + You can change which language tool to use in the project properties dialog.

To resolve this, click Tools -> Options -> Embedded -> Build Tools -> Scan for Build Tools
Once done, all the compiler toolchains installed in your PC should be listed under Toolchain.

7. Choose the XC32 compiler installed by you and click Apply -> OK Once done, Compiler Toolchain will be properly listed in the project Dashboard as shown in the figure.

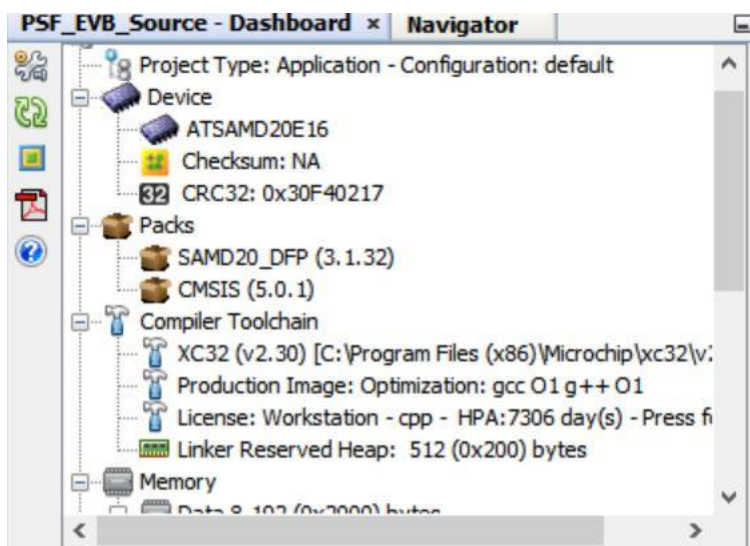



Figure 6.2 Project Dashboard in MPLAB X IDE

8. Make the project as main project by right clicking on the project name (PSF_EVB_Source_Lite) and selecting *Set as Main Project*.
9. Follow the steps mentioned in [Appendix 8.1](#) in case you are using a non-professional XC32 compiler version.
10. Right click on the project and select *Clean and Build* or use  icon at the top of IDE to build the firmware.
11. Once the build completes, a *Build Successful* message will be shown in the output window of the IDE.
12. Generated Hex file can be found under the path

{Local_Folder}\PSF\Demo\PSF_EVB_Source_Lite\firmware\PSF_EVB_Source_Lite.X\dist\default\production

7 Programming

Follow these steps to program the generated hex file in the EVB

1. After building the project by following the steps mentioned in section 6 above, take a Micro B to USB A cable and connect Micro B side to *DEBUG USB* marked on the board and connect the other end to the PC. Or Connect Atmel ICE Debugger to the PC. Connect the other end of debugger to the *ICE I/F or DGB I/F* marked on the board. A dot will be present in Atmel ICE which gives an indication that this pin should be connected to 3v3 of *ICE I/F or DGB I/F*.
2. Right click on the project, select properties, and make sure to select Atmel ICE below *Hardware tools* and click ok

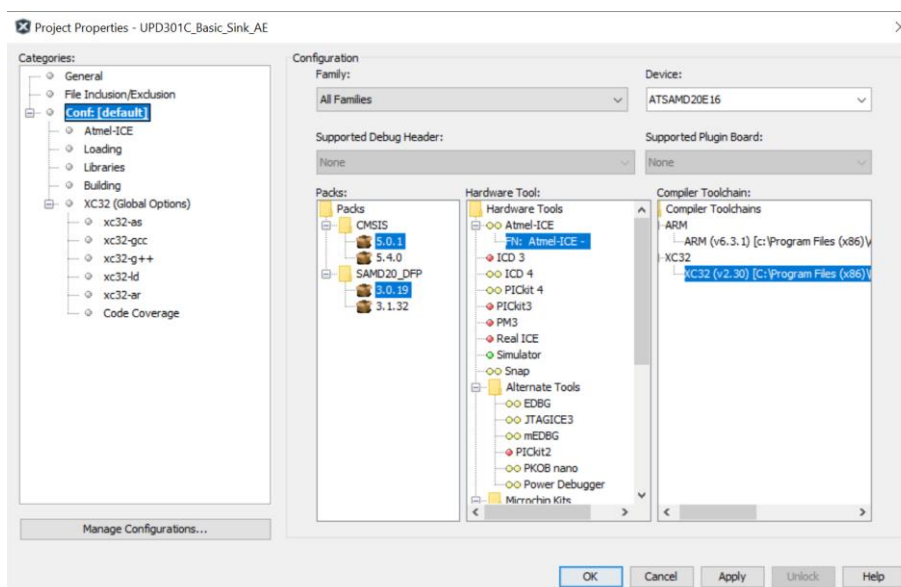



Figure 7.2 MPLAB IDE with Atmel-ICE detected



3. Power on the PSF-EVB and click  or *Make and Program device main project* in the IDE.
4. Once the hex file is programmed successfully, the output window of the IDE will show the following message.


```

PCLint x Configuration Loading Error x Project Loading Warning x UPD301C_Basic_Sink_AE (Build, Load, ...) x Atmel-ICE x

Currently loaded versions:
Application version.....1.41.137 (0x01.0x29.0x89)
Target voltage detected

The following memory area(s) will be programmed:
program memory: start address = 0x0, end address = 0xe5ff
configuration memory

Programming complete
|

```

Figure 7.3 IDE Output Window

8 Appendix

8.1 Non-Professional XC32 Compiler Edition

If the PSF firmware is built with a non-professional XC32 compiler, build errors may occur because of compiler optimization issues. In case you are using a non-professional XC32 compiler and want the build process to be error free and successful, please follow these steps. Refer the image shown below for easier navigation.

1. Set the project as the main project by right clicking on the project and selecting *Set as Main Project*.
2. Click File -> Project Properties
3. Choose xc32-gcc under XC32(Global Options) and select *Optimization* under Option Categories.
4. Select optimization-level as 1 or 2 while building project using Non-Professional Compiler to avoid errors.

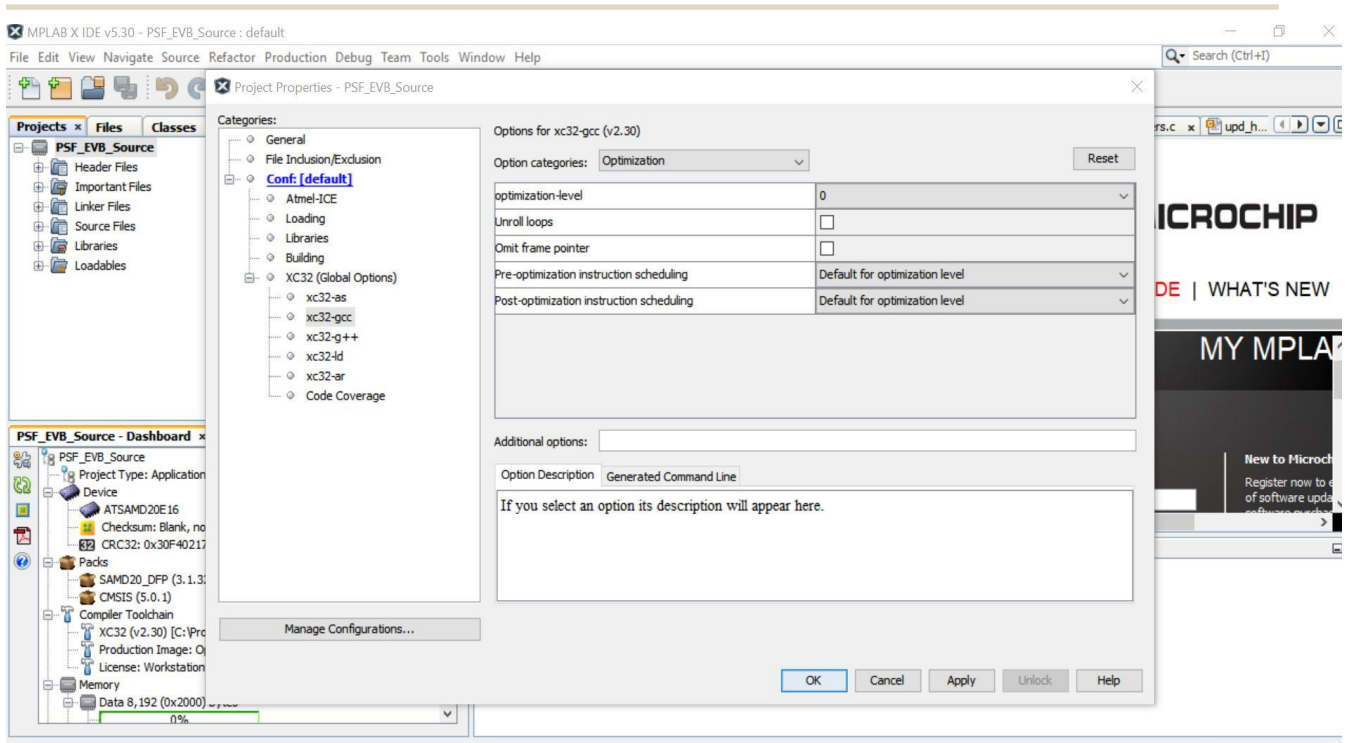


Figure 8.1 Optimization settings in MPLAB X IDE

8.2 Harmony Framework Usage

1. Download MPLAB Harmony Configurator 3 by choosing Tools -> Plugins -> Available Plugins -> MPLAB Harmony 3 Configurator -> Install in the MPLAB X IDE.

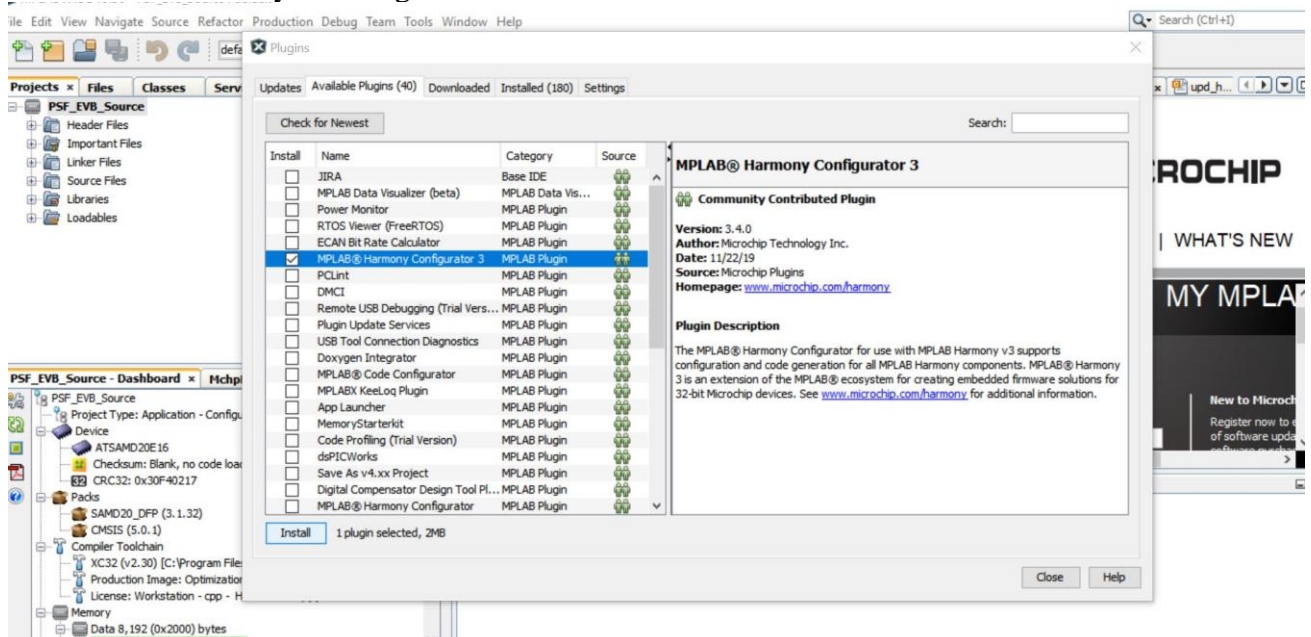


Figure 8.2 Harmony Framework installation

2. To download basic Harmony Configuration, Go to Tools -> Embedded -> MPLAB Harmony 3 Content Manager

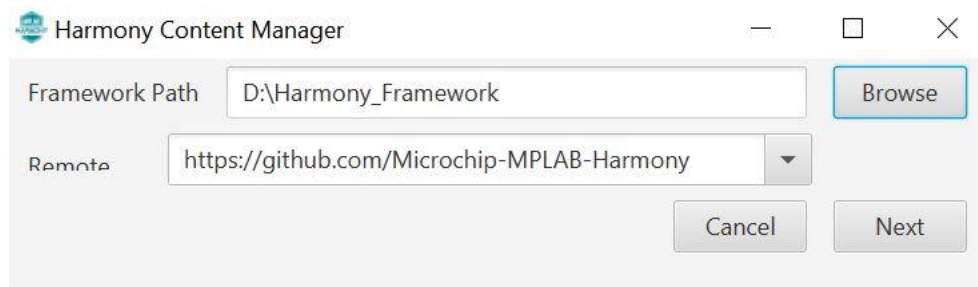


Figure 8.3 Harmony Content Manager

3. Choose a local *Framework Path* with a new folder created explicitly for downloading harmony packages, say *Harmony_Framework*. If the folder has any other local files, user might face difficulties in downloading the packages.
4. Choose GitHub Harmony path for *Remote* and click *Next*. The local Framework Path shall not have any spaces in the folder name.
The following screen will appear indicating that the Connection was successful.

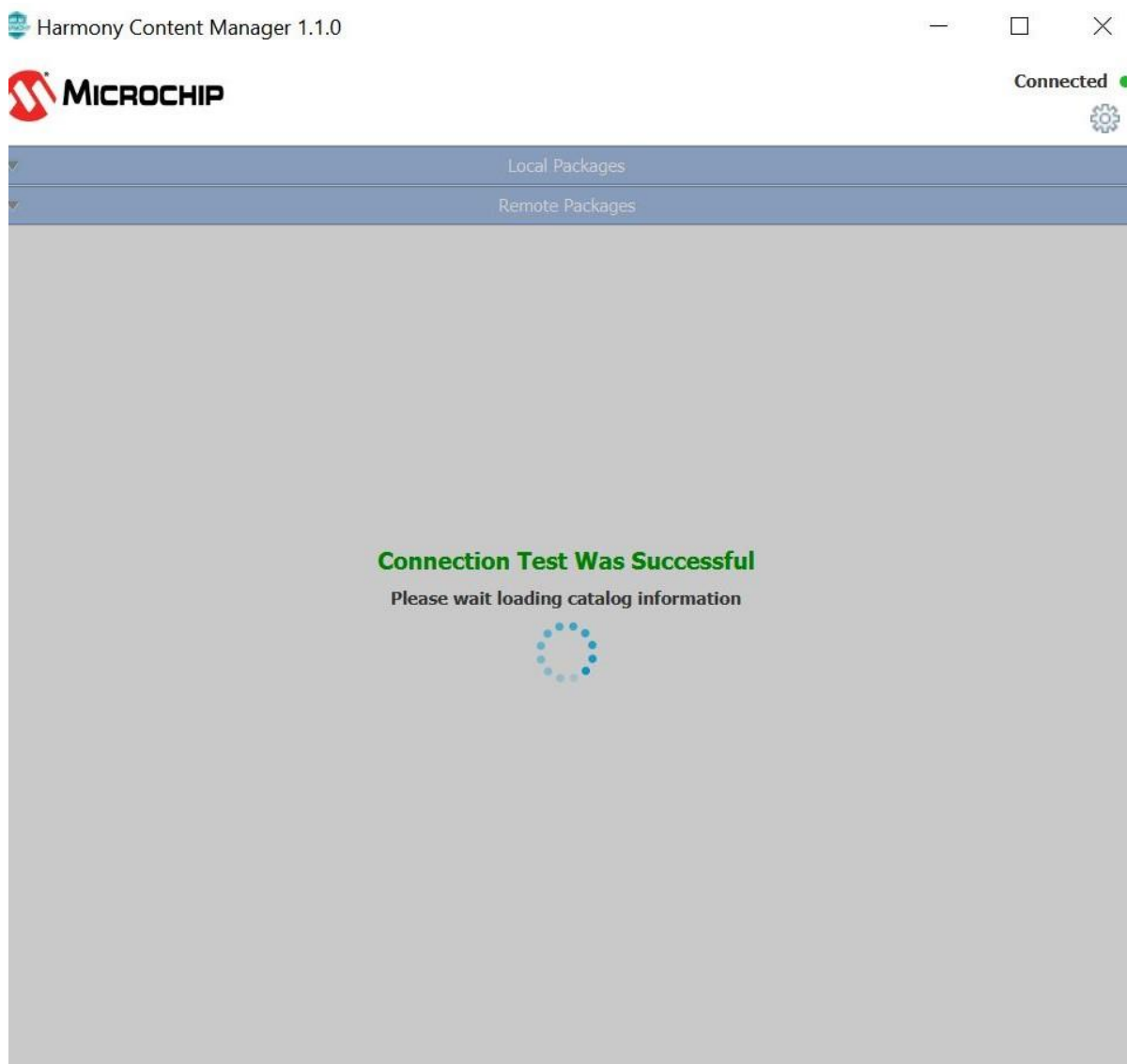


Figure 8.4 Harmony Content Manager with Connection Successful message

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5. Select the Remote Packages – ‘mhc, dev_packs, bsp and csp’ and click *Download selected*.

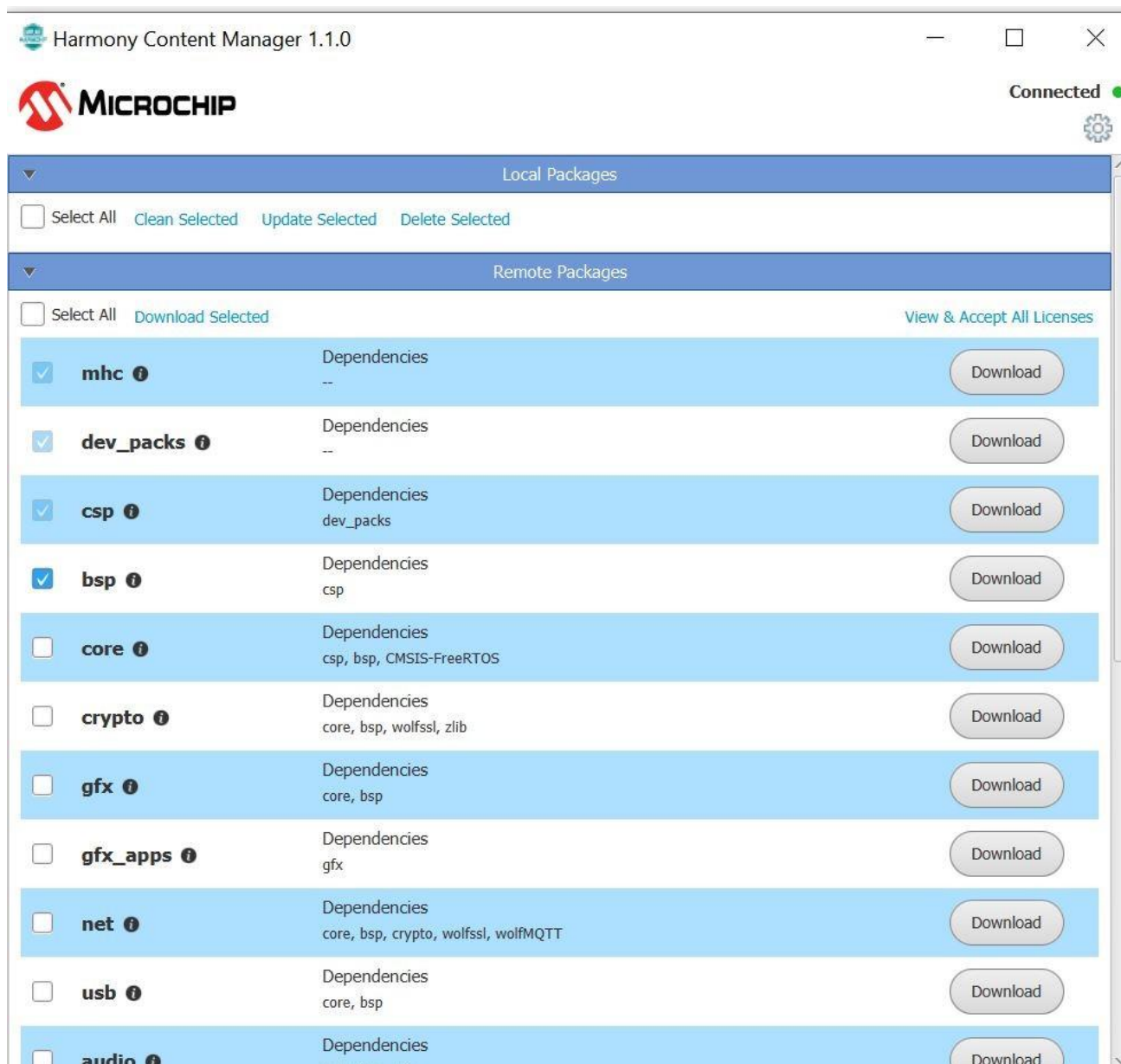


Figure 8.5 Remote Packages in Harmony Content Manager

6. A License Window will pop up. Read the license terms and accept before proceeding to the next step. Close the window.

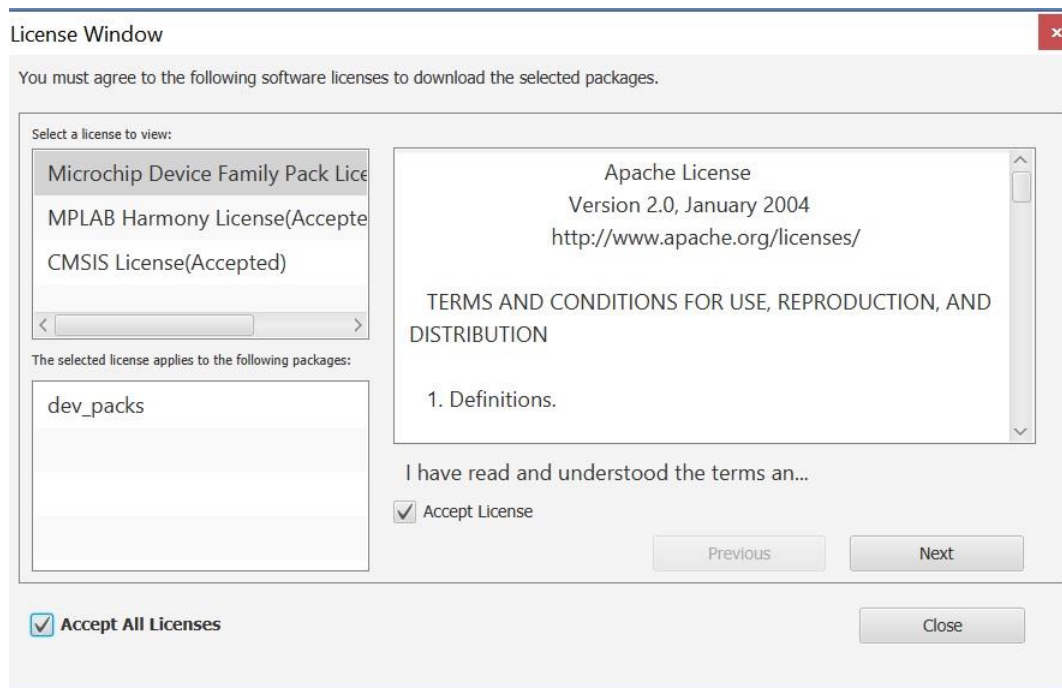


Figure 8.6 License Agreement

7. Current Download Status will be shown until remote packages are downloaded to local.

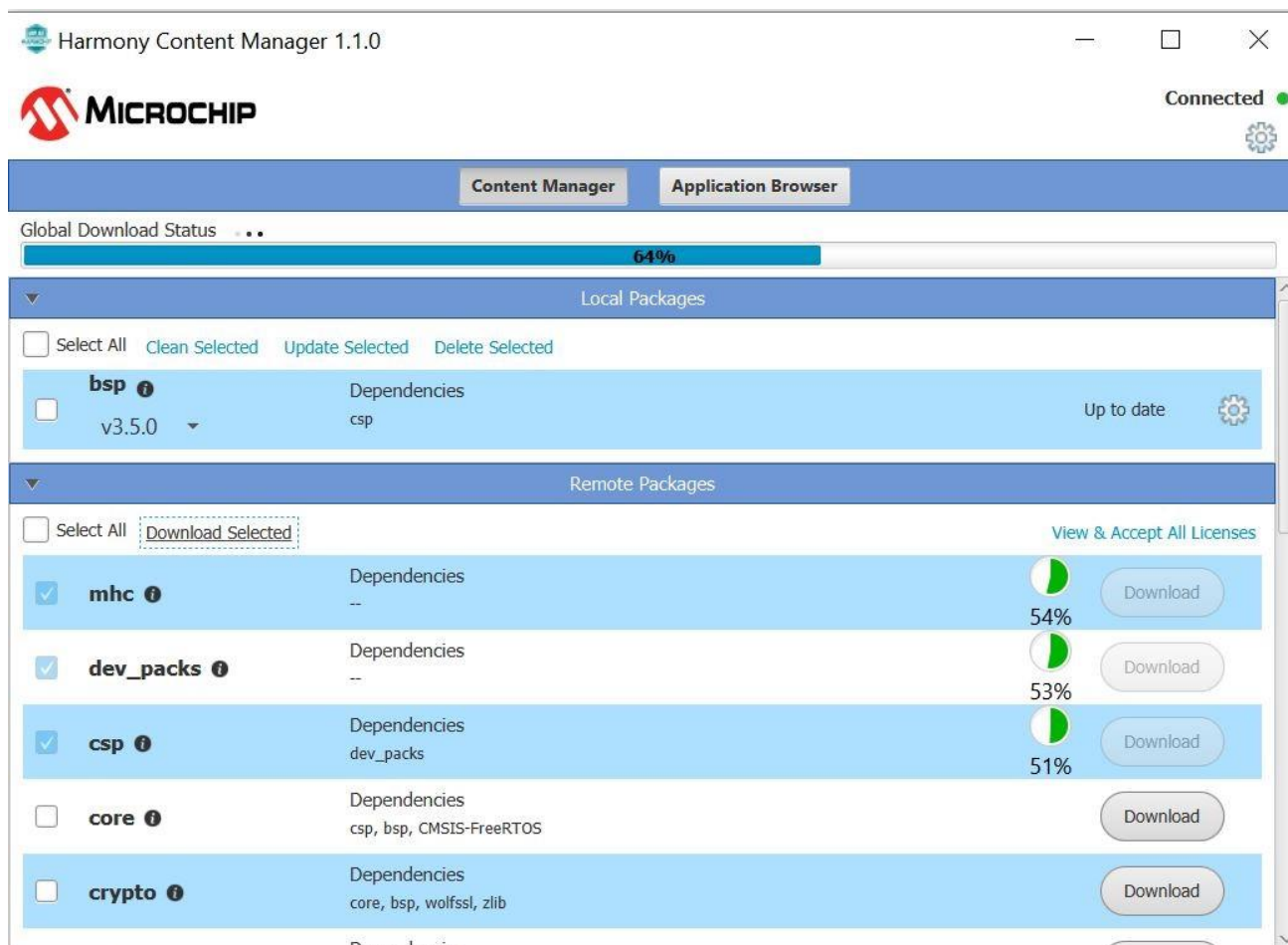


Figure 8.7 Download status of Packages

8. Once all the remote packages are downloaded, they will be listed under *Local Packages* section.

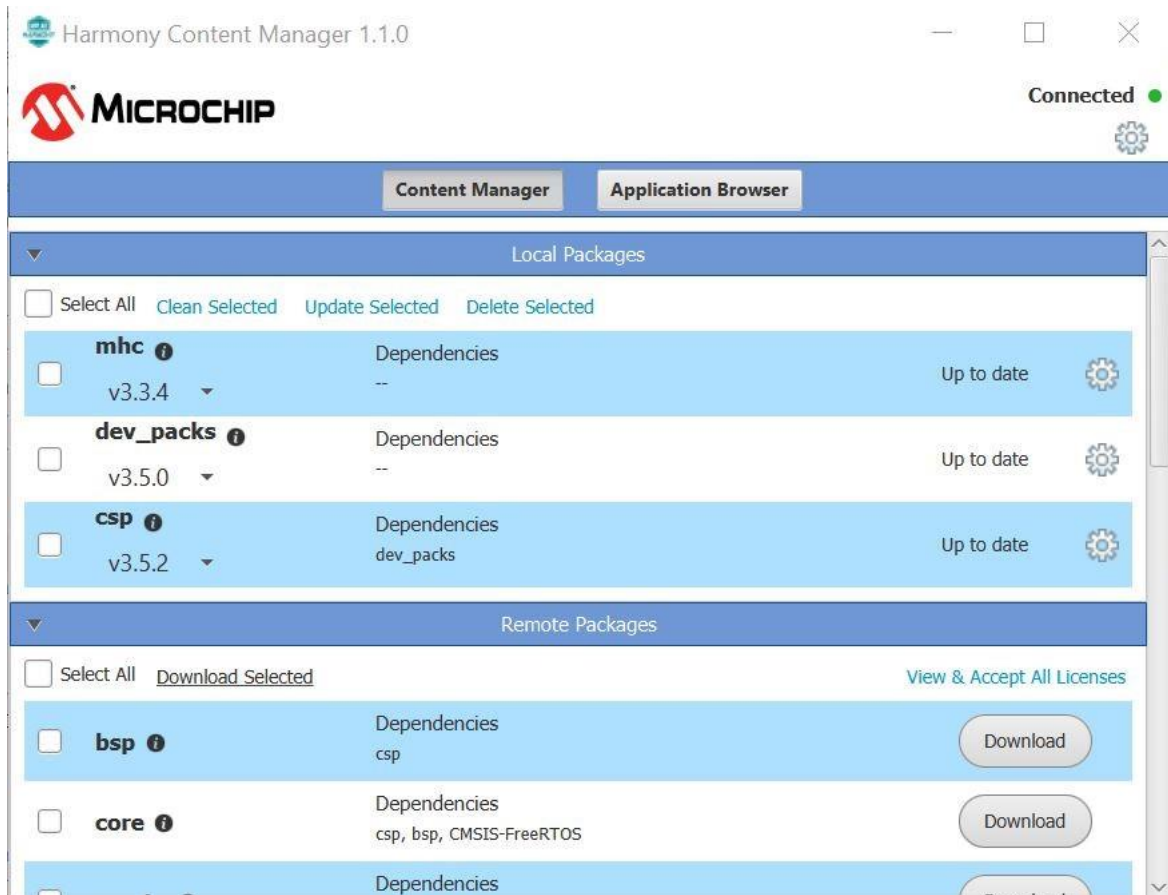


Figure 8.8 Harmony Content Manager with packages downloaded

9. To launch Harmony 3 Configurator, choose Tools -> Embedded -> MPLAB Harmony 3 Configurator

10. Select *MPLAB Harmony Project Path* and choose *Reconfigure Paths*

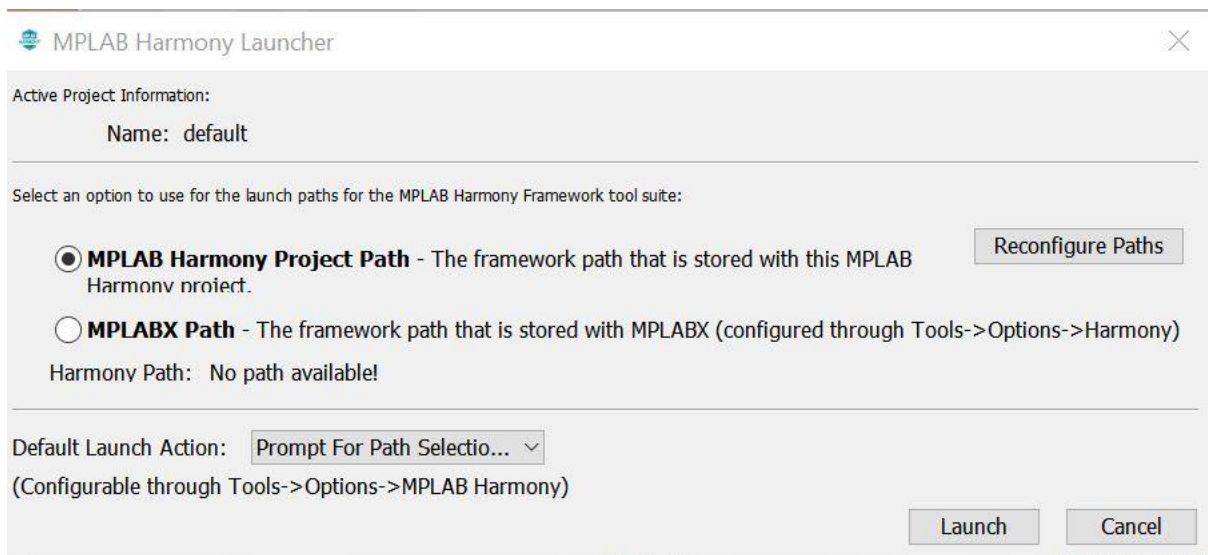


Figure 8.9 MPLAB Harmony Launcher

11. Choose the Harmony Framework path by clicking the folder icon(Green box) and click Ok

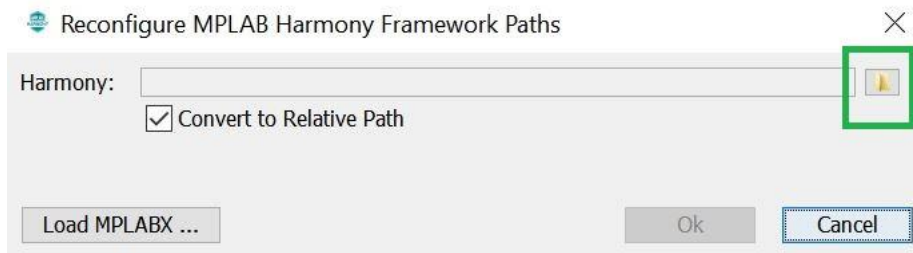


Figure 8.10 MPLAB Harmony Framework Paths

12. Click *Launch*.

13. *Window Manager Selection Dialog* window will open. Select Native window management mode if you want the Harmony tabs to be embedded into MPLABX IDE else choose *Standalone* mode for separate MHC window. This will be prompted during first time only.

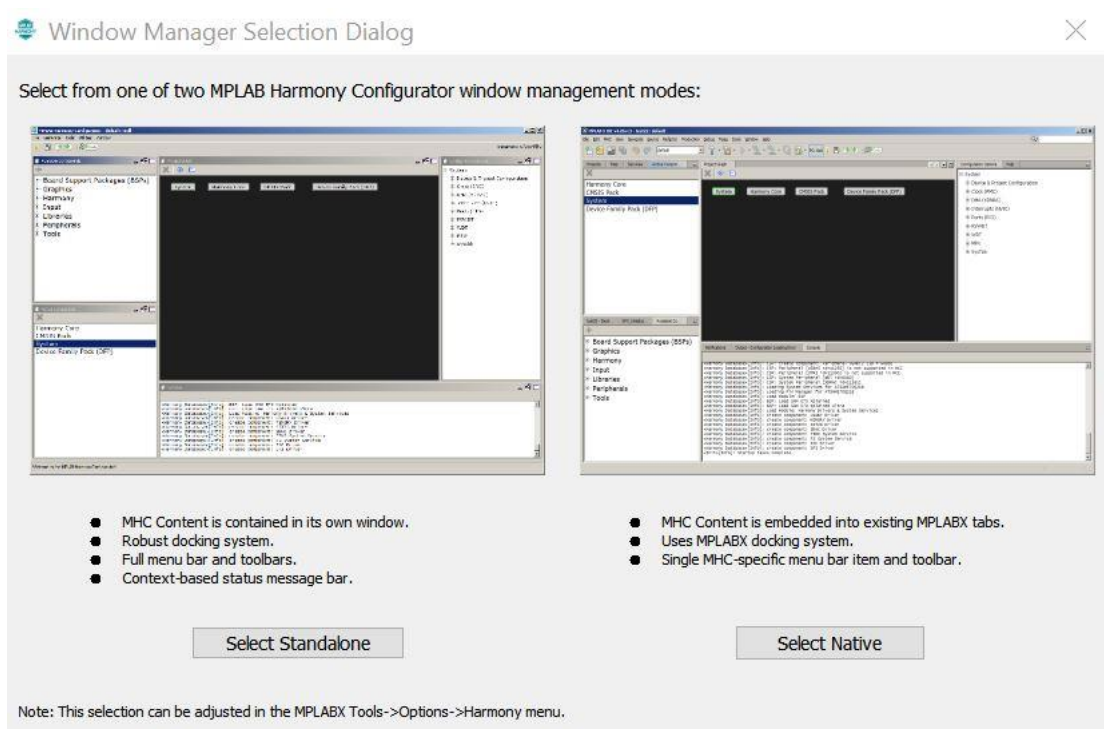


Figure 8.11 Window Manager Selection Dialog

14. After having an overview of Native Window Manager Quick Tour, click *I Understand*. This will also appear for the first time only

15. *Configuration Database Setup* page appears with DFP and CMSIS path shown by default. Click *Launch*.

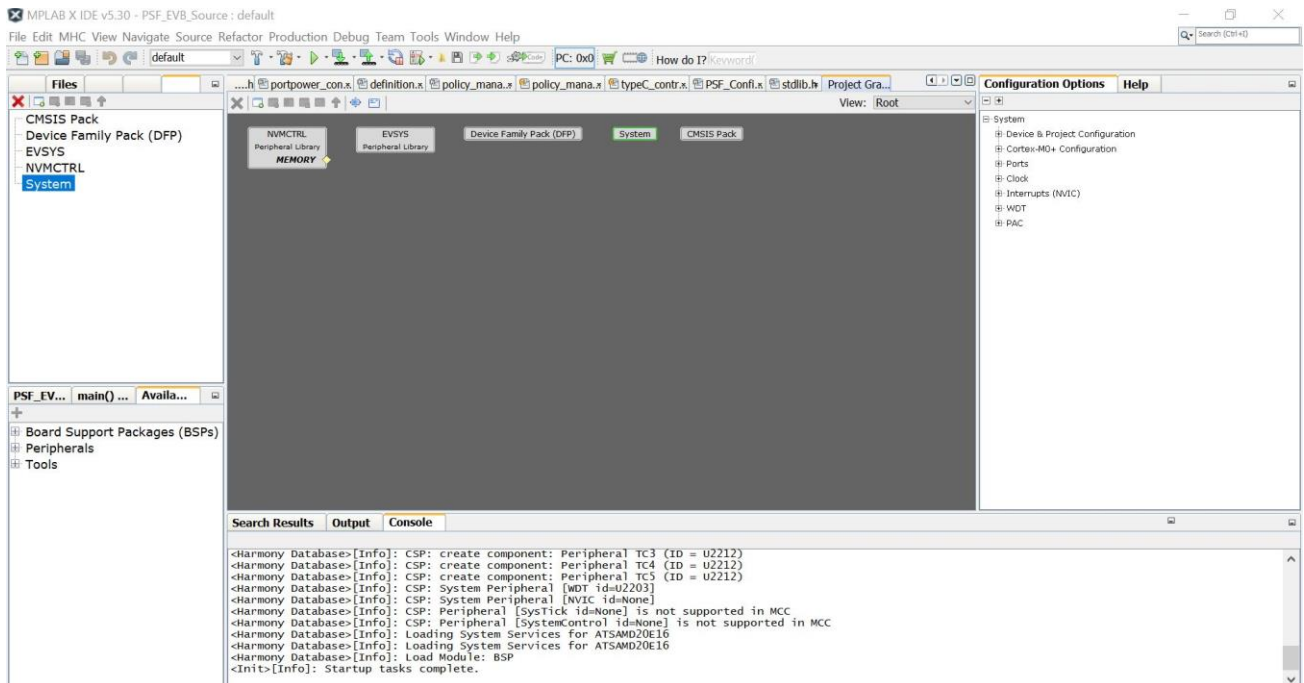


Figure 8.12 Configuration Database Setup

16. Load the Harmony Configuration File by choosing MHC -> Load State. Give the file path as {Local_Path}\PSF\PSF\Demo\PSF_EVB_Source\firmware\src\config\default\SAMD20_PSFHarmonyConfiguration.xml

17. Project Graph will be shown with the peripherals and any changes can be done through *Configuration Options* page

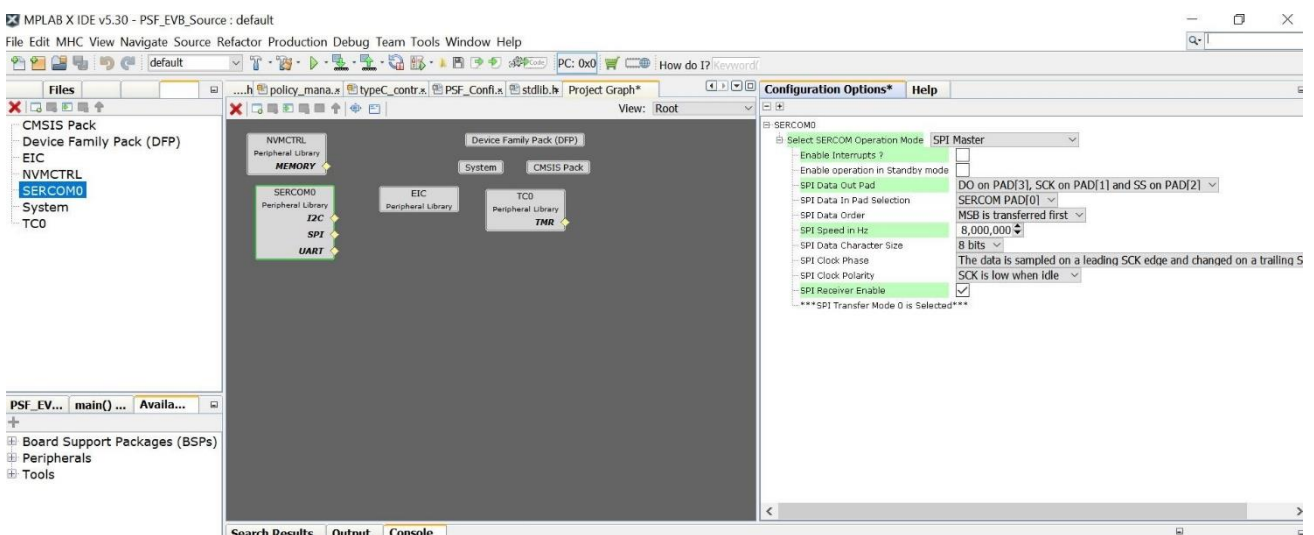


Figure 8.13 Configuration Options Page

Refer MPLAB Harmony Release Notes for any clarifications/settings specific to MHC from <https://www.microchip.com/mplab/mplab-harmony>

For more information on how to use MHC, refer <https://github.com/Microchip-MPLAB-Harmony/mhc/wiki>