

Integrated Circuits Design (積體電路設計)

Tzi-Dar Chiueh (闕志達)

Fall, 2019

Topics

1. Introduction
2. Devices and Layout
3. Speed (Delay)
4. Power
5. Wires (Interconnect)
6. Gates (Combinational Circuit Design)
7. Sequencing (Sequential Circuit Design)
8. Datapaths
9. Memories
10. Power/Clock Distribution, I/O, Packaging

Text

N. H. E. Weste and D. Harris, *Integrated Circuits Design*, 4th Ed., 2011.

Grading

Homework 40 %, Midterm 30 %, and Final 30 %.

Course Webpage: https://ceiba.ntu.edu.tw/1081EE3020_icdesign



上課時間: 13:20AM --- 14:10AM ; 14:20AM --- 15:10AM ; 15:20AM --- 16:10AM 。