

## 23/07/2024 08:32:15 Mockup Connector Board Checking (2 tests).

Test Description	Reading	Lower Range	Upper Range	Result
Reference Voltage 1	4091	4014	4179	PASS
Measured Reference Voltage 1	4097	4014	4179	PASS
Comms	OK	-	-	PASS

## Isolated DCDC Checking (24 tests).

Test Description	Reading	Lower Range	Upper Range	Result
Power supply PCB DUT – TP10	2880	2592	3168	PASS
Input power supply isolated DCDC – TP15	2871	2579	3158	PASS
Output isolated DCDC without load – TP16	3813	3423	4533	PASS
Current supply DUT	12	9	17	PASS
Output isolated DCDC with load – TP16	3305	2900	3636	PASS
Current supply DUT	46	40	54	PASS
Power supply PCB DUT – TP10	2983	2680	3281	PASS
Input power supply isolated DCDC – TP15	2973	2671	3270	PASS
Output isolated DCDC without load – TP16	3955	3546	4759	PASS
Current supply DUT	26	21	33	PASS
Output isolated DCDC with load – TP16	3422	3010	3770	PASS
Current supply DUT	54	47	64	PASS
Power supply PCB DUT – TP10	3081	2773	3389	PASS
Input power supply isolated DCDC – TP15	3071	2759	3378	PASS
Output isolated DCDC without load – TP16	4091	3665	4947	PASS
Current supply DUT	28	23	35	PASS
Output isolated DCDC with load – TP16	3544	3119	3904	PASS
Current supply DUT	57	50	67	PASS
Power supply PCB DUT – TP10	3183	2860	3501	PASS
Input power supply isolated DCDC – TP15	3168	2848	3490	PASS
Output isolated DCDC without load – TP16	4223	3784	5134	PASS
Current supply DUT	29	23	37	PASS
Output isolated DCDC with load – TP16	3671	3225	4038	PASS
Current supply DUT	59	51	69	PASS

## Checking the external inputs (4 tests).

Test Description	Reading	Lower Range	Upper Range	Result
External input 1 Disable – TP1	4995	4496	5495	PASS
External input 1 Enable – TP1	1508	1200	1610	PASS
External input 2 Disable – TP2	4995	4496	5495	PASS
External input 2 Enable – TP2	1469	1200	1610	PASS

## Checking the low voltage transil (20 tests).

Test Description	Reading	Lower Range	Upper Range	Result
Test 5V Off - Bootloader Transmission TP13	0	0	25	PASS
Test 5V On - Bootloader Transmission TP13	4915	4424	5407	PASS
Test 5V Off – Bootloader Reception TP14	0	0	25	PASS
Test 5V On - Bootloader Reception TP14	4929	4436	5422	PASS
Test 5V Off – External switch enable 1 TP17	0	0	25	PASS
Test 5V On – External switch enable 1 TP17	4929	4436	5422	PASS
Test 5V Off – External switch enable 2 TP18	0	0	25	PASS
Test 5V On – External switch enable 2 TP18	4929	4449	5437	PASS
Test 5V Off – External Reset TP19	0	0	25	PASS
Test 5V On – External Reset TP19	4929	4436	5437	PASS
Test 12V Off - Bootloader Transmission TP13	0	0	25	PASS
Test 12V On - Bootloader Transmission TP13	6900	6151	7604	PASS
Test 12V Off - Bootloader Reception TP14	0	0	25	PASS
Test 12V On - Bootloader Reception TP14	6955	6151	7651	PASS
Test 12V Off – External switch enable 1 TP17	0	0	25	PASS
Test 12V On – External switch enable 1 TP17	11659	10493	12825	PASS
Test 12V Off – External switch enable 2 TP18	0	0	25	PASS
Test 12V On – External switch enable 2 TP18	11670	10503	12852	PASS
Test 12V Off – External Reset TP19	0	0	25	PASS
Test 12V On – External Reset TP19	6872	6151	7635	PASS

**Checking the high voltage transil (32 tests).**

Test Description	Reading	Lower Range	Upper Range	Result
Test 42V Off – Transil Electrostimulation Channel C Negative TP3	0	0	25	PASS
Test 42V On – Transil Electrostimulation Channel C Negative TP3	41043	36823	45076	PASS
Test 42V Off – Transil Electrostimulation Channel C Positive TP4	0	0	25	PASS
Test 42V On – Transil Electrostimulation Channel C Positive TP4	40978	36776	45005	PASS
Test 42V Off – Transil Electrostimulation Channel B Negative TP5	0	0	25	PASS
Test 42V On – Transil Electrostimulation Channel B Negative TP5	40733	36602	44806	PASS
Test 42V Off – Transil Electrostimulation Channel B Positive TP6	0	0	25	PASS
Test 42V On – Transil Electrostimulation Channel B Positive TP6	41108	36939	45219	PASS
Test 42V Off – Transil Electrostimulation Channel A Negative TP7	0	0	25	PASS
Test 42V On – Transil Electrostimulation Channel A Negative TP7	40978	36776	45005	PASS
Test 42V Off – Transil Electrostimulation Channel A Positive TP8	0	0	25	PASS
Test 42V On – Transil Electrostimulation Channel A Positive TP8	40914	0	45005	PASS
Test 42V Off – Transil Galvanic Negative TP11	0	0	25	PASS
Test 42V On – Transil Galvánica Negativo TP11	40978	36823	45076	PASS
Test 42V Off – Transil Galvánica Positivo TP12	0	0	25	PASS
Test 42V On – Transil Galvanic Positive TP12	40914	36718	44948	PASS
Test 59V Off – Transil Electrostimulation Channel C Negative TP3	0	0	25	PASS
Test 59V On – Transil Electrostimulation Channel C Negative TP3	55366	46644	59723	PASS
Test 59V Off – Transil Electrostimulation Channel C Positive TP4	0	0	25	PASS
Test 59V On – Transil Electrostimulation Channel C Positive TP4	55236	46249	59652	PASS
Test 59V Off – Transil Electrostimulation Channel B Negative TP5	0	0	25	PASS
Test 59V On – Transil Electrostimulation Channel B Negative TP5	53725	45796	59582	PASS
Test 59V Off – Transil Electrostimulation Channel B Positive TP6	0	0	25	PASS
Test 59V On – Transil Electrostimulation Channel B Positive TP6	55366	46644	60135	PASS
Test 59V Off – Transil Electrostimulation Channel A Negative TP7	0	0	25	PASS
Test 59V On – Transil Electroestimulación Canal A Negative TP7	55107	46481	59723	PASS
Test 59V Off – Transil Electrostimulation Channel A Positive TP8	0	0	25	PASS
Test 59V On – Transil Electrostimulation Channel A Positive TP8	54978	0	59993	PASS
Test 59V Off – Transil Galvanic Negative TP11	0	0	25	PASS
Test 59V On – Transil Galvanic Negative TP11	53842	46703	59851	PASS
Test 59V Off – Transil Galvanic Positive TP12	0	0	25	PASS
Test 59V On – Transil Galvanic Positive TP12	54733	46133	59652	PASS

**23/07/2024 13:28:02 Mockup Connector Board Checking (2 tests).**

Test Description	Reading	Lower Range	Upper Range	Result
Reference Voltage 1	4096	4014	4179	PASS
Measured Reference Voltage 1	4097	4014	4179	PASS
Comms	OK	-	-	PASS

**All tests performed with the overall result of PASS**