

Mihir Dixit

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EDUCATION

VIT , Vellore — BTech ECE (3rd year,5th Semester) – 8.77 CGPA

2021 - 2025

- **Coursework:**
 - Digital Signal processing
 - Control Systems
 - Analog Communication Systems
 - Digital Communication Systems
 - VLSI Systems
- **Technical Club Activities:**
 - Vice Captain and Co-Mechatronics head of ASME projects Team - VIT (May 2023 - present)
 - Member of Aerodynamics Department of ASME Projects Team -VIT (August 2022 - May 2023).
- **Competitions / Achievements:**
 - Represented VIT in IIT- Roorkee's Aeromodelling Competition - Flight Fury (March 2023).
 - Developing various rc-planes for competitions like Tathva(NIT- Calicut), Aerodominator(VIT-Vellore)
 - Secured 2nd place in hackathon in "Role of Engineers for Sustainable Cooling towards Conference of Parties (COP) 28" an Industry Academia Summit(June 2023)
- **Projects:**
 - Ongoing Research paper under Prof. Malaya Kumar Hota(SENSE Dept VIT Vellore).
 - Research aims to perform various analysis on the ECG signal filtering techniques, resulting in enhancing the quality of filter output.

SKILLS

- Matlab
- Python
- Java
- C
- Data Structures and Algorithms

Geetanjali College , Bengaluru — 12th

2019 - 2021

- 88% in Boards
- 92 percentile in JEE Mains

New Horizon Public School, Bengaluru — 10th

2008 - 2019

- 95% in Boards

INTERNSHIPS

- Organization : TSC Technologies Pvt. Ltd.
 - Duration : 2.5 months
 - Work Description : Worked in development of architecture of Embedded Systems, MCU's like ESP32, DAC, NRF24L01+. Worked with ESP-IDF and interfacing with SPI communication protocol