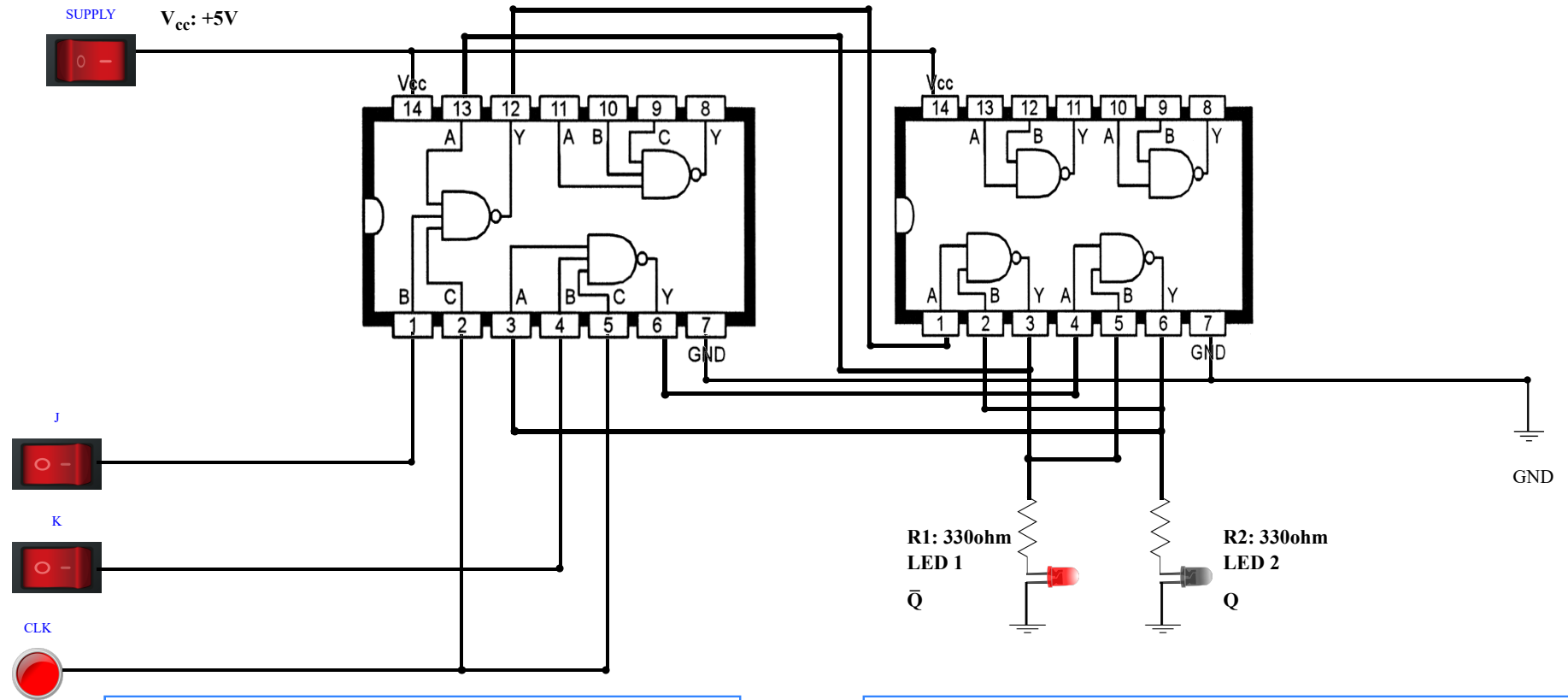


INSTRUCTIONS

Experiment to perform logic of JK FLIP FLOP on kit



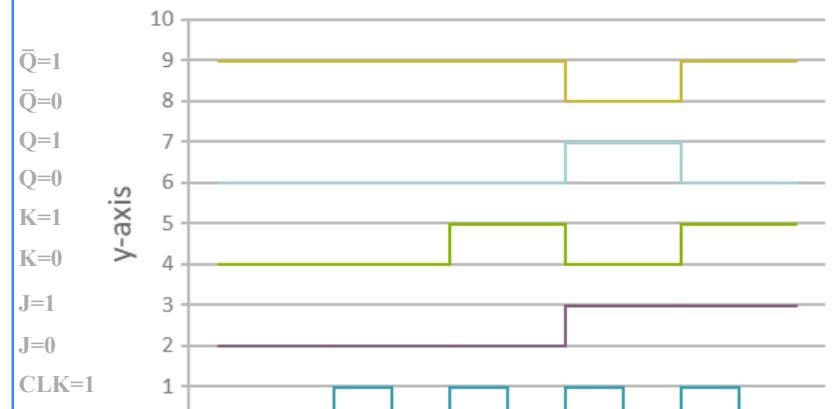
TRUTH TABLE

PRINT

Add

Serial No.	clock	J	K	Q(n-1)	$\bar{Q}(n-1)$	Q	\bar{Q}	Remark
1	0	0	0	X	X	0	1	No Change
2	1	0	0	0	1	0	1	No change
3	1	0	1	0	1	0	1	Reset
4	1	1	0	0	1	1	0	set
5	1	1	1	1	0	0	1	toggle

CLOCK DIAGRAM



CLK=0

