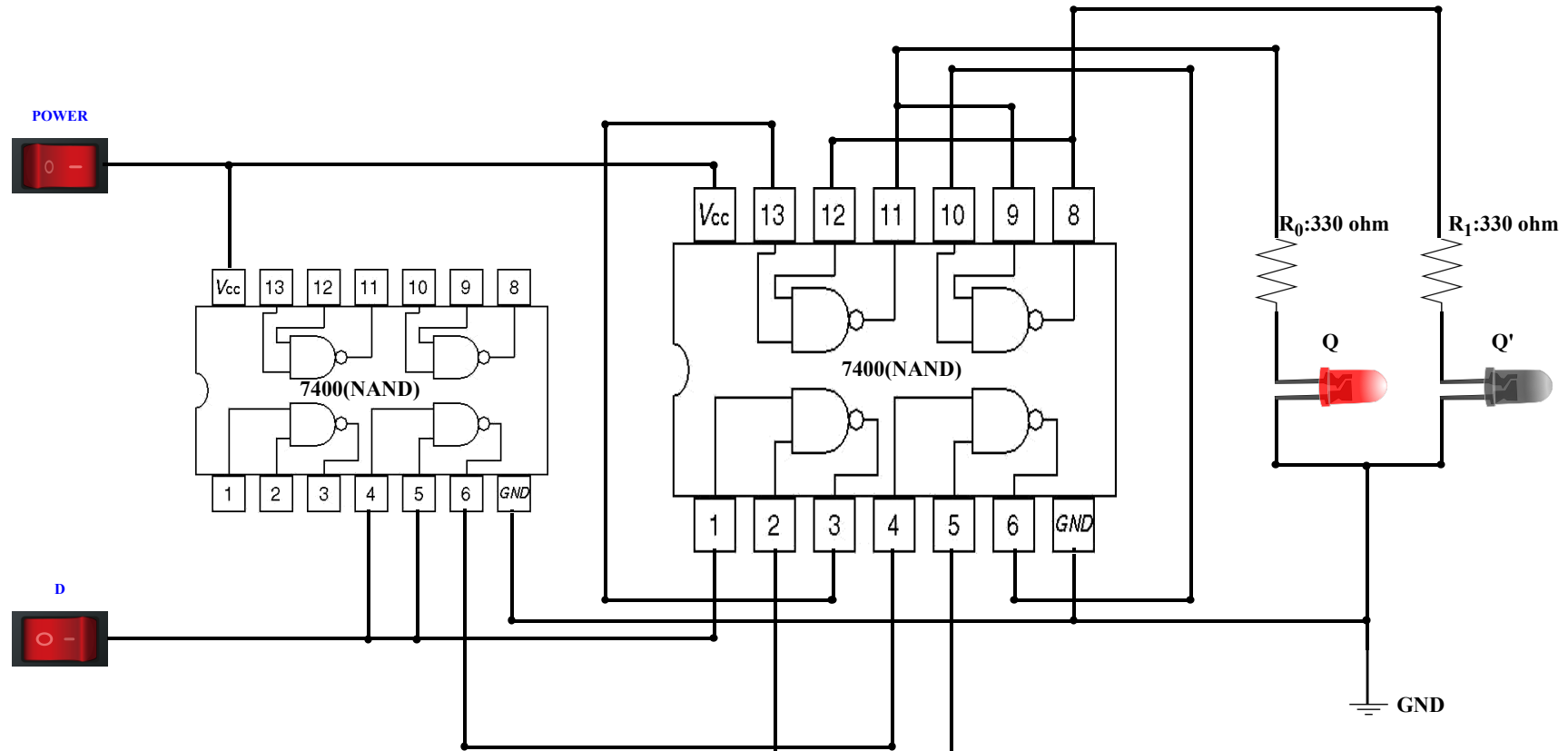


## INSTRUCTIONS

## Experiment to perform logic of D - Flipflop on kit



TRUTH TABLE

PRINT

Add

Serial No.	clock	D	Q(n-1)	Q'(n-1)	Q	Q'	Remark
1	0	0	X	X	0	1	No Change
2	0	1	0	1	0	1	No change
3	1	0	0	1	0	1	Reset
4	1	1	0	1	1	0	set

CLOCK DIAGRAM



