Verification of truth table for AND gate

1

1

A

B

out

1

Check
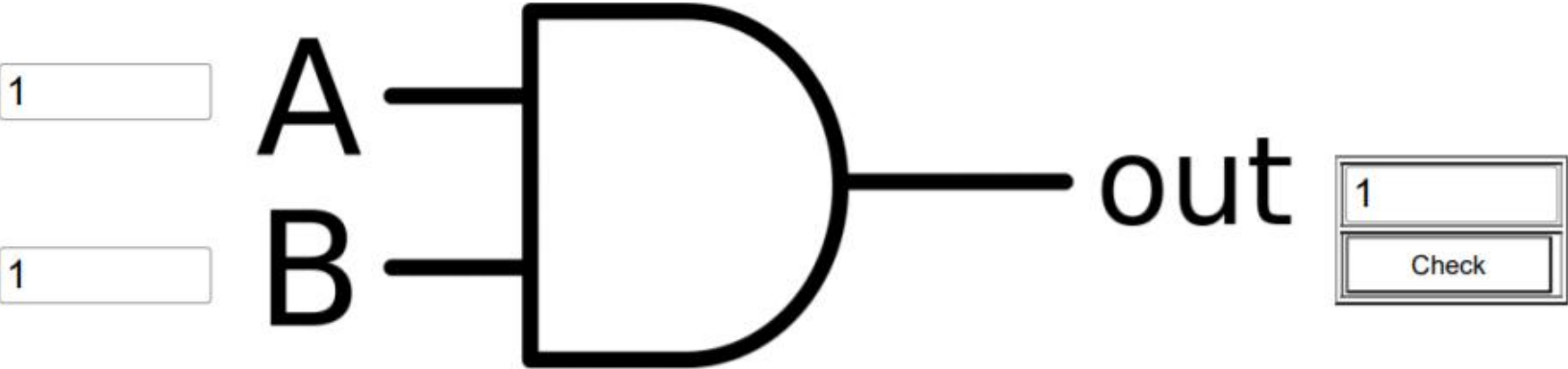
TRUTH TABLE

Print

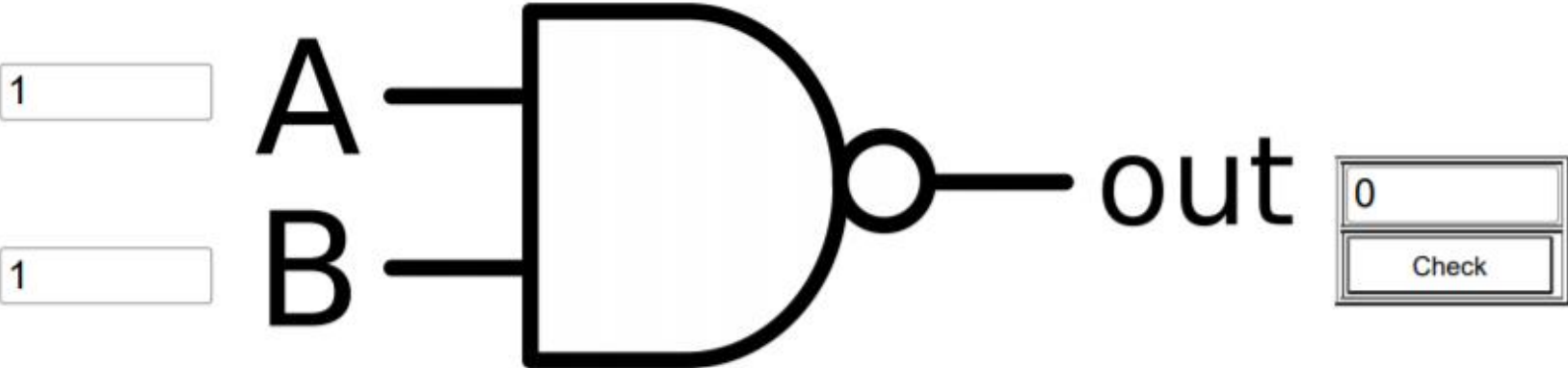| Serial No.	A	B	Output	Remarks
1	0	0	0	Correct
2	0	1	0	Correct
3	1	0	0	Correct
4	1	1	1	Correct

Reset

Verification of truth table for NAND gate

1

A

B

1

out

0

Check

	TRUTH TABLE			Print
Serial No.	A	B	Output	Remarks
1	0	0	1	Correct
2	0	1	1	Correct
3	1	0	1	Correct
4	1	1	0	Correct

Reset

Verification of truth table for NOR gate

1

A

B

1

Q

0

Check

Print

Serial No.	A	B	Output	Remarks
1	0	0	1	Correct
2	0	1	0	Correct
3	1	0	0	Correct
4	1	1	0	Correct

Reset

Verification of truth table for NOT gate

A   1

out   0

Check
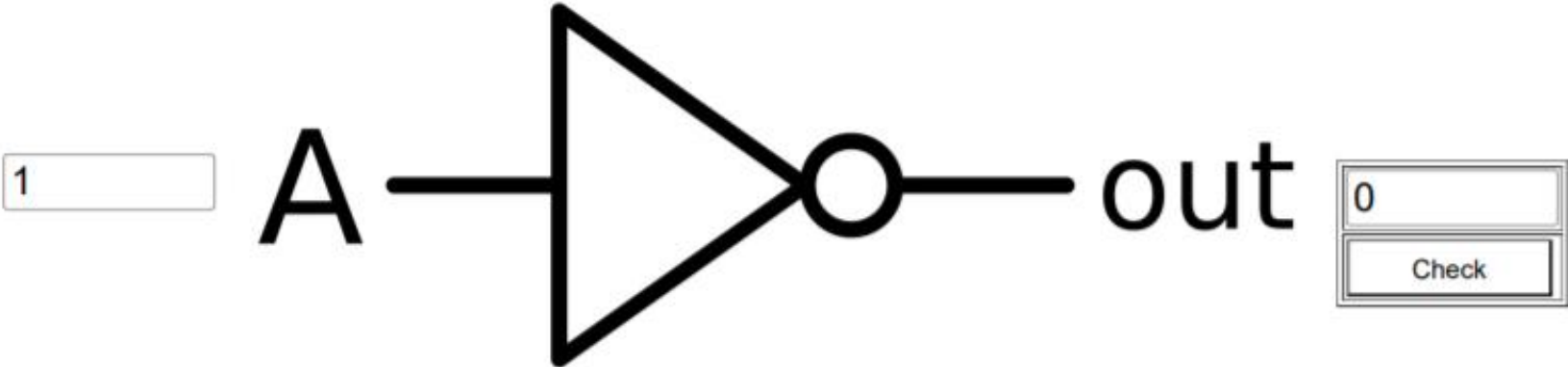
Reset

TRUTH TABLE   Print

Serial No.	A	Output	Remarks
1	0	1	Correct
2	1	0	Correct

Verification of truth table for OR gate

1

A

1

B

out

1

Check
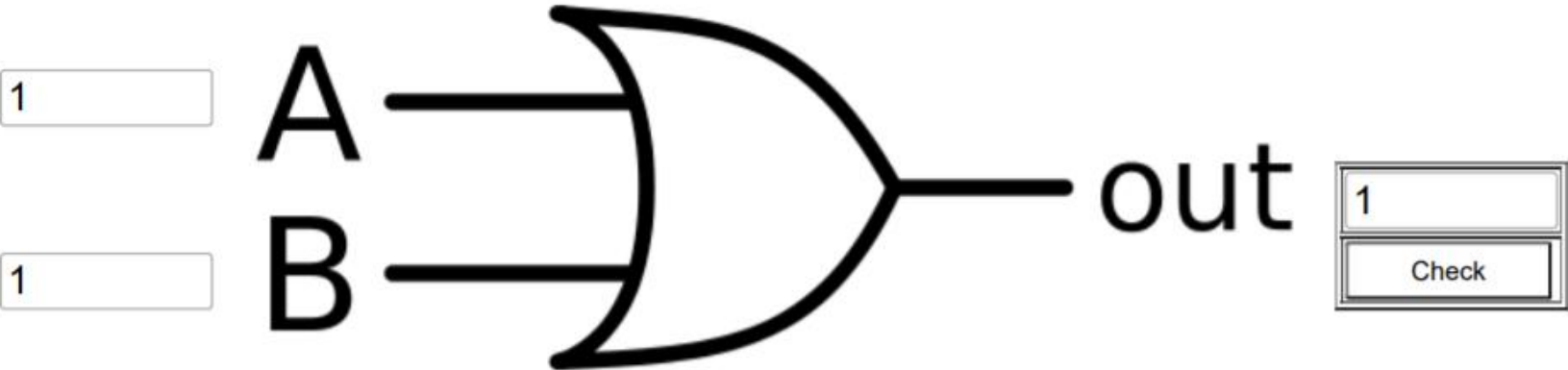
TRUTH TABLE

Print

Serial No.	A	B	Output	Remarks
1	0	0	0	Correct
2	0	1	1	Correct
3	1	0	1	Correct
4	1	1	1	Correct

Reset

Verification of truth table for XNOR gate
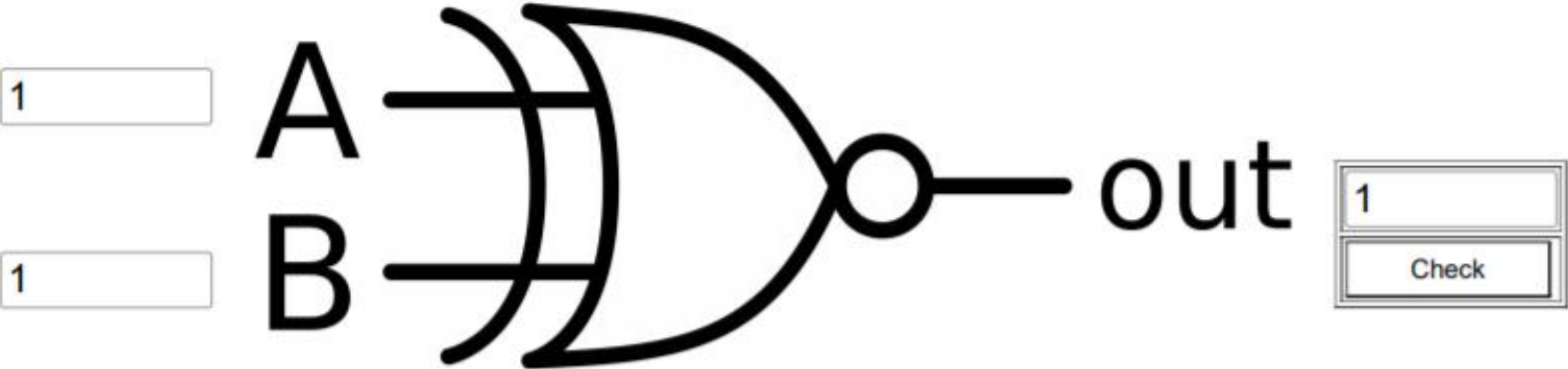
A

B

out

1

1

1

Check

TRUTH TABLE

Print

Serial No.	A	B	Output	Remarks
1	0	0	1	Correct
2	0	1	0	Correct
3	1	0	0	Correct
4	1	1	1	Correct

Reset

Verification of truth table for XOR gate

1

1

A

B

out

0

Check

TRUTH TABLE

Print

Serial No.	A	B	Output	Remarks
1	0	0	0	Correct
2	0	1	1	Correct
3	1	0	1	Correct
4	1	1	0	Correct

Reset