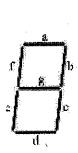
CS2521 Lab 7

Draw the logic of one 7-segment display from binary coded decimal format (Conversion from 4 bit input [X,Y,Z,W] to 7 output bits [a,b,c,d,e,f,g] assuming each of the segment is illuminated upon 0 in output) using the given truth table. Write equations of Boolean Expressions and draw logic circuit over Logism, attach screenshot with this worksheet.



$$a=2(0,7,3,5,7,8,9)$$

 $6=4(0,1,2,3,4,5,6,7,8,9)$
 $c=2(0,1,3,4,5,6,7,8,9)$
 $c=2(0,2,3,5,6,8)$
 $c=2(0,2,6,8)$
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Truth Table:

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