

## Machine language

1 Register m/c → ACCUMULATOR

Variable names ~> memory locations  
 Other temp storage ~> ints

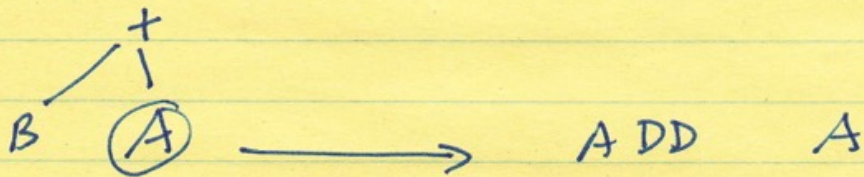
LOAD	X	/	LOAD	n	} M/c instruction set
STORE	X	/	STORE	n	
ADD	X	/	ADD	n	
MUL	X	/	MUL	n	

no control flow



code generation is done by  
recursively traversing the tree.

$A \longrightarrow \text{LOAD } A$



Need to indicate the context.

$\text{codegen}(\uparrow, \text{tree})$

some char to  
indicate the context.

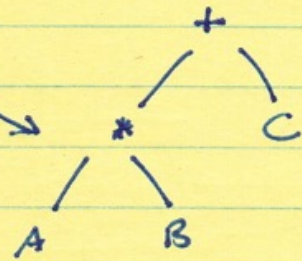
$(\$)$        $\begin{matrix} = & + & * \\ \uparrow & \uparrow & \\ \text{fresh} & & \end{matrix}$

$\text{codegen}(=, A) \longrightarrow \text{LOAD } A$   
 $\text{codegen}(*, A) \longrightarrow \text{MUL } A$   
 $\text{codegen}(+, A) \longrightarrow \text{ADD } A$



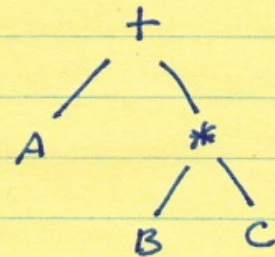
"A \* B + C"

LOAD A  
MUL B  
ADD C



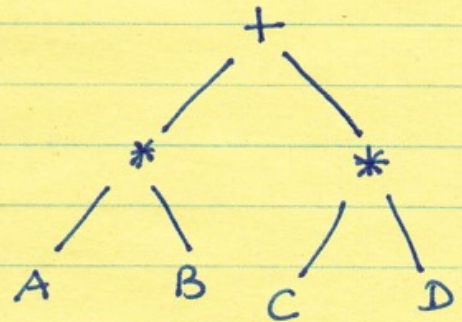
A + B \* C

LOAD A  
STORE 1  
LOAD B  
MUL C  
ADD 1

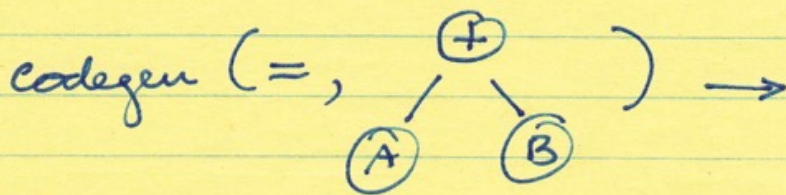


A \* B + C \* D

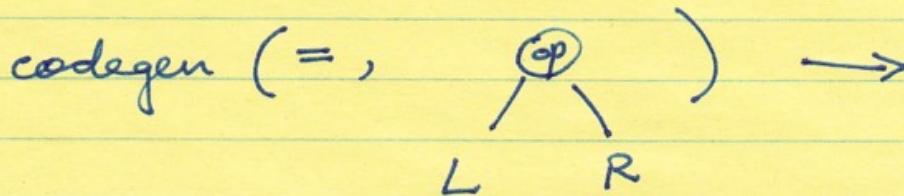
LOAD A  
MUL B  
STORE 1  
LOAD C  
MUL D  
ADD 1



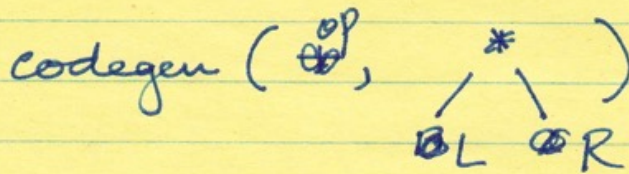




codegen (=, A)  
codegen (+, B)



codegen (=, L)  
codegen (op, R)



temp. storage needed.

only place where  
you need  
memory  
allocation.

memory := memory + 1  
output (STORE memory)  
codegen (=, L) } work by  
codegen (op, R) } magic  
if op = '+' Then  
    output (ADD, memory)  
else if op = '\*' Then  
    output (MUL, memory)  
memory := memory - 1



$A + B * C$

LOAD A  
STORE 1  
LOAD B  
MUL C  
ADD 1

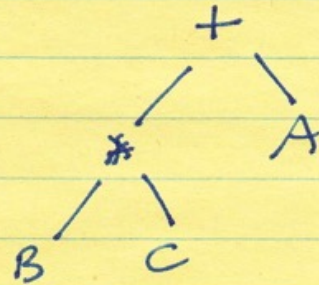
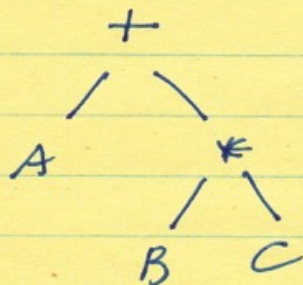
(2) codegen(=, .)

(4) CG(=, .)

(1) CG(=, .)

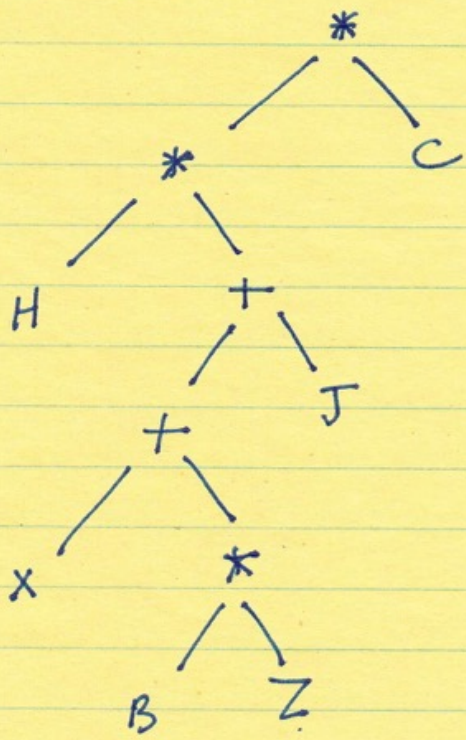
(3) CG(+, .)

(5) CG(\*, .)



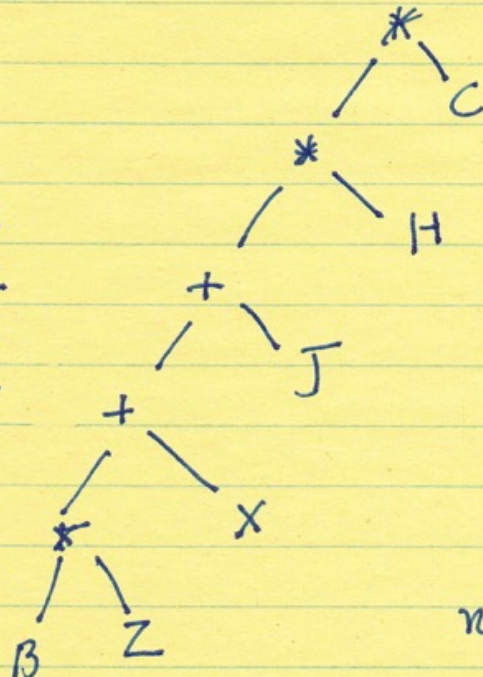
LOAD B  
MUL C  
ADD A

3 inst



LOAD H  
STORE 1  
LOAD X  
STORE 2  
LOAD B  
MUL Z  
ADD 2  
ADD J  
MUL 1  
MUL C

10 steps  
2 extra cells



LOAD B  
MUL Z  
ADD X  
ADD J  
MUL H  
MUL C

6 steps  
no extra memory.