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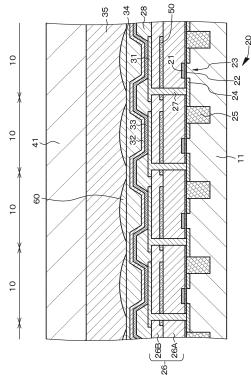
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# (54) LIGHT-EMITTING ELEMENT, PROJECTION TYPE DISPLAY DEVICE, AND PLANAR LIGHT-EMITTING DEVICE

(57) A light emitting element 10 includes a first electrode 31, an organic layer 33 formed on the first electrode 31 and including a luminescent layer 33A composed of an organic luminescent material and a second electrode 32 formed on the organic layer 33, and further includes a light reflecting layer 50 provided below the first electrode 31. Light emitted in the luminescent layer 33A is resonated between the light reflecting layer 50 and an interface of the second electrode 32 and the organic layer 33, and a portion of the light is output from the second electrode 32.

Fig. 1



# Description

[Technical Field]

- [0001] The present disclosure relates to a light emitting element and a projection type display device and a planar light emitting device provided with such a light emitting element, and more specifically, a self-luminous type light emitting element and a projection type display device and a planar light emitting device provided with such a self-luminous type light emitting element.
- 10 [Background Art]

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[0002] In recent years, lighting devices and organic electroluminescence display devices (hereinafter, simply abbreviated as organic EL display devices) that use an organic electroluminescence element (hereinafter, simply abbreviated as an organic EL element) as a light emitting element are becoming widespread. Further, there is a strong demand for the development of methods for efficiently extracting light from organic EL display devices. This is because where the light extraction efficiency is low, the actual amount of light emitted from an organic EL element is not effectively utilized, which causes a large loss in terms of power consumption and the like. Attempts have been made to control the light generated in the luminescent layer, such as improving the color purity of the emitted color and increasing the luminous efficiency by introducing a resonator structure in an organic EL element (see, for example, WO 2001/039554). Further, for example, JP 2009-049135A discloses that the emission intensity can be maximized by ensuring the relationship such that the light generated in a resonator structure and the light reflected at each reflection end and returned intensify each other.

[0003] In addition, a conventional projection type display device includes, for example, a light source that outputs light, a spatial modulator that modulates the light from this light source to form an image, and a projection optical system that projects an image from the spatial modulator onto, for example, a screen.

[Citation List]

[Patent Literature]

[0004]

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[PTL 1] WO 2001/039554 [PTL 2] JP 2009-049135 A

[Summary]

40 [Technical Problem]

> [0005] However, as far as the investigation conducted by the present inventors has shown, a projection type display device in which a light source that outputs light and a space modulator that forms an image are integrated, that is, a projection type display device and a planar light emitting device that can form an image by a self-luminous type light emitting element instead of the light source and the space modulator are not known.

> [0006] Therefore, an object of the present disclosure is to provide a projection type display device capable of forming an image by self-luminous type light emitting elements, a planar light emitting device configured of self-luminous type light emitting elements, and a light emitting element suitable for use in such projection type display device or planar light emitting device.

[Solution to Problem]

[0007] A light emitting element according to the first aspect of the present disclosure for achieving the above object

55 a first electrode;

an organic layer formed on the first electrode and including a luminescent layer composed of an organic luminescent

a second electrode formed on the organic layer, wherein

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the luminescent layer is configured by layering a plurality of luminescent layers that emit light of the same color.

[0008] A light emitting element according to the second aspect of the present disclosure for achieving the above object comprises:

a first electrode;

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an organic layer formed on the first electrode and including a luminescent layer composed of an organic luminescent material; and

a second electrode formed on the organic layer, and further comprises a light reflecting layer provided below the first electrode, wherein light emitted in the luminescent layer is resonated between the light reflecting

layer and an interface of the second electrode and the organic layer, and a portion of the light is output from the second electrode. That is, a resonator structure is formed between the light reflecting layer and the interface of the second electrode and the organic layer.

[0009] A projection type display device according to the first aspect of the present disclosure for achieving the above object is configured of

a panel comprising:

- 20 a first substrate:
  - a second substrate; and
  - a plurality of light emitting elements sandwiched between the first substrate and the second substrate, wherein

each light emitting element includes

a first electrode:

an organic layer formed on the first electrode and including a luminescent layer composed of an organic luminescent material; and

a second electrode formed on the organic layer,

the luminescent layer being configured by layering a plurality of luminescent layers that emit light of the same color.

**[0010]** A projection type display device according to the second aspect of the present disclosure for achieving the above object is configured of

a panel comprising:

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- a first substrate;
- a second substrate; and
- a plurality of light emitting elements sandwiched between the first substrate and the second substrate, wherein
- 40 each light emitting element includes
  - a first electrode;
  - an organic layer formed on the first electrode and including a luminescent layer composed of an organic luminescent material; and
  - a second electrode formed on the organic layer, and
  - each light emitting element further includes a light reflecting layer provided below the first electrode, and light emitted in the luminescent layer is resonated between the light reflecting layer and an interface of the second electrode and the organic layer, and a portion of the light is output from the second electrode. That is, a resonator structure is formed between the light reflecting layer and the interface of the second electrode and the organic layer.
- [0011] The projection type display device according to the third aspect of the present disclosure for achieving the above object is configured of

a panel comprising:

- 55 a first substrate;
  - a second substrate; and
  - a plurality of light emitting elements sandwiched between the first substrate and the second substrate, wherein

each light emitting element is configured of the light emitting element according to the first or second aspect of the present disclosure described above.

[0012] A planar light emitting device of the present disclosure for achieving the above object is configured of

a panel comprising:

- a first substrate;
- a second substrate; and
- a plurality of light emitting elements sandwiched between the first substrate and the second substrate, wherein

each light emitting element is configured of the light emitting element according to the first or second aspect of the present disclosure described above.

15 [Brief Description of Drawings]

# [0013]

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[Fig. 1]

Fig. 1 is a schematic partial cross-sectional view of a light emitting element of Example 1 and a panel constituting a projection type display device of Example 1.

[Fig. 2]

Fig. 2A and 2B are diagrams showing a schematic arrangement of four panels constituting the projection type display device of Example 1.

<sup>25</sup> [Fig. 3]

Fig. 3 is a diagram schematically showing an image projection state of four panels constituting the projection type display device of Example 1 shown in Fig. 2A.

[Fig. 4]

Fig. 4 is a diagram schematically showing an image projection state of four panels constituting the projection type display device of Example 1 shown in Fig. 2B.

[Fig. 5]

Fig. 5 is a schematic partial cross-sectional view of Modification Example 1 of the light emitting element of Example 1 and the panel constituting the projection type display device of Example 1.

[Fig. 6]

Fig. 6 is a schematic partial cross-sectional view of Modification Example 2 of the light emitting element of Example 1 and the panel constituting the projection type display device of Example 1.

[Fig. 7]

Fig. 7 is a schematic partial cross-sectional view of Modification Example 3 of the light emitting element of Example 1 and the panel constituting the projection type display device of Example 1.

40 [Fig. 8]

Fig. 8 is a plan view schematically showing a metal thin-film filter layer in the light emitting element of Example 1 and the panel constituting the projection type display device of Example 1 shown in Fig. 7.

[Fig. 9]

Fig. 9 is a schematic partial cross-sectional view of Modification Example 4 of the light emitting element of Example 1 and the panel constituting the projection type display device of Example 1.

[Fig. 10]

Fig. 10 is a conceptual diagram of Modification Example 5 of the panel constituting the projection type display device of Example 1.

[Fig. 11]

Figs. 11A and 11B are conceptual diagrams of Modification Example 6 of the panel constituting the projection type display device of Example 1.

[Fig. 12]

Fig. 12 is a diagram schematically showing an outputting state of light from a light emitting element.

[Fig. 13]

Figs. 13A and 13B are schematic partial cross-sectional views of the light emitting elements of Example 1 and Example 2, respectively.

[Fig. 14]

Fig. 14 is a schematic partial cross-sectional view of the light emitting element of Example 2 and the panel constituting

the projection type display device of Example 2.

[Fig. 15]

Fig. 15 is a schematic partial cross-sectional view of Modification Example 7 of the light emitting element of Example 1 and the panel constituting the projection type display device of Example 1.

[Fig. 16]

Fig. 16 is a schematic partial cross-sectional view of Modification Example 8 of the light emitting element of Example 1 and the panel constituting the projection type display device of Example 1.

[Fia. 17]

Fig. 17 is a graph showing the emission spectra of the light emitting elements of Examples 1, 2 and 3, and a conventional organic electroluminescence element.

[Fig. 18]

Fig. 18A is a graph showing the emission spectra of Example 2 and the conventional organic electroluminescence element, and Fig. 18B is a graph showing the relationship between the light outputting angle from the center line passing through the center of a light emitting unit of the light emitting element and light intensity.

[Fig. 19]

Fig. 19A is a schematic arrangement diagram of three organic electroluminescence elements constituting one pixel of the conventional organic electroluminescence display device, and Fig. 19B is a schematic arrangement diagram of light emitting regions.

#### 20 [Description of Embodiments]

**[0014]** Hereinafter, the present disclosure will be described based on examples with reference to the drawings, but the present disclosure is not limited to the examples, and various numerical values and materials in the examples are merely illustrative. The description will be given in the following order.

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- 1. General explanation of the light emitting element according to the first and second aspects of the present disclosure, the projection type display device according to the first to third aspects of the present disclosure, and the planar light emitting device of the present disclosure
- 2. Example 1 (the light emitting element according to the second aspect of the present disclosure, and the projection type display device according to the second aspect of the present disclosure and the third aspect of the present disclosure)
- 3. Example 2 (the light emitting element according to the first aspect of the present disclosure, and the projection type display device according to the first aspect of the present disclosure and the third aspect of the present disclosure)
- 4. Example 3 (combination of Example 1 and Example 2)
- 5. Other

<General explanation of the light emitting element according to the first and second aspects of the present disclosure, the projection type display device according to the first to third aspects of the present disclosure, and the planar light emitting device of the present disclosure>

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[0015] In a possible embodiment of the light emitting element according to the first aspect of the present disclosure, an intermediate layer (charge generation layer) is formed between the luminescent layer and the luminescent layer. Here, a material constituting the intermediate layer can be exemplified by at least one kind of material selected from the group consisting of lithium (Li), calcium (Ca), sodium (Na), cesium (Cs), molybdenum oxide (MoO $_3$ ), vanadium oxide ( $V_2O_5$ ) and tungsten oxide ( $V_3$ ), and broadly by conductive metal materials, alloy materials, and metal compounds. It is necessary to prevent damage to the luminescent layer due to the film formation of the intermediate layer. The intermediate layer is formed on a luminescent layer composed of an organic material by, for example, a sputtering method, but if the film forming temperature of the intermediate layer exceeds, for example, 100°C, the luminescent layer may be damaged. Therefore, when the intermediate layer is formed based on the sputtering method, the material constituting the intermediate layer needs to be selected from the materials for which the film forming temperature can be 100°C or lower. The thickness of the intermediate layer may be, but is not limited to, 2 nm to 10 nm.

**[0016]** Furthermore, in a possible embodiment of the light emitting element according to the first aspect of the present disclosure including the preferred embodiments described above, the plurality of luminescent layers have the same composition.

<sup>55</sup> **[0017]** Furthermore, in a possible embodiment, the light emitting element according to the first aspect of the present disclosure including the preferred embodiments described above further includes

a light reflecting layer, wherein

light emitted in the luminescent layer is resonated between the light reflecting layer and an second interface of the second electrode and the organic layer, and a portion of the light is output from the second electrode. Here, in a possible embodiment, the light reflecting layer is arranged below the first electrode, or is configured to be arranged above the first electrode and below the luminescent layer. Such a preferable configuration of the light emitting element according to the first aspect of the present disclosure can be inclusive of various below-described preferable forms of the light emitting element according to the second aspect of the present disclosure. In this case, the maximum light emission position of the luminescent layer in the luminescent layer formed by layering a plurality of luminescent layers emitting the same color refers to the average position in the thickness direction of the plurality of luminescent layers. Specifically, the position corresponding to the average value of the distances between the interface between the first electrode and the organic layer (referred to as "first interface" for convenience) and the center in the thickness direction of each luminescent layer is taken as the maximum light emitting position. In the configuration in which the light reflecting layer is arranged above the first electrode and below the luminescent layer, the first electrode is not necessarily required to be transparent. Further, in some cases, an embodiment is possible in which the formation of the light reflecting layer is omitted so that the first electrode also serves as the light reflecting layer. In this case, the distance from the maximum light emission position of the luminescent layer to the first electrode is taken as L1, the optical distance is taken as OL1, and the phase shift amount of the reflected light (light reflected by the light reflecting layer) generated at the first electrode is taken as  $\Phi_1$ . The above will be described in detail hereinbelow.

[0018] In the light emitting element according to the first aspect of the present disclosure, the number of luminescent layers (N) is 2 or more, but this number is not limiting, and "4" can be exemplified as an upper limit value. The number of layers in the intermediate layer is (N - 1). In general, as the number N of the luminescent layers increases, the voltage for driving the light emitting element increases. Therefore, the number N of the luminescent layers is limited by the voltage for driving the light emitting element, and also by the withstand voltage of the light emitting element drive unit. [0019] In the light emitting element according to the second aspect of the present disclosure, a resonance structure is formed between the interface between the second electrode and the organic layer (referred to as "second interface" for convenience) and the surface of the light reflecting layer on the first electrode side (this surface is also referred to as "first interface" for convenience), but a configuration may be used such that assuming that the optical distance from the maximum light emission position of the luminescent layer to the first interface is  $OL_1$ , the optical distance from the maximum light emission position of the luminescent layer to the second interface is  $OL_2$ , and  $M_1$  and  $M_2$  are integers, the following formulas (1-1) and (1-2) are satisfied.

$$0.7\{-\Phi_1/(2\pi) + m_1\} \le 2 \times OL_1/\lambda \le 1.2\{-\Phi_1/(2\pi) + m_1\}$$
 (1-1)

$$0.7\{-\Phi_2/(2\pi) + m_2\} \le 2 \times OL_2/\lambda \le 1.2\{-\Phi_2/(2\pi) + m_2\}$$
 (1-2)

[0020] Here,

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 $\lambda$ : maximum peak wavelength of the spectrum of light generated in the luminescent layer (or the desired wavelength in the light generated in the luminescent layer);

 $\Phi_1$ : phase shift amount (unit: radian) of reflected light generated at the first interface (light reflected at the first interface). However,  $-2\pi < \Phi_1 \le 0$ ; and

 $\Phi_2$ : phase shift amount (unit: radian) of reflected light (light reflected at the second interface) generated at the second interface. However,  $-2\pi < \Phi_2 \le 0$ .

**[0021]** Here, the value of  $m_1$  is 0 or more, and the value of  $m_2$  is 0 or more, independently of the value of  $m_1$ , but from the viewpoint of potential design in the organic layer, that is, from the viewpoint of optimizing the potential in the organic layer, it is preferable that  $m_1 \ge 1$  and  $m_2 \ge 1$ . As a result, as will be described in detail hereinbelow, the value of the full width at half maximum (FWHM) of the light output from the light emitting element can be made smaller.

[0022] The distance  $L_1$  from the maximum light emission position of the luminescent layer to the first interface refers to the actual distance (physical distance) from the maximum light emission position of the luminescent layer to the first interface, and the distance  $L_2$  from the maximum light emission position of the luminescent layer to the second interface refers to the actual distance (physical distance) from the maximum light emission position of the luminescent layer to the second interface. The optical distance is also called an optical path length, and generally refers to  $n \times L$  when a light ray passes through a medium having a refractive index n by a distance L. The same applies also hereinbelow. Therefore, assuming that the average refractive index is  $n_{ave}$ , the following relationships are valid.

$$OL_1 = L_1 \times n_{ave}$$

 $OL_2 = L_2 \times n_{ave}$ 

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**[0023]** Here, the average refractive index  $n_{ave}$  is obtained by adding up the products of the refractive index and the thickness of each layer constituting the organic layer (or the organic layer, the first electrode, and the interlayer insulating layer), and dividing the sum by the thickness of the organic layer (or the organic layer, the first electrode, and the interlayer insulating layer).

**[0024]** The light emitting element may be designed by determining the desired wavelength  $\lambda$  (specifically, the red wavelength, the green wavelength, and the blue wavelength) of the light generated in the luminescent layer, and obtaining various parameters such as  $OL_1$  and  $OL_2$  in the light emitting element on the basis of formulas (1-1) and (1-2).

[0025] The light reflecting layer and the second electrode absorb a part of the incident light and reflect the rest. Therefore, a phase shift occurs in the reflected light. The phase shift amounts  $\Phi_1$  and  $\Phi_2$  can be determined by measuring the values of the real and imaginary parts of the complex refractive index of the materials constituting the light reflecting layer and the second electrode with, for example, an ellipsometer, and performing calculations on the basis of these values (see, for example, "Principles of Optic", Max Born and Emil Wolf, 1974 (PERGAMON PRESS)). The refractive indexes of the organic layer, the interlayer insulating layer, and the like and also the refractive index of the first electrode, or the refractive index of the first electrode in the case where the first electrode absorbs a part of the incident light and reflects the rest, can be also obtained by measuring with the ellipsometer.

**[0026]** Examples of the material constituting the light reflecting layer include aluminum, aluminum alloys (for example, Al-Nd or Al-Cu), Al/Ti layered structure, Al-Cu/Ti layered structure, chromium (Cr), silver (Ag), and silver alloys (for example, Ag-Cu, Ag-Pd-Cu, and Ag-Sm-Cu). The light reflecting layer can be formed by, for example, vapor deposition methods including an electron beam vapor deposition method, a hot filament vapor deposition method, and a vacuum vapor deposition method, a sputtering method, a CVD method or an ion plating method; a plating method (electroplating method or electroless plating method); a lift-off method; a laser ablation method; a sol-gel method; or the like. Depending on the material constituting the light-reflecting layer, it is preferable to form a base film composed of, for example, TiN in order to control the crystal state of the light reflecting layer to be formed.

**[0027]** The light emitting element according to the first aspect and the second aspect of the present disclosure can be configured such that the value of the full width at half maximum (FWHM) of the light output from the light emitting element is 30 nm or less. The value of the full width at half maximum (FWHM) of the light output from the conventional organic EL element described hereinbelow is often 60 nm to 100 nm, and the light emitting element according to the first aspect and the second aspect of the present disclosure has a sharper emission spectrum than the conventional organic EL elements.

**[0028]** Further, in a possible embodiment of the light emitting element according to the first aspect and the second aspect of the present disclosure, where the light intensity (light intensity of the light output from the light emitting unit) at the center line passing through the center of the light emitting unit of the light emitting element is taken as 100%, the directional half-value angle, which is the angle formed by the center line and the direction with respect to the center line that has a 50% light intensity (light intensity of the light output from the light emitting unit), is 25 degrees or less. The directional half-value angle of Lambersian radiation is about 70 degrees. That is, the light output from the light emitting element according to the first aspect and the second aspect of the present disclosure has higher directivity than the conventional organic EL element described later, or is close to parallel light. Therefore, a telecentric optical system that is often required between the light source of the conventional projection type display device (projector) and a spatial modulator is unnecessary, and the projection type display device can be simplified. Further, the light output from the light emitting element according to the first aspect and the second aspect of the present disclosure is highly directional light or light close to parallel light. Therefore, it is possible to prevent the occurrence of a phenomenon that the light generated in the luminescent layer repeatedly undergoes total reflection between the first substrate and the second substrate to be output from the joint portion (end face of the panel) of the first substrate and the second substrate, and a loss occurs in the light output from the panel.

**[0029]** Further, in a possible embodiment of the light emitting element according to the second aspect of the present disclosure including the preferred configurations described above, a lens member (on-chip lens) is arranged on the light outputting side of the organic layer. By adopting such an embodiment, the light output from the light emitting element can be in a desired state such as parallel light. The lens member (on-chip microlens) can be configured of, for example, a transparent resin material such as an acrylic resin, and can be obtained by melt-flowing or by etching back the transparent resin material, and can also be obtained by a method such as forming a transparent resin material into a lens shape on the basis of a nanoprint method. In a possible embodiment, the lens member (on-chip lens) has, for example, a positive optical power, and in a possible, but not limiting, embodiment, has a plano-convex lens shape. An

embodiment is also possible in which a convex surface is located on the second substrate side or on the first substrate side, and the convex surface can be spherical or aspherical, and the planar shape can be a shape suitable for the planar shape of the light emitting region, which will be described hereinbelow, such as circular, elliptical, or rectangular with rounded corners. In a possible embodiment, a second lens member that controls the traveling direction of the light output from the lens member is further included.

[0030] In a possible embodiment, a light absorption layer (black matrix layer) is formed between the lens members of the adjacent light emitting elements. By forming a light absorption layer (black matrix layer) between the lens members of the adjacent light emitting elements, it is possible to reliably suppress the occurrence of duplication of minute images between the adjacent light emitting elements. That is, a phenomenon in which a unit image formed by a certain light emitting element and a unit image formed by a light emitting element adjacent to or located near the certain light emitting element partially overlap (hereinafter, referred to as "partial duplication of unit pixels" for convenience), can be reliably suppressed. The light absorption layer is composed of, for example, a black resin film (specifically, for example, a black polyimide resin) having an optical density of 1 or more that is mixed with a black colorant, or the light absorption layer is configured of a thin-film filter that uses the interference of thin films. The thin-film filter is formed by layering two or more thin films made of, for example, a metal, a metal nitride or a metal oxide, and attenuates light by utilizing the interference of the thin films. Specific examples of the thin-film filter include those in which Cr and chromium (III) oxide (Cr<sub>2</sub>O<sub>3</sub>) are alternately layered.

[0031] Further, in a possible embodiment of the light emitting device according to the second aspect of the present disclosure including the preferable configurations and embodiments described above, a metal thin-film filter layer is further formed between the first electrode and the light reflecting layer. The metal thin-film filter layer is composed of, for example, gold (Au), silver (Ag), platinum (Pt), aluminum (Al), copper (Cu), tungsten (W), or alloys including these materials. In the metal thin-film filter layer, for example, a large number of holes having a planar shape of a circle, an ellipse, a rectangle, a letter "U", a cross, and the like and a size of about 200 nm are formed and arrayed two-dimensionally (for example arranged on grid points or arranged in a staggered pattern), or a large number of slits are formed. The metal thin-film filter layer is disclosed in, for example, JP 2015-232599 A. In the metal thin-film filter layer, only light having a specific wavelength is transmitted by means of surface plasmon polariton (SPR) in which free electrons on the metal surface are coupled with electromagnetic waves. A metal thin-film filter layer subjected to such periodic microfabrication is also called a plasmonic filter (hole array filter). Although the metal thin-film filter layer is widely known in the field of image capturing devices, the use thereof in a projection type display device provided with a self-luminous type light emitting elements is not known as far as the present inventors have investigated. Depending on the wavelength of the light output by the light emitting element (for example, when the light emitting element outputs infrared rays), it is also possible, to form the metal thin-film filter layer described above instead of the light reflecting layer, and reflect the light by the metal thin-film filter layer.

**[0032]** Further, in a possible embodiment of the light emitting element according to the first and second aspects of the present disclosure including the preferable configurations and embodiments described above, when the light intensity (light intensity of the light output from the light emitting element) at the center line passing through the center of the light emitting unit of the light emitting element is denoted by  $I_0$  and the light intensity (light intensity of the light output from the light emitting element) at the center line passing through the center of the light emitting unit of the light emitting element without the light reflecting layer is denoted by  $I_{conv}$ ,

# $I_0/I_{conv} > 5$

is satisfied.

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[0033] Further, in a possible embodiment of the light emitting element according to the second aspect of the present disclosure including the preferable configurations and embodiments described above, the first electrode and the light reflecting layer are surrounded by a light shielding portion or a light reflecting portion (reflector portion). That is, a light shielding portion may be provided between the light emitting element and the light emitting element, or a light reflecting portion may be provided. By adopting such an embodiment, it is possible to reliably prevent the occurrence of partial duplication of unit pixels. Specific examples of the light shielding material constituting the light shielding portion include materials capable of shielding light, such as titanium (Ti), chromium (Cr), tungsten (W), tantalum (Ta), aluminum (Al), and MoSi<sub>2</sub>. The light shielding portion can be formed by a vapor deposition method including an electron beam vapor deposition method, a hot filament vapor deposition method, and a vacuum vapor deposition method, a sputtering method, a CVD method, an ion plating method, or the like. Examples of the material constituting the light reflecting portion (reflector portion) include an aluminum (Al) layer, an aluminum alloy layer (for example, Al-Nd layer), a chromium (Cr) layer, a silver (Ag) layer, and a silver alloy layer (for example, Ag-Cu layer, Ag-Pd-Cu layer, Ag-Sm-Cu layer), and this portion can be formed by, for example, a vapor deposition method including an electron beam vapor deposition method, a thermal filament vapor deposition method, and a vacuum vapor deposition method, a Sputtering method, a CVD method

or an ion plating method; a plating method (electroplating method or electroless plating method); a lift-off method; a laser ablation method; a sol-gel method or the like.

**[0034]** Further, in a possible embodiment of the light emitting device according to the second aspect of the present disclosure including the preferable configurations and embodiments described above, the first electrode is composed of a light transmitting material and the second electrode is composed of a semi-light transmitting material. Here, in a possible embodiment, the first electrode is composed of ITO or IZO, and the second electrode is composed of at least one kind of material selected from the group consisting of Ag, Ag-Mg, Ag-Nd-Cu, Au, Ag-Cu, Al, and Al-Cu. The average light transmittance of the second electrode is desirably 50% to 90%, and preferably 60% to 90%.

**[0035]** In the projection type display device according to the first to third aspects of the present disclosure, or the planar light emitting device of the present disclosure, the panel outputs monochromatic light (for example, red light (has an emission spectrum peak within the range of red light with a wavelength of 620 nm to 750 nm), green light (has an emission spectrum peak within the range of green light with a wavelength of 495 nm to 570 nm), and blue light (has an emission spectrum peak within the range of green light with a wavelength of 450 nm to 495 nm)).

**[0036]** In a possible embodiment, the projection type display device according to the first to third aspects of the present disclosure is configured of three panels of

one red light outputting panel that outputs red light,

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one green light outputting panel that outputs green light, and

one blue light outputting panel that outputs blue light. In another possible embodiment, in this case, the projection type display device further has one blue light outputting panel for outputting blue light or one green light outputting panel for outputting green light, and thus is configured of four panels. In this case, the four panels may be arranged in an array ( $1 \times 4$  state), or the four panels may be arranged in a  $2 \times 2$  state.

**[0037]** The projection type display device according to the first to third aspects of the present disclosure including the above preferable embodiments and configurations can be embodied to further include a projection lens system on the light outputting side. Alternatively,

an embodiment is possible in which there are provided an image synthesizing means (for example, an unpolarized dichroic prism or a Philips prism) for synthesizing images output from a plurality of panels into one image, and a projection lens system on the light outputting side of the image synthesizing means (for example, an unpolarized dichroic prism or a Philips prism), and in this case, three panels or four panels constituting the projection type display device may be arranged at the optimum positions in the image synthesizing means.

**[0038]** A lens having a high F number and a high depth of field (DOF) is preferably used as a lens constituting the projection lens system. Further, the light incident on the projection lens system or the image synthesizing means is preferably parallel light with respect to the optical axis of the lens in the paraxial region of the lens. When using a lens with a high F number, it is necessary to reduce the divergence angle (direction half-value angle) of the light output from the light emitting element. For that purpose, an on-chip microlens may be provided on the light outputting side of the light emitting element or the resonator structure may be optimized.

**[0039]** Furthermore, in the projection type display device according to the first to third aspects of the present disclosure including the preferred embodiments and configurations described above, the panel can be embodied not only to be flat, but can also be embodied to be curved.

**[0040]** When the projection type display device according to the first to third aspects of the present disclosure including the preferred configurations and embodiments described above does not include means for synthesizing images output from a plurality of panels into one image, various corrections such as keystone correction, distortion correction and magnification correction of the image to be formed on the screen may be performed using software simultaneously with properly aligning a plurality of panels, for example, in order to properly display (synthesize) images from a plurality of panels on a screen, pixels of the images from the plurality of panels may be partially overlapped, rather than completely overlapped. There is no problem even if the pixels are displaced as long as the displacement is below the resolution limit of the observer's eyes.

**[0041]** In a possible embodiment of the light emitting elements according to the first and second aspects of the present disclosure including the preferred configurations and embodiments described above, and the light emitting elements according to the first and second aspects of the present disclosure including the preferred configurations and embodiments described above that constitute the projection type display device or the planar light emitting device according to the first to third aspects of the present disclosure including the preferred configurations and embodiments described above (hereinafter, these light emitting elements are collectively referred to as "light emitting elements of the present disclosure"), the light emitting unit constituting the light emitting element includes an organic electroluminescence layer.

That is, the planar light emitting device or the panel constituting the projection type display device according to the first to third aspects of the present disclosure including various preferable embodiments and configurations described above can be embodied to be composed of an organic electroluminescence panel (organic EL panel), the light emitting element can be embodied to be composed of an organic electroluminescence element (organic EL element), and the organic layer can be embodied to be composed of an organic electroluminescence layer. Further, the organic EL panel can be configured as a top emission type (upper-surface emission type) organic EL panel (upper-surface emission type organic EL panel) that outputs light from the second substrate where the light from the organic layer is output through the second substrate.

**[0042]** Hereinafter, an embodiment in which the light emitting unit constituting the light emitting element includes an organic electroluminescence layer and an embodiment in which the panel is composed of a top emission type organic EL panel will be described.

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**[0043]** The light emitting unit in the light emitting element is configured of a first electrode, an organic layer, and a second electrode. The first electrode can be configured to be in contact with a part of the organic layer, or the organic layer can be configured to be in contact with a part of the first electrode. Specifically, a configuration may be such that the size of the first electrode is smaller than that of the organic layer, or a configuration may be such that the size of the first electrode is the same as that of the organic layer, but an insulating layer is formed in a part between the first electrode and the organic layer, or a configuration may be such that the size of the first electrode is larger than that of the organic layer. The region where the first electrode and the organic layer are in contact is a light emitting region. The area center of gravity of the light emitting region corresponds to the center of the light emitting unit described above.

[0044] The first electrode is provided for each light emitting element. The organic layer is provided for each light emitting element, or is provided to be shared by the light emitting elements. The second electrode may be a common electrode for a plurality of light emitting elements. That is, the second electrode may be a so-called solid electrode. The first substrate is arranged below or under the substrate, and the second substrate is arranged above the second electrode. The light emitting region is provided on the substrate. The light emitting elements are formed on the first substrate side. [0045] The material constituting the substrate can be exemplified by insulating materials such as SiO<sub>2</sub>, SiN, and SiON. The substrate is formed by a forming method suitable for the material constituting the substrate, specifically, can be formed by a well-known method, for example, various CVD methods, various coating methods, various PVD methods including a sputtering method and a vacuum vapor deposition method, various printing methods such as a screen printing method, a plating method, an electrodeposition method, a dipping method, and a sol-gel method.

[0046] A light emitting element drive unit is provided under or below the substrate, but such a configuration is not limiting. The light emitting element drive unit is configured of, for example, a transistor (specifically, for example, MOSFET) formed on a silicon semiconductor substrate constituting the first substrate, or a thin-film transistor (TFT) provided on various substrates constituting the first substrate. In a possible embodiment, the transistor or TFT constituting the light emitting element drive unit and the first electrode can be configured to be connected to each other via a contact hole (contact plug) formed in the substrate or the like. The light emitting element drive unit may have a well-known circuit configuration. The second electrode is connected to the light emitting element drive unit via a contact hole (contact plug) formed in the substrate or the like on the outer peripheral portion of the organic EL panel.

[0047] The first substrate or the second substrate can be configured of a silicon semiconductor substrate, a high-distortion point glass substrate, a soda glass ( $Na_2O \cdot CaO \cdot SiO_2$ ) substrate, a borosilicate glass ( $Na_2O \cdot B_2O_3 \cdot SiO_2$ ) substrate, a forsterite ( $2MgO \cdot SiO_2$ ) substrate, a lead glass ( $Na_2O \cdot PbO \cdot SiO_2$ ) substrate, various glass substrates with an insulating material layer formed on the surface, a quartz substrate, a quartz substrate with an insulating material layer formed on the surface, and organic polymers (having the form of a polymer material such as a flexible plastic film, a plastic sheet, or a plastic substrate configured of a polymer material) exemplified by polymethyl methacrylate (poly(methyl methacrylate), PMMA), polyvinyl alcohol (PVA), polyvinylphenol (PVP), polyether sulfones (PES), polyimides, polycarbonates, and polyethylene terephthalate (PET). The materials constituting the first substrate and the second substrate may be the same or different. However, the substrate that transmits the light from the light emitting element is required to be transparent to the light from the light emitting element.

[0048] In the light emitting device according to the first aspect of the present disclosure, basically, as described above, transparency is not necessarily required for the first electrode. In this case, when the first electrode functions as an anode electrode, examples of the material constituting the first electrode include metals or alloys with high work function such as platinum (Pt), gold (Au), silver (Ag), chromium (Cr), tungsten (W), nickel (Ni), copper (Cu), iron (Fe), cobalt (Co), and tantalum (Ta) (for example, a Ag-Pd-Cu alloy including silver as the main component, 0.3% by mass to 1% by mass of palladium (Pd) and 0.3% by mass to 1% by mass of copper (Cu), an Al-Nd alloy, an Al-Cu alloy, and an Al-Cu-Ni alloy). Furthermore, when a conductive material having a small work function value, such as aluminum (Al) and an alloy including aluminum, and having high light reflectance is used, the material can be made suitable for an anode electrode by improving the hole injection characteristic by providing an appropriate hole injection layer or the like. The thickness of the first electrode can be exemplified by 0.1 pm to 1 pm. Alternatively, a structure can be obtained in which a transparent conductive material having excellent hole injection characteristic, such as indium and tin oxide (ITO) or indium and zinc

oxide (IZO), is layered on a reflective material film having high light reflectivity, such as a dielectric multilayer film or aluminum (AI) or an alloy thereof (for example, AI-Cu-Ni alloy).

**[0049]** In the light emitting element according to the first aspect of the present disclosure, or in some cases, in the light emitting element according to the second aspect of the present disclosure, transparency is required for the first electrode. In this case, in addition to the aforementioned indium-tin oxide (including ITO, Indium Tin Oxide, Sn-doped  $ln_2O_3$ , crystalline ITO and amorphous ITO) and indium-zinc oxide (IZO, Indium Zinc Oxide), examples of the material constituting the first electrode include various transparent conductive materials such as transparent conductive materials having indium oxide, indium-gallium oxide (IGO), indium-doped gallium-zinc oxide (IGZO, In-GaZnO<sub>4</sub>), IFO (F-doped  $ln_2O_3$ ), ITO (Ti-doped  $ln_2O_3$ ), InSn, InSnZnO, tin oxide (SnO<sub>2</sub>), ATO (Sb-doped SnO<sub>2</sub>), FTO (F-doped SnO<sub>2</sub>), zinc oxide (ZnO), aluminum oxide-doped zinc oxide (AZO), gallium-doped zinc oxide (GZO), B-doped ZnO, AlMgZnO (aluminum oxide- and magnesium oxide-doped zinc oxide), antimony oxide, titanium oxide, NiO, spinel-type oxides, oxides having an YbFe<sub>2</sub>O<sub>4</sub> structure, gallium oxide, titanium oxide, niobium oxide, nickel oxide, or the like as a base layer.

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[0050] When the first electrode is to be caused to function as a cathode electrode, it is desirable that the first electrode be configured of a conductive material having a small work function value and a high light reflectance, but a conductive material having a high light reflectance and suitable as an anode electrode can also be made suitable for the cathode electrode by improving the electron injection characteristic thereof by providing an appropriate electron injection layer. [0051] When the second electrode is to be caused to function as a cathode electrode, it is desirable that the second electrode be configured of a conductive material with a small work function value so that the material (semi-light transmitting material or light transmitting material) constituting the second electrode could transmit emitted light and could efficiently inject electrons into the organic layer (luminescent layer), and in a possible embodiment, as described above, the second electrode can be configured of at least one kind of material selected from the group consisting of Ag, Ag-Mg, Ag-Nd-Cu, Ag-Cu, Au, Al and Al-Cu. Alternatively, for example, metals and alloys having a small work function, such as aluminum (Al), silver (Ag), magnesium (Mg), calcium (Ca), sodium (Na), strontium (Sr), alloys of an alkali metal or an alkaline earth metal and silver (Ag) (for example, an alloy of magnesium (Mg) and silver (Ag) (Mg-Ag alloy)), a magnesium-calcium alloy (Mg-Ca alloy), an alloy of aluminum (Al) and lithium (Li) (Al-Li alloy), and the like, and among them, a Mg-Ag alloy is preferable, and such alloy can be exemplified by a volume ratio of magnesium and silver of Mg: Ag = 5 : 1 to 30 : 1. Alternatively, an alloy of magnesium and calcium with a volume ratio of Mg : Ca = 2 : 1 to 10 : 1 can be exemplified.

**[0052]** The thickness of the second electrode can be exemplified by 4 nm to 50 nm, preferably 4 nm to 20 nm, and more preferably 6 nm to 12 nm. Alternatively, the second electrode can be configured to have a layered structure of the above-mentioned material layer and a so-called transparent electrode composed of, for example, ITO or IZO (for example, a thickness of  $3 \times 10^{-8}$  m to  $1 \times 10^{-6}$  m) from the organic layer side.

**[0053]** A bus electrode (auxiliary electrode) composed of a low resistance material such as aluminum, aluminum alloy, silver, silver alloy, copper, copper alloy, gold, or gold alloy may be provided for the second electrode to reduce the resistance of the second electrode as a whole. It is desirable that the average light transmittance of the second electrode be 50% to 90%, preferably 60% to 90%. Meanwhile, when the second electrode is to be caused to function as an anode electrode, it is desirable that the second electrode be configured of a conductive material that transmits emitted light and has a large work function value.

**[0054]** Examples of the method for forming the first electrode and the second electrode include a vapor deposition method including an electron beam vapor deposition method, a hot filament vapor deposition method, and a vacuum vapor deposition method, a sputtering method, a chemical vapor deposition method (CVD method), a MOCVD method, a combination of a plating method and an etching method; various printing methods such as a screen printing method, an inkjet printing method, a metal mask printing method; plating methods (electroplating method and electroless plating method); a lift-off method; a laser ablation method; a sol-gel method, and the like. With various printing methods and plating methods, it is possible to directly form the first electrode and the second electrode having a desired shape (pattern). Since the second electrode is formed after the organic layer is formed, from the viewpoint of preventing the occurrence of damage to the organic layer, it is preferable that the second electrode be formed on the basis of, in particular, a film forming method with a small energy of film forming particles, such as a vacuum vapor deposition method, or a film forming method such as MOCVD. Where the organic layer is damaged, non-emission pixels (or non-emission sub-pixels) called "dead points" may be generated due to the generation of leak current.

[0055] The organic layer includes a luminescent layer composed of an organic luminescent material, and specifically can be configured of, for example, a layered structure of a hole transport layer, a luminescent layer and an electron transport layer, a layered structure of a hole transport layer and a luminescent layer also serving as an electron transport layer, a layered structure of a hole injection layer, a hole transport layer, a luminescent layer, an electron transport layer, an electron injection layer, and the like. A method for forming the organic layer can be exemplified by a physical vapor deposition method (PVD method) such as a vacuum vapor deposition method; a printing method such as a screen printing method and an inkjet printing method; a laser transfer method in which a layered structure of an organic layer and a laser absorption layer formed on a transfer substrate is irradiated with laser radiation to separate the organic layer

on the laser absorption layer and transfer the organic layer, and various coating methods. Where the organic layer is formed based on the vacuum vapor deposition method, for example, a so-called metal mask can be used to deposit a material that has passed through an opening provided in the metal mask thereby obtaining an organic layer.

[0056] It is preferable that a protective layer (planarizing layer) be formed so as to cover the second electrode or between the second electrode and the second substrate. The material constituting the protective layer can be exemplified by an acrylic resin and can also be exemplified by SiN, SiON, SiC, amorphous silicon (a-Si), Al<sub>2</sub>O<sub>3</sub>, and TiO<sub>2</sub>. The protective layer can be formed based on known methods such as various CVD methods, various coating methods, various PVD methods including a sputtering method and a vacuum vapor deposition method, and various printing methods such as a screen printing method. Further, an ALD (Atomic Layer Deposition) method can also be adopted as a method for forming the protective layer. The protective layer may be shared by a plurality of light emitting elements, or may be individually provided in each light emitting element. The protective layer and the second substrate, or when a structure (for example, a lens member) is provided on the inner surface of the second substrate facing the first substrate, the protective layer and the second substrate including the protective layer are joined through, for example, a resin layer (sealing resin layer). A material constituting the resin layer (sealing resin layer) can be exemplified by heat-curable adhesives such as acrylic adhesives, epoxy adhesives, urethane adhesives, silicone adhesives, and cyanoacrylate adhesives, and ultraviolet curable adhesives.

**[0057]** An ultraviolet absorbing layer, a contamination prevention layer, a hard coat layer, and an antistatic layer may be formed, and a protective member (for example, a cover glass) may be arranged on the outermost surface (specifically, the outer surface of the second substrate) that outputs light of the organic EL panel.

[0058] In an organic EL panel, a substrate, an insulating layer, and an interlayer insulating layer are formed, and insulating materials constituting the substrate, insulating layer, and interlayer insulating layer can be exemplified by a SiOx-based material (material constituting a silicon oxide film) such as SiO2, NSG (non-doped silicate glass), BPSG (boron phosphorus silicate glass), PSG, BSG, AsSG, SbSG, PbSG, SOG (spin-on glass), LTO (Low Temperature Oxide, low temperature CVD-SiO<sub>2</sub>), low-melting-point glass, and glass paste; a SiN-based material including SiON-based material; SiOC; SiOF; and SiCN. Alternative examples include inorganic insulating materials such as titanium oxide (TiO<sub>2</sub>), tantalum oxide (Ta<sub>2</sub>O<sub>5</sub>), aluminum oxide (Al<sub>2</sub>O<sub>3</sub>), magnesium oxide (MgO), chromium oxide (CrO<sub>x</sub>), zirconium oxide (ZrO2), niobium oxide (Nb<sub>2</sub>O<sub>5</sub>), tin oxide (SnO<sub>2</sub>), and vanadium oxide (VO<sub> $\chi$ </sub>). Other examples include various resins such as polyimide resin, epoxy resin, and acrylic resin, insulating materials with a low dielectric constant such as SiOCH, organic SOG, and fluorine-based resin (for example, materials with a dielectric constant k (=  $\varepsilon/\varepsilon_0$ )) of, for example, 3.5 or less, specifically, for example, fluorocarbons, cycloperfluorocarbon polymers, benzocyclobutene, cyclic fluororesins, polytetrafluoroethylene, amorphous tetrafluoroethylene, polyaryl ethers, fluoroaryl ethers, fluorinated polyimide, amorphous carbon, parylene (polyparaxylylene), and fluorinated fullerenes), and also Silk (a trademark of The Dow Chemical Co., a coating type low dielectric constant interlayer insulating film material), Flare (a trademark of Honeywell Electronic Materials Co., polyallyl ether (PAE) -based material). These can be used alone or in combination as appropriate. The insulating layer, interlayer insulating layer, and substrate can be formed by well-known methods such as various CVD methods, various coating methods, various PVD methods including a sputtering method and a vacuum vapor deposition method, various printing methods such as a screen printing method, a plating method, an electrodeposition method, a dipping method, and a sol-gel method.

[0059] In an organic EL panel, it is desirable that the thickness of the hole transport layer (hole supply layer) and the thickness of the electron transport layer (electron supply layer) be approximately equal. Alternatively, the electron transport layer (electron supply layer) may be thicker than the hole transport layer (hole supply layer), which is necessary for high efficiency at a low drive voltage and enables sufficient supply of electrons to the luminescent layer. That is, the hole supply can be increased by arranging the hole transport layer between the first electrode corresponding to the anode electrode and the luminescent layer and forming the hole transport layer with a film thickness smaller than that of the electron transport layer. As a result, it is possible to obtain a carrier balance in which there is no excess or deficiency of holes and electrons and the amount of carrier supply is sufficiently large, so that high luminous efficiency can be obtained. Further, since there is no excess or deficiency of holes and electrons, the carrier balance is unlikely to be lost, drive deterioration is suppressed, and the light emission life can be extended.

# 50 Example 1

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**[0060]** Example 1 relates to a light emitting element according to the second aspect of the present disclosure, and to a projection type display device (projector) according to the second aspect and the third aspect of the present disclosure. Fig. 1 shows a schematic partial cross-sectional view of the light emitting element of Example 1 and a panel constituting the projection type display device of Example 1, Figs. 2A and 2B show a schematic arrangement of the four panels constituting the projection type display device of Example 1, and Figs. 3 and 4 schematically show the image projection state of the four panels constituting the projection type display device of Example 1 shown in Figs. 2A and 2B. Fig. 13A shows a schematic partial cross-sectional view of the light emitting element of Example 1.

# [0061] A light emitting element 10 of Example 1 includes

a first electrode 31,

an organic layer 33 formed on the first electrode 31 and including a luminescent layer 33A composed of an organic luminescent material, and

a second electrode 32 formed on the organic layer 33, and

further includes a light reflecting layer 50 provided below the first electrode 31, wherein

light emitted in the luminescent layer 33A is resonated between the light reflecting layer 50 and an interface (second interface) of the second electrode 32 and the organic layer 33, and a portion of the light is output from the second electrode 32.

[0062] Further, the projection type display device of Example 1 is configured of

a panel comprising:

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- a first substrate 11;
- a second substrate 41; and
- a plurality of light emitting elements 10 sandwiched between the first substrate 11 and the second substrate 41, wherein

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each light emitting element 10 includes

the first electrode 31,

the organic layer 33 formed on the first electrode 31 and including the luminescent layer 33A composed of an organic luminescent material, and the second electrode 32 formed on the organic layer 33, and

each light emitting element 10 further includes

the light reflecting layer 50 provided below the first electrode 31, wherein light emitted in the luminescent layer 33A is resonated between the light reflecting layer 50 and an interface (second interface) of the second electrode 32 and the organic layer 33, and a portion of the light is output from the second electrode 32.

[0063] Alternatively, the projection type display device of Example 1 is configured of

a panel comprising:

- a first substrate 11;
- a second substrate 41; and
- a plurality of light emitting elements sandwiched between the first substrate 11 and the second substrate 41, wherein

each light emitting element is configured of the light emitting element 10 of Example 1.

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[0064] The projection type display device of Example 1 or Examples 2 and 3 described hereinbelow is configured of

one red light outputting panel (first panel 1R) that outputs red light,

one green light outputting panel (second panel 1G) that outputs green light, and

one blue light outputting panel (third pan el  $1B_1$ ) that outputs blue light, and further includes

one blue light outputting panel (fourth panel  $1B_2$ ) that outputs blue light and is thus configured of four panels 1R, 1G,  $1B_1$ ,  $1B_2$ . A projection lens system 70 is provided on the light outputting side of the projection type display device. The four panels may be arranged in an array (1  $\times$  4 state) as shown in Figs. 2A and 3, or the four panels may be arranged in a 2  $\times$  2 state as shown in Figs. 2B and 4. In Figs. 3 and 4, the light rays output from the panel are indicated by dotted lines and dash-dot lines. The number of pixels of the panel is, for example,  $1920 \times 1080$ , and one light emitting element 10 constitutes one pixel. The size of the panel is, for example,  $10 \text{ mm} \times 10 \text{ mm}$ .

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**[0065]** A lens having a high F number and a high depth of field (DOF) is preferably used as a lens constituting the projection lens system 70. Further, the light incident on the projection lens system 70 or the image synthesizing means is preferably parallel light with respect to the optical axis of the lens in the paraxial region of the lens. When a lens with a high F number is used in the projection lens system 70, it is necessary to reduce the divergence angle (direction half-value angle) of the light output from the light emitting element. For that purpose, an on-chip microlens may be provided on the light outputting side of the light emitting element or the resonator structure may be optimized. In particular, since

the light output from the peripheral portion of the panel is incident on the peripheral portion of the lens, it is preferable to provide an on-chip microlens in order to optimize the incidence of such light on the lens.

**[0066]** In Example 1 or Examples 2 to 3 described hereinbelow, the panel constituting the projection type display device is composed of an organic EL panel, the light emitting element 10 is composed of an organic EL element, and the organic layer 33 is composed of an organic electroluminescence layer. Further, the organic EL panel is a top emission type organic EL panel that outputs light from the second substrate 41, and the light from the organic layer 33 is output to the outside via the second substrate 41.

[0067] Each panel outputs monochromatic light. The luminescent layer in the light emitting element constituting the first panel 1R is composed of a red luminescent layer that emits red light, and the light output from the first panel 1R is red light (has an emission spectrum peak within the range of red light with a wavelength of 620 nm to 750 nm). Specifically, the peak wavelength  $\lambda_R$  is as shown in Table 1 below. Further, the luminescent layer in the light emitting element constituting the second panel 1G is composed of a green luminescent layer that emits green light, and the light output from the second panel 1G is green light (has an emission spectrum peak within the range of green light with a wavelength of 495 nm to 570 nm). Specifically, the peak wavelength  $\lambda_G$  is as shown in Table 1 below. Further, the luminescent layer in the light emitting element constituting the third panel 1B<sub>1</sub> and the fourth panel 1B<sub>2</sub> is composed of a blue luminescent layer that emits blue light, the light output from the third panel 1B<sub>1</sub> and the fourth panel 1B<sub>2</sub> is blue light (has an emission spectrum peak within the range of green light with a wavelength of 450 nm to 495 nm), and the peak wavelength  $\lambda_B$  is as shown in Table 1 below.

**[0068]** Here, the light emitting element 10 of Example 1 satisfies the above-mentioned formulas (1-1) and (1-2). Specifically,  $m_1 = m_2 = 1$ . However, these values are not limiting, and for example,  $m_1 = m_2 = 0$  can be set. The values of the optical distance ( $OL_1 + OL_2$ ) obtained from the formulas (1-1) and (1-2) are shown in Table 1 below.

[Table 1]

	m <sub>1</sub>	m <sub>2</sub>	Peak wavelength, $\lambda$	OL <sub>1</sub> + OL <sub>2</sub>
First panel	1	1	530 nm	280 nm
Second panel	1	1	630 nm	230 nm
Third panel/Fourth panel	1	1	440 nm	170 nm

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[0069] The second electrode 32 is covered with a protective layer (planarizing layer) 34 made of an acrylic resin. A lens member (on-chip lens) 60 is arranged on the light outputting side of the organic layer 33. That is, the lens member (on-chip microlens) 60 composed of a well-known material is formed on the protective layer 34 by a well-known method. The light output from the lens member (on-chip lens) 60 is converted into parallel light. The protective layer 34 and the lens member 60 are attached to the second substrate 41 through the sealing resin layer 35. Examples of the materials constituting the sealing resin layer 35 include thermosetting adhesives such as acrylic adhesives, epoxy adhesives, urethane adhesives, silicone adhesives, and cyanoacrylate adhesives, and ultraviolet curable adhesives. In a possible, but not limiting, embodiment, the lens member (on-chip lens) 60 has a plano-convex lens shape, and in the illustrated example, the convex surface is located on the second substrate side. Further, the convex surface can be spherical or aspherical, and the planar shape of the lens member (on-chip lens) 60 may be made to have a shape suitable to a planar shape of the light emitting region, such as a circle, an ellipse, or a rectangle with rounded corners (corner portions).

**[0070]** A light reflecting layer 50 made of Al-Cu, Ag, or Ag-Cu is formed inside the substrate (interlayer insulating layer)  $26 \text{ composed of SiO}_2$  formed based on the CVD method. That is, the substrate (interlayer insulating layer) 26 is configured of two layers, a lower interlayer insulating layer 26 A and an upper interlayer insulating layer 26 B, and the light reflecting layer 50 is located between the lower interlayer insulating layer 26 A and the upper interlayer insulating layer 26 B. When the light reflecting layer 50 is configured of silver (Ag), for example, a base film composed of TiN is preferably formed on the lower interlayer insulating layer 26 A in order to control the crystal state of the light reflecting layer 50 to be formed. It is not necessary to form a TiN layer on the light reflecting layer 50 Eurther, the lower interlayer insulating layer and the upper interlayer insulating layer may be configured of the same material, or may be configured of different materials in order to set  $0 \text{L}_1$  to an appropriate value.

**[0071]** Further, a light emitting element drive unit is provided below the substrate (interlayer insulating layer) 26. The light emitting element drive unit may have a well-known circuit configuration. The light emitting element drive unit is configured of a transistor (specifically, a MOSFET) formed on a silicon semiconductor substrate corresponding to the first substrate 11. The transistor 20 composed of the MOSFET includes a gate insulating layer 22 formed on the first substrate 11, a gate electrode 21 formed on the gate insulating layer 22, a source/drain region 24 formed on the first substrate 11, a channel forming region 23 formed within the source/drain region 24, and an element separation region 25 surrounding the channel forming region 23 and the source/drain region 24. The transistor 20 and the first electrode

31 are electrically connected to each other via a contact plug 27 provided in the substrate 26. In the drawings, one transistor 20 is shown for each light emitting element drive unit.

**[0072]** The second electrode 32 is connected to the light emitting element drive unit via a contact hole (contact plug) (not shown) formed in the substrate (interlayer insulating layer) 26 on the outer peripheral portion of the organic EL panel. On the outer peripheral portion of the organic EL panel, an auxiliary electrode connected to the second electrode 32 may be provided below the second electrode 32, and the auxiliary electrode may be connected to the light emitting element drive unit.

**[0073]** The first electrode 31 functions as an anode electrode, and the second electrode 32 functions as a cathode electrode. The first electrode 31 is composed of a light transmitting material, and the second electrode 32 is composed of a semi-light transmitting material. Specifically, the first electrode 31 is composed of a transparent conductive material layer, more specifically, ITO or IZO, and the second electrode 32 is composed of silver (Ag). The first electrode 31 is formed based on the combination of the vacuum vapor deposition method and the etching method on the substrate (interlayer insulating layer) 26. Further, the second electrode 32 is formed by a film forming method, in particular, such as a vacuum vapor deposition method in which the energy of the film-forming particles is small, and is not patterned. The organic layer 33 is also not patterned. However, the present invention is not limited to this, and the organic layer 33 may be patterned.

**[0074]** In Example 1, the organic layer 33 has a layered structure of, for example, a hole injection layer (HIL), a hole transport layer (HTL), the luminescent layer 33A, an electron transport layer (ETL), and an electron injection layer (EIL). In Fig. 13A, the hole injection layer and the hole transport layer are collectively shown by reference number 33C, and the electron transport layer and the electron injection layer are collectively shown by reference number 33E.

**[0075]** The hole injection layer is a layer that enhances the hole injection efficiency and also functions as a buffer layer that prevents leaks, and has a thickness of, for example, about 2 nm to 10 nm. The hole injection layer is composed of, for example, a hexaazatriphenylene derivative represented by the following formula (A) or formula (B). When the end face of the hole injection layer is in contact with the second electrode, it becomes a main cause of luminance variation between pixels and leads to deterioration of display image quality.

$$\begin{array}{c|c}
R^6 & R^5 \\
X^6 & X^5 \\
X^1 & X^5 \\
R^1 & X^2 & X^4 \\
R^2 & X^3 & R^4
\end{array}$$
(A)

**[0076]** Here,  $R^1$  to  $R^6$  are independently hydrogen, halogen, hydroxy group, amino group, arulamino group, substituted or unsubstituted carbonyl group having 20 or less carbon atoms, substituted or unsubstituted carbonyl ester group having 20 or less carbon atoms, a substituted or unsubstituted alkyl group having 20 or less carbon atoms, a substituted or unsubstituted alkoxy group having 20 or less carbon atoms, a substituted alkoxy group having 20 or less carbon atoms, a substituted or unsubstituted or unsubstituted or unsubstituted heterocyclic group having 30 or less carbon atoms, a nitrile group, a cyano group, a nitro group, or a silyl group, and adjacent  $R^m$  (m = 1 to 6) may be bonded to each other via a cyclic structure. Further,  $X^1$  to  $X^6$  are independently carbon or nitrogen atoms, respectively.

[0077] The hole transport layer is a layer that enhances the hole transport efficiency to the luminescent layer 33A. In the luminescent layer 33A, where an electric field is applied, recombination of electrons and holes occurs to generate light. The electron transport layer is a layer that enhances the electron transport efficiency to the luminescent layer 33A, and the electron injection layer is a layer that enhances the electron injection efficiency into the luminescent layer 33A.

[0078] The hole transport layer is composed of, for example, 4,4',4"-tris (3-methylphenylphenylamino)triphenylamine (m-MTDATA) or α-naphthylphenyldiamine (αNPD) having a thickness of about 40 nm.

[0079] In the red luminescent layer, when an electric field is applied, some of the holes injected from the first electrode 31 and some of the electrons injected from the second electrode 32 are recombined, and red light is generated. Such a red luminescent layer includes, for example, at least one kind of material among a red luminescent material, a hole transport material, an electron transport material and an amphoteric charge transport material. The red luminescent material may be a fluorescent material or a phosphorescent material. The red luminescent layer having a thickness of about 5 nm is composed of, for example, 4,4-bis(2,2-diphenylvinyl)biphenyl (DPVBi) mixed with 30% by mass of 2,6-bis[(4'-methoxydiphenylamino)styryl]-1,5-dicyanonaphthalene (BSN).

**[0080]** In the green luminescent layer, when an electric field is applied, some of the holes injected from the first electrode 31 and some of the electrons injected from the second electrode 32 are recombined, and green light is generated. Such a green luminescent layer includes, for example, at least one kind of material among a green luminescent material, a hole transport material, an electron transport material and an amphoteric charge transport material. The green luminescent material may be a fluorescent material or a phosphorescent material. The green luminescent layer having a thickness of about 10 nm is composed of, for example, DPVBi mixed with 5% by mass of coumarin 6.

**[0081]** In the blue luminescent layer, when an electric field is applied, some of the holes injected from the first electrode 31 and some of the electrons injected from the second electrode 32 are recombined, and blue light is generated. Such a blue luminescent layer includes, for example, at least one kind of material among a blue luminescent material, a hole transport material, an electron transport material and an amphoteric charge transport material. The blue luminescent material may be a fluorescent material or a phosphorescent material. The blue luminescent layer having a thickness of about 30 nm is composed of, for example, DPVBi mixed with 2.5% by mass of 4,4'-bis[2-{4-(N,N-diphenylamino)phenyl} vinyl|biphenyl (DPAVBi).

[0082] The electron transport layer having a thickness of about 20 nm is composed of, for example, 8-hydroxyquinoline aluminum (Alq3). The electron injection layer having a thickness of about 0.3 nm is composed of, for example, LiF or Li<sub>2</sub>O. [0083] However, the materials that make up each layer are exemplary, and are not limiting. Where the luminescent layer 33A is configured of a phosphorescent material, it is possible to increase the luminance by about 2.5 to 3 times as compared with the case where the luminescent layer 33A is configured of a fluorescent material. The luminescent layer 33A can also be made of a thermally activated delayed fluorescence (TADF) material.

**[0084]** The outline of the method for manufacturing of the light emitting element 10 of Example 1 shown in Fig. 1 will be described hereinbelow.

[Step-100]

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[0085] First, a light emitting element drive unit is formed on a silicon semiconductor substrate (first substrate 11) on the basis of a known MOSFET manufacturing process.

[Step-110]

[0086] Next, a substrate (interlayer insulating layer) 26 is formed on the entire surface. Specifically, first, the lower interlayer insulating layer 26A is formed based on a CVD method, the light reflecting layer 50 is formed on the lower interlayer insulating layer 26A on the basis of a sputtering method, the light reflecting layer 50 is patterned on the basis of an etching method, and then upper interlayer insulating layer 26B is formed on the lower interlayer insulating layer

26A and the light reflecting layer 50 on the basis of a CVD method. The illustrated interlayer insulating layer 26 is configured of the lower interlayer insulating layer 26A and the upper interlayer insulating layer 26B.

[Step-120]

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[0087] Then, a connection hole is formed in the portion of the substrate 26 (including the light reflecting layer 50) located above one source/drain region of the transistor 20 on the basis of photolithography technique and etching technique. Then, the first electrodes 31 can be formed on a portion of the substrate 26 by forming a metal layer on the substrate 26 including the connection holes on the basis of, for example, a sputtering method, and then patterning the metal layer on the basis of photolithography technique and etching technique. The first electrode 31 is separated for each light emitting element. At the same time, a contact hole (contact plug) 27 for electrically connecting the first electrode 31 and the transistor 20 can be formed in the connection hole. The contact hole (contact plug) 27 is made of, for example, tungsten (W). The light reflecting layer 50 is connected to the contact hole (contact plug) 27.

<sup>15</sup> [Step-130]

**[0088]** Next, for example, after forming the insulating layer 28 on the entire surface on the basis of a CVD method, the insulating layer 28 is left on the substrate 26 between the first electrode 31 and the first electrode 31 on the basis of photolithography technique and etching technique.

[Step-140]

**[0089]** After that, the organic layer 33 is formed on the first electrodes 31 and the insulating layer 28 by, for example, a PVD method such as a vacuum deposition method or a sputtering method, a coating method such as a spin coating method or a die coating method, or the like. In some cases, the organic layer 33 may be patterned into a desired shape.

[Step-150]

[0090] Next, the second electrode 32 is formed on the entire surface on the basis of, for example, a vacuum vapor deposition method. In some cases, the second electrode 32 may be patterned into a desired shape. In this way, the organic layer 33 and the second electrode 32 can be formed on the first electrode 31.

[Step-160]

[0091] Then, the protective layer 34 is formed on the entire surface on the basis of a coating method, and then the top surface of the protective layer 34 is planarized. Since the protective layer 34 can be formed based on the coating method, there are few restrictions on the processing process and the material selection range is wide. Then, the lens member 60 is formed on the protective layer 34 by a well-known method.

40 [Step-170]

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**[0092]** Then, the protective layer 34, the lens member 60, and the second substrate 41 are bonded together by a sealing resin layer 35 composed of an acrylic adhesive. In this way, the light emitting element (organic EL element) 10 shown in Fig. 1 and the organic EL panel of Example 1 can be obtained.

[0093] The outputting state of light from the light emitting element 10 is schematically shown in Fig. 12 where the light beam is indicated by an arrow and the directional half-value angle is indicated by "θhalf".

**[0094]** Here, in the conventional organic EL element, the luminescent layer constituting the organic EL element is configured by, for example, layering a red luminescent layer, a green luminescent layer and a blue luminescent layer, causing the output of white light, providing a red color filter layer to obtain a red light organic EL element 100R, providing a green color filter layer to obtain a green light organic EL element 100G, and providing a blue color filter layer to obtain a blue light organic EL element 100B. Such a layered structure of the red luminescent layer, the green luminescent layer, and the blue luminescent layer may be hereinafter referred to as an "RGB layered structure" for convenience. In addition, a resonator structure is adopted, and normally, the values of  $m_1$  and  $m_2$  in the formulas (1-1) and (1-2) are set to "0" or "1", respectively, but these values are not limiting.

**[0095]** In the light emitting element 10 of Example 1, when the light intensity (light intensity of the light output from the light emitting element 10) at the center line passing through the center of the light emitting unit of the light emitting element 10 is denoted by I<sub>0</sub> and the light intensity (light intensity of the light output from the light emitting element 10) at the center line passing through the center of the light emitting unit of the light emitting element 10 without the light

reflecting layer 50 is denoted by I<sub>conv</sub>,

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$$I_0/I_{conv} \geq 5$$

is satisfied. In Fig. 17, the emission spectra of the light emitting element of Example 1, the light emitting elements of Examples 2 and 3 described hereinbelow, and the conventional organic EL element are shown in the graph of Fig. 17. In Fig. 17, "A" indicates the emission spectrum of the light emitting element of Example 3, "B" indicates the emission spectrum of the light emitting element of Example 1, and "D" indicates the emission spectrum of the conventional organic EL element. From Fig. 17, in the light emitting device of Example 1,

$$I_0/I_{conv} = 20$$

[0096] In addition, in the light emitting element of Example 2 described hereinbelow,

$$I_0/I_{conv} = 40$$
,

and in the light emitting device of the third embodiment described hereinbelow,  $I_0/I_{conv} = 80$ .

[0097] Figs. 19A and 19B show a schematic arrangement diagram of the color filter layer and a schematic arrangement diagram of the light emitting region in the conventional organic EL element. Let "a" be the length of one side of one pixel having a square planar shape. The surface area occupied by the red light organic EL element 100R, the green light organic EL element 100B is  $(1/3)a^2$ . Further, since the red light organic EL element 100R, the green light organic EL element 100G, and the blue light organic EL element 100B are provided so as to be separated from each other, the light emitting region is, for example,  $(1/3) \times 0.75 = 0.25a^2$ . When the current flowing through these organic EL elements for light emission of the red light organic EL element 100R, the green light organic EL element 100B is taken as "1.00", for example, the ratio of the current flowing through the green light organic EL element 100B is, for example, "0.38". Further, each of the red light organic EL element 100R, the green light organic EL element 100G, and the blue light organic EL element 100B is provided with a color filter layer, and about 10% of the light emitted in the luminescent layer of the organic EL element is absorbed by the color filter layer. Therefore, the luminance efficiency of the conventional green light organic EL element 100G is

(Ratio of current flowing through the green light organic EL element 100G)  $\times$  (Light transmittance of the color filter layer)  $\times$  (Ratio occupied by the light emitting region of the green light organic EL element 100G in one pixel) is

$$= 0.38 \times 0.9 \times 0.25a^2$$

$$= 0.0855 \times a^2$$
.

**[0098]** Meanwhile, in the light emitting element 10 of Example 1, the ratio of the current flowing through the light emitting element 10 is 1.00, the color filter layer is not provided, and the area occupied by the light emitting element 10 in one pixel is a<sup>2</sup>. Therefore, the luminance efficiency of the light emitting element 10 of Example 1 is 1/0.0855 = 12 times the luminance efficiency of the conventional green light organic EL element G. Actually, it is as shown in "C" and "D" of Fig. 17. Thus, the light emitting element 10 of Example 1 can achieve a very high luminance efficiency as compared with the conventional organic EL element.

**[0099]** Further, in the conventional organic EL element, it is difficult to design the maximum light emission position in the luminescent layer and to control the maximum light emission position at the time of manufacturing the luminescent layer. Further, the material constituting the blue luminescent layer generally has a shorter life than the materials constituting the red luminescent layer and the green luminescent layer. Therefore, when the light emission state of the blue luminescent layer deteriorates, the white chromaticity point moves from the desired chromaticity point, and the panel becomes unusable. That is, the life of the light emitting element having the RGB layered structure is defined by the material constituting the blue luminescent layer. In addition, when the RGB laminated structure is configured, unintended interference may occur.

**[0100]** Meanwhile, where the projection type display device of Example 1 is configured of four panels, namely, one red light outputting panel that outputs red light, one green light outputting panel that outputs green light, and two blue light outputting panels that output blue light, the drive current in the blue light outputting panel can be reduced, and as a result, the life of the blue light outputting panels can be extended. Moreover, since the life of the blue light outputting panels can be extended, the selection range and the degree of freedom of selection of the material constituting the luminescent layer of the light emitting element constituting the red light outputting panel and the green light outputting panel are increased. As described above, in Example 1, it is possible to provide a projection type display device capable of forming an image by self-luminous type light emitting elements, and also to provide a light emitting element suitable for use in such projection type display device or a planar light emitting device. Further, since the number of layers constituting the organic layer can be reduced as compared with the conventional organic EL element, the drive voltage of the light emitting element can be reduced.

**[0101]** Fig. 5 shows a schematic partial cross-sectional view of Modification Example 1 of the light emitting element of Example 1 and the panel constituting the projection type display device of Example 1. In the light emitting element 10 of Example 1 shown in Fig. 1, the light reflecting layer 50 was connected to the contact hole (contact plug) 27, but in the light emitting element 10 of Modification Example 1, the light reflecting layer 50 is not connected to the contact hole (contact plug) 27.

**[0102]** Fig. 6 shows a schematic partial cross-sectional view of Modification Example 2 of the light emitting element 10 of Example 1 and the panel constituting the projection type display device of Example 1. In Modification Example 2, the first electrode 31 and the light reflecting layer 50 are surrounded by a light shielding portion or a light reflecting portion (reflector portion). A light shielding portion 51 is provided between the light emitting element 10 and the light emitting element 10, or a light reflecting portion 51 is provided. The light shielding portion 51 or the light reflecting portion 51 is connected to the light reflecting layer 50 and the first electrode 31.

**[0103]** Fig. 7 is a schematic partial cross-sectional view of Modification Example 3 of the light emitting element 10 of Example 1 and the panel constituting the projection type display device of Example 1, and Fig. 8 is a plan view schematically showing a metal thin-film filter layer in the light emitting element 10 of Example 1 and the panel constituting the projection type display device of Example 1. In this Modification Example 3, a metal thin-film filter layer 52 is further formed between the first electrode 31 and the light reflecting layer 50. The metal thin-film filter layer 52 is composed of, for example, a gold (Au) thin film or a silver (Ag) thin film, and a large number of pores 53 having a size of about 200 nm are formed and arranged two-dimensionally in these thin films.

**[0104]** Fig. 9 is a schematic partial cross-sectional view of Modification Example 4 of the light emitting element 10 of Example 1 and the panel constituting the projection type display device of Example 1. In Modification Example 4, a light absorption layer (black matrix layer) 54 is formed between the lens members (on-chip microlenses) 60 of the adjacent light emitting elements 10. As a result, the occurrence of partial duplication of unit pixels can be reliably suppressed.

**[0105]** Fig. 10 is a conceptual diagram of Modification Example 5 of the panel constituting the projection type display device of Example 1. In Modification Example 5, there are provided an image synthesizing means 71 (specifically, an unpolarized dichroic prism 72) for synthesizing images output from a plurality of (specifically, three) panels 1R, 1G and 1B into one image, and a projection lens system 70 on the light outputting side of the image synthesizing means 71. Alternatively, Figs. 11A and 11B are conceptual diagrams of Modification Example 6 of the panel constituting the projection type display device of Example 1, and in Modification Example 6, there are provided an image synthesizing means 71 (specifically, a Philips prism 73) for synthesizing images output from a plurality of (specifically, three or four) panels 1R, 1G and 1B (see Fig. 11A) or panels 1R, 1G, 1B<sub>1</sub>, and 1B<sub>2</sub> (see Fig. 11B) into one image, and a projection lens system 70 on the light outputting side of the image synthesizing means 71. The Philips prism 73 has an air gap between the two prisms, but it can also be gapless without an air gap. Here, the light incident on the image synthesizing means 71 is preferably parallel light, whereby bright light can be made incident on the image synthesizing means 71, and three or four panels can be combined into one module.

# Example 2

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**[0106]** Example 2 relates to a light emitting element according to the first aspect of the present disclosure, and a projection type display device (projector) according to the first aspect and the third aspect of the present disclosure. A schematic partial cross-sectional view of the light emitting element of Example 2 is shown in Fig. 13B, and a schematic partial cross-sectional view of the panel constituting the light emitting element of Example 2 and the projection type display device of Example 2 is shown in Fig. 14.

[0107] A light emitting element 10' of Example 2 comprises:

#### a first electrode 31;

an organic layer 33 formed on the first electrode 31 and including a luminescent layer composed of an organic luminescent material; and

a second electrode 32 formed on the organic layer 33, wherein the luminescent layer is configured by layering a plurality (specifically, two layers in Example 2) of luminescent layers 33A and 33B that emit light of the same color.

[0108] Further, the projection type display device of Example 2 is configured of

a panel comprising:

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a first substrate 11;

a second substrate 41; and

a plurality of light emitting elements 10' sandwiched between the first substrate 11 and the second substrate 41, wherein

each light emitting element 10' includes

a first electrode 31;

an organic layer 33 formed on the first electrode 31 and including a luminescent layer composed of an organic luminescent material; and

a second electrode 32 formed on the organic layer 33,

the luminescent layer being configured by layering a plurality of luminescent layers 33A and 33B that emit light of the same color.

[0109] Alternatively, the projection type display device of Example 2 is configured of

a panel comprising:

a first substrate 11;

a second substrate 41; and

a plurality of light emitting elements sandwiched between the first substrate 11 and the second substrate 41, wherein

each light emitting element is configured of the light emitting element 10' of Example 2.

**[0110]** Here, for example, an intermediate layer (charge generation layer) 33D composed of Li is formed between the luminescent layer 33A and the luminescent layer 33B on the basis of the sputtering method. The thickness of the intermediate layer 33D is, for example, 2 nm to 10 nm. The plurality of luminescent layers 33A and 33B have the same composition.

[0111] Fig. 18A shows the emission spectrum of the light emitting element of Example 2 (see "A" in Fig. 18A) and the emission spectrum of the conventional organic EL element (see "B" in Fig. 18A). Further, Fig. 18B shows the relationship between the light outputting angle  $\psi$  from the center line passing through the center of the light emitting unit of the light emitting element and the light intensity (luminance). From Fig. 18B, it can be seen that the conventional organic EL element (see "B" in Fig. 18B) emits Lambersian radiation. Meanwhile, in the light emitting element 10 of Example 2 (see "A" in Fig. 18B), the directional half-value angle  $\theta_{half}$  is 25 degrees or less (see also Fig. 12). The directional half-value angle of Lambersian radiation of the conventional organic EL element is about 70 degrees. That is, the light output from the light emitting element of Example 2 has higher directivity than the conventional organic EL element, or is close to parallel light.

**[0112]** As shown in Fig. 18B, in the light emitting element of Example 2, the value of  $\theta$  can be made smaller than that of the conventional organic EL element. Therefore, the value of the electric field intensity  $E_t$  of the light output from the second electrode 32 can be increased. That is, the light intensity from the light emitting element can be increased.

**[0113]** As shown in Fig. 18A, in the light emitting element 10 of Example 2, the value of the full width at half maximum (FWHM) of the light output from the light emitting element is 30 nm or less. That is, the light emitting element of Example 2 has a sharper light emission spectrum than the conventional organic EL element. In Fig. 18A, the full width at half maximum (FWHM) of the light emitting element 10 of Example 2 is indicated by a black arrow, and the full width at half maximum (FWHM) of the conventional organic EL element is indicated by a gray arrow.

**[0114]** In Example 2, the luminescent layer is formed by layering a plurality of luminescent layers that emits light of the same color. Therefore, in the panel of Example 2 configured of a light emitting elements including a luminescent layer formed by layering a plurality of luminescent layers that emit light of the same color, the light intensity can be substantially doubled as compared with the panel of Example 1 (see "C" in Fig. 17) configured of light emitting elements provided with one luminescent layer as shown in "B" of Fig. 17. That is, the luminance efficiency of the light emitting element of Example 2 is about 24 times (=  $2 \times 12$  times) as compared with the luminance efficiency of the conventional

green light organic EL element, and is actually such as shown in "B" and "D" in Fig. 17.

**[0115]** Since the configuration and structure of the light emitting element and the projection type display device of Example 2 can be substantially the same as the configuration and structure of the light emitting element and the projection type display device of Example 1, except for the above points, detailed description will be omitted.

Example 3

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**[0116]** The panel in the projection type display device of the third embodiment is a combination of the panel in the projection type display device of Example 1 and the panel in the projection type display device of Example 2. That is, in the projection type display device (projector) of the third embodiment, the luminescent layer in the light emitting element constituting the panel is formed by layering a plurality of luminescent layers 33A and 33B that emit light of the same color. **[0117]** When a resonator structure is formed between the light reflecting layer 50 and the second interface as described in Example 1, in an optical resonator of Fabry Perot, assuming that the light reflectance of the light transmittance is  $t_F$ , the light reflectance of the second electrode 32 is  $t_B$  and the light transmittance is  $t_B$ , the electric field intensity of the light emitted in the luminescent layer 33A is  $t_B$ , and the electric field intensity of the light output from the second electrode 32 is  $t_B$ .

$$|E_t/E_i|^2 = t_F^2/\{1 + (\mathbf{a} \cdot \mathbf{r}_F)^2 + 2\mathbf{a} \cdot \mathbf{r}_F \cdot \cos(\delta)\}$$
 (2)

**[0118]** However, assuming that a is the absolute value of light intensity, the maximum peak wavelength of the spectrum of light generated in the luminescent layer 33A is  $\lambda$ , the cavity length is L, and the angle of incidence of light on the second interface when the light resonating in the cavity collides with the second interface is  $\theta$ ,

$$\delta = 2\pi (2nL/\lambda)\cos(\theta) \tag{3}$$

**[0119]** Here, n is a positive integer, and L is the value of optical distance  $(OL_1 + OL_2)$ .

**[0120]** Further, the value of the full width at half maximum (FWHM) of the light output from the light emitting element can be represented by.

$$FWHM = c(1 - r_F)/\{2\pi L(r_F)^{1/2}\}$$
 (4)

where c is the speed of light. In Example 2, each of the values of m<sub>1</sub> and m<sub>2</sub> is set to "1". That is, the resonator length L is set to a large value. Therefore, the value of FWHM represented by formula (4) can be made smaller than that of the conventional organic EL element.

[0121] Furthermore, the light emitting element of Example 3 further includes a light reflecting layer 50, the light emitted in the luminescent layer is resonated between the light reflecting layer 50 and an second interface of the second electrode 32 and the organic layer 33, and a portion of the light is output from the second electrode 32. The light reflecting layer 50 may be arranged below the first electrode 31, as in Example 1, or may be arranged above the first electrode 31 and below the luminescent layer 33A. Alternatively, the formation of the light reflecting layer 50 may be omitted, and the first electrode 31 may also serve as the light reflecting layer 50. Such a resonator structure can be substantially the same as the resonator structure described in Example 1. The increase in luminance is actually as shown in "A" and "D" in Fig. 17.

**[0122]** The light emitting element of Example 3 also satisfies the above-mentioned formulas (1-1) and (1-2). Specifically,  $m_1 = m_2 = 1$ . However, these values are not limiting, and for example,  $m_1 = m_2 = 0$  can be set. Table 2 below shows the values of the optical distance (OL<sub>1</sub> + OL<sub>2</sub>) obtained from formulas (1-1) and (1-2) when  $m_1 = m_2 = 0$ . The peak wavelength  $\lambda$  is as shown in Table 1.

[Table 2]

	m <sub>1</sub>	m <sub>2</sub>	OL <sub>1</sub> + OL <sub>2</sub>
First panel	0	0	110 nm
Second panel	0	0	70 nm to 90 nm
Third panel/Fourth panel	0	0	60 nm

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**[0123]** Further, the results of obtaining the xy chromaticity coordinates of the first panel, the second panel, and the third panel/fourth panel are shown in Table 3 below, and the red color of the BT. 2020 standard can be expressed.

[Table 3]

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	Drive voltage	Efficien cy (cd/A)	Х	у
First panel	5.1	23.2	0.700	0.300
Second panel	6.3	114.4	0.318	0.634
Third panel/Fourth panel	6.2	4.4	0.156	0.093

**[0124]** Since the configuration and structure of the light emitting element and the projection type display device of Example 3 can be substantially the same as the configuration and structure of the light emitting element and the projection type display device of Example 1 and Example 2, except for the above points, detailed description will be omitted.

**[0125]** Although the present disclosure has been described above based on preferred examples, the present disclosure is not limited to these examples. The configuration and structural configuration of the light emitting element, projection type display device, and panel described in the examples are merely exemplary and can be changed as appropriate, and the manufacturing method of the light emitting element is also merely exemplary and can be changed as appropriate. The panel can be embodied not only to be flat, but can also be embodied to be curved. Further, a projection type display device can be configured of a panel provided with light emitting elements that output light other than visible light, for example, infrared light, or a combination of such a panel and a panel provided with light emitting elements that output visible light.

**[0126]** In the light emitting elements of the examples, the insulating layer 28 is left on the substrate 26 between the first electrode 31 and the first electrode 31, but the insulating layer 28 does not have to be formed, and in this case the organic layer 33 may be formed on the substrate 26 and the first electrode 31. Because of a stepped portion generated on the first electrode 31, a cut portion may be formed between a layer (for example, an intermediate layer) constituting a portion of the organic layer 33 formed on the substrate 26, but this is not a problem.

[0127] In the examples, the lens member (on-chip lens) 60 was embodiment to have a plano-convex lens shape, and the convex surface was embodied to be located on the second substrate side, but the convex surface can be also embodied to be located on the first substrate side as in Modification Example 7 of Example 1 shown in Fig. 15. The light emitting element having such a configuration can be obtained by forming, for example, a base layer 36 composed of an acrylic resin on the inner surface 41A of the second substrate 41 facing the first substrate 11, forming the lens member (on-chip lens) 60 on the base layer 36 (on the surface of the base layer 36 facing the protective layer 34), and then joining the protective layer 34 to the lens member 60 and the base layer 36 (corresponding to the structure) through the resin layer 35 (sealing resin layer).

[0128] Further, a light guide path 37 may be formed above (light outputting side) the lens member (on-chip lens) 60, as in Modification Example 8 of Example 1 shown in Fig. 16. Specifically, the base layer 36 is formed on the inner surface 41A of the second substrate 41, and an opening (hole) 38 is formed in the region of the base layer 36 on which the lens member (on-chip lens) 60 is to be formed, and a light reflecting film 39A is formed on the side surface of the opening 38. Then, for example, a transparent material 39B made of an acrylic resin is embedded in the opening 38, and then a lens member (on-chip lens) 60 is formed on the transparent material 39B (on the surface of the transparent material 39B facing the protective layer 34). Next, the protective layer 34 is joined to the lens member 60 and the base layer 36 (corresponding to the structure) through the resin layer (sealing resin layer) 35 thereby making it possible to obtain a light emitting element having such a configuration. Here, by selecting materials such that the value of the refractive index of the material constituting the light reflecting film 39A is smaller than the value of the refractive index of the material constituting the transparent material 39B, the probability that the light from the transparent material 39B that collides with the light reflecting film 39A is totally reflected by the light reflecting film 39A is increased, and the light output from the lens member 60 can be more reliably guided to the space located above the lens member 60. Further, it is preferable that the side surface of the opening (hole) 38 has a shape narrowed toward the second substrate 41 (so-called reverse taper shape in the state shown in Fig. 16). Modification Examples 7 and 8 of Example 1 shown in Figs. 15 and 16 can be adopted in other modification examples of Example 1 and other examples.

**[0129]** Further, the projection type display device according to the first to third aspects of the present disclosure including various preferable embodiments described above may be also incorporated in, for example, a personal computer, a mobile phone, a PDA (personal digital assistant), game equipment, a watch, a bracelet, a ring, and the like.

**[0130]** A planar light emitting device can also be configured from the light emitting elements and panels described in the examples. That is, the planar light emitting device is

configured of a panel comprising:

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the first substrate 11;

the second substrate 41: and

a plurality of light emitting elements sandwiched between the first substrate 11 and the second substrate 41, wherein each light emitting element is configured of the light emitting element 10, 10' of Example 1 to Example 3. With such a planar light emitting device, for example, a bulletin board such as a sign board, a poster, and a blackboard, an electronic advertisement, and an electronic POP can be configured, and various lighting devices including various backlight devices and planar light source devices can be configured.

**[0131]** A light shielding region may be provided between the light emitting element and the light emitting element in order to prevent light output from a certain light emitting element from penetrating into the light emitting element adjacent to the certain light emitting element and causing optical crosstalk. That is, a groove may be formed between the light emitting element and the light emitting element, and a light shielding material may be embedded in the groove to form a light shielding region. By providing the light shielding region in this way, it is possible to reduce the rate at which the light output from a certain light emitting element penetrates into the adjacent light emitting element, and it is possible to reliably suppress the occurrence of partial duplication of unit pixels.

**[0132]** The projection type display devices of Examples 1 to 3 can be applied to various technical fields. For example, when adopted in a display device constituting a head-mounted display (HMD), this display device includes

a frame attached to the observer's head and an image display device attached to the frame, wherein the image display device includes an image forming apparatus provided with the projection type display device of

[0133] Examples 1 to 3 and

an optical device that receives and outputs light output from the image forming apparatus, and the optical device is configured of

a light guide plate that outputs light toward the observer after the light incident from the image forming apparatus propagates inside by total reflection;

a first deflecting means for deflecting the light incident on the light guide plate, so that the light incident on the light guide plate, so that the light incident on the light guide plate, and

a second deflecting means that deflects the light propagated inside the light guide plate by total reflection multiple times in order to output the light propagated inside the light guide plate by total reflection from the light guide plate.

**[0134]** Alternatively, the projection type display device of Examples 1 to 3 can also be adopted in an image forming apparatus in a retinal projection type display based on Maxwell vision in which an image is displayed by directly projecting an image (light beam) onto the observer's retina, specifically, in a retinal projection type head-mounted display.

**[0135]** Alternatively, it can also be adopted in a light source (projector) for structured light, and in this case, a 3D sensing device includes

an image forming apparatus provided with the projection type display device of Examples 1 to 3 and an image capturing device that captures an image projected onto an object by the projection type display device.

**[0136]** Alternatively, for example, a wearable device such as a wristwatch, a bracelet, or a ring may be configured to include the projection display devices of Examples 1 to 3.

[0137] The present disclosure can also take the following configurations.

[A01] «Light Emitting Element: First Aspect»

[0138] A light emitting element

a first electrode;

an organic layer formed on the first electrode and including a luminescent layer composed of an organic luminescent material; and

a second electrode formed on the organic layer, wherein

the luminescent layer is configured by layering a plurality of luminescent layers that emit light of the same color.

[A02]

<sup>5</sup> **[0139]** The light emitting element according to [A01], wherein an intermediate layer is provided between the luminescent layer and the luminescent layer.

[A03]

10 **[0140]** The light emitting element according to [A02], wherein the intermediate layer is configured of at least one kind of material selected from the group consisting of, lithium (Li), calcium (Ca), sodium (Na), cesium (Cs), molybdenum oxide (MoO<sub>3</sub>), vanadium oxide (V<sub>2</sub>O<sub>5</sub>) and tungsten oxide (WO<sub>3</sub>).

[A04]

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**[0141]** The light emitting element according to any one of [A01] to [A03], wherein the plurality of luminescent layers have the same composition.

[A05]

[0142] The light emitting element according to any one of [A01] to [A04], further comprising

a light reflecting layer, wherein

light emitted in the luminescent layer is resonated between the light reflecting layer and an interface of the second electrode and the organic layer, and a portion of the light is output from the second electrode.

[A06]

[0143] The light emitting element according to [A05], wherein the light reflecting layer is arranged below the first electrode.

[A07]

[0144] The light emitting element according to [A05], wherein the light reflecting layer is arranged above the first electrode.

[80A]

**[0145]** The light emitting element according to any one of [A05] to [A07], wherein where an optical distance from a maximum light emission position of the luminescent layer to the luminescent layer is denoted by OL<sub>1</sub>, an optical distance from the maximum light emission position of the luminescent layer to the interface is denoted by OL<sub>2</sub>, and m<sub>1</sub> and m<sub>2</sub> are integers, the following formulas (1-1) and (1-2) are satisfied.

$$0.7\{-\Phi_1/(2\pi) + m_1\} < 2 \times OL_1/\lambda < 1.2\{-\Phi_1/(2\pi) + m_1\}$$
 (1-1)

$$0.7\{-\Phi_2/(2\pi) + m_2\} < 2 \times OL_2/\lambda < 1.2\{-\Phi_2/(2\pi) + m_2\}$$
 (1-2)

50 [0146] Here,

λ: maximum peak wavelength of the spectrum of light generated in the luminescent layer (or the desired wavelength in the light generated in the luminescent layer);

 $\Phi_1$ : phase shift amount (unit: radian) of reflected light generated at the light reflecting layer (light reflected at the light reflecting layer). However,  $-2\pi < \Phi_1 \le 0$ ; and

 $\Phi_2$ : phase shift amount (unit: radian) of reflected light (light reflected at the interface) generated at the interface. However,  $-2\pi < \Phi_2 \le 0$ .

[A09]

**[0147]** The light emitting element according to [A07], wherein  $m_1 \ge 1$  and  $m_2 \ge 1$  are satisfied.

<sup>5</sup> [A10]

**[0148]** The light emitting element according to [A08] or [A09], wherein the value of a full width at half maximum of the light output from the light emitting element is 30 nm or less.

<sup>10</sup> [A11]

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**[0149]** The light emitting element according to any one of [A08] to [A10], wherein where a light intensity at a center line passing through the center of a light emitting unit of the light emitting element is taken as 100%, a directional half-value angle, which is the angle formed by the center line and the direction with respect to the center line that has a 50% light intensity, is 25 degrees or less.

[A12]

**[0150]** The light emitting element according to any one of [A01] to [A11], wherein a lens member is arranged on the light outputting side of the organic layer.

[A13]

**[0151]** The light emitting element according to any one of [A01] to [A12], wherein a metal thin-film filter layer is further formed between the first electrode and the light reflecting layer.

[A14]

**[0152]** The light emitting element according to any one of [A01] to [A13], wherein when the light intensity at the center line passing through the center of the light emitting unit of the light emitting element is denoted by I<sub>0</sub> and the light intensity at the center line passing through the center of the light emitting unit of the light emitting element without a light reflecting layer is denoted by I<sub>conv</sub>,

 $m I_0/I_{conv} \geq 5$ 

is satisfied.

[A15]

**[0153]** The light emitting element according to any one of [A01] to [A14], wherein the first electrode and the light reflecting layer are surrounded by a light shielding portion or a light reflecting portion.

[A16]

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**[0154]** The light emitting element according to any one of [A01] to [A15], wherein the first electrode is composed of a light transmitting material, and the second electrode is composed of a semi-light transmitting material.

[A17]

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[0155] The light emitting element according to [A16], wherein

the first electrode is composed of ITO or IZO, and

the second electrode is composed of at least one kind of material selected from the group consisting of Ag, Ag-Mg, Ag-Nd-Cu, Au, Ag-Cu, Al, and Al-Cu.

[B01] «Light Emitting Element: Second Aspect»

[0156] A light emitting element comprising

5 a first electrode;

an organic layer formed on the first electrode and including a luminescent layer composed of an organic luminescent material; and

a second electrode formed on the organic layer, and

further comprising

a light reflecting layer below the first electrode, wherein

light emitted in the luminescent layer is resonated between the light reflecting layer and an interface of the second electrode and the organic layer, and a portion of the light is output from the second electrode.

[B02]

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[0157] The light emitting element according to [B01], wherein

where an optical distance from a maximum light emission position of the luminescent layer to the luminescent layer is denoted by  $OL_1$ , an optical distance from the maximum light emission position of the luminescent layer to the interface is denoted by  $OL_2$ , and  $m_1$  and  $m_2$  are integers, the following formulas (1-1) and (1-2) are satisfied.

$$0.7\{-\Phi_1/(2\pi) + m_1\} \le 2 \times OL_1/\lambda \le 1.2\{-\Phi_1/(2\pi) + m_1\}$$
 (1-1)

$$0.7\{-\Phi_2/(2\pi) + m_2\} \le 2 \times OL_2/\lambda \le 1.2\{-\Phi_2/(2\pi) + m_2\} \tag{1-2}$$

[0158] Here,

λ: maximum peak wavelength of the spectrum of light generated in the luminescent layer (or the desired wavelength in the light generated in the luminescent layer);

 $\Phi_1$ : phase shift amount (unit: radian) of reflected light generated at the light reflecting layer (light reflected at the light reflecting layer). However,  $-2\pi < \Phi_1 \le 0$ ; and

 $\Phi_2$ : phase shift amount (unit: radian) of reflected light (light reflected at the interface) generated at the interface. However,  $-2\pi < \Phi_2 \le 0$ .

[B03]

**[0159]** The light emitting element according to [B02], wherein  $m_1 \ge 1$  and  $m_2 \ge 1$  are satisfied.

40 [B04]

**[0160]** The light emitting element according to [B02] or [B03], wherein the value of a full width at half maximum of the light output from the light emitting element is 30 nm or less.

45 [B05]

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**[0161]** The light emitting element according to any one of [B02] to [B04], wherein where a light intensity at a center line passing through the center of a light emitting unit of the light emitting element is taken as 100%, a directional half-value angle, which is the angle formed by the center line and the direction with respect to the center line that has a 50% light intensity, is 25 degrees or less.

[B06]

**[0162]** The light emitting element according to any one of [B01] to [B05], wherein a lens member is arranged on the light outputting side of the organic layer.

[B07]

**[0163]** The light emitting element according to any one of [B01] to [B06], wherein a metal thin-film filter layer is further formed between the first electrode and the light reflecting layer.

[B08]

**[0164]** The light emitting element according to any one of [B01] to [B07], wherein when the light intensity at the center line passing through the center of the light emitting unit of the light emitting element is denoted by I<sub>0</sub> and the light intensity at the center line passing through the center of the light emitting unit of the light emitting element without a light reflecting layer is denoted by I<sub>conv</sub>,

 $I_0/I_{conv} \ge 5$ 

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is satisfied.

[B09]

<sup>20</sup> **[0165]** The light emitting element according to any one of [B01] to [B08], wherein the first electrode and the light reflecting layer are surrounded by a light shielding portion or a light reflecting portion.

[B10]

<sup>25</sup> **[0166]** The light emitting element according to any one of [B01] to [B09], wherein the first electrode is composed of a light transmitting material, and the second electrode is composed of a light semi-transmitting material.

[B11]

30 [0167] The light emitting element according to [B10], wherein

the first electrode is composed of ITO or IZO, and

the second electrode is composed of at least one kind of material selected from the group consisting of Ag, Ag-Mg, Ag-Nd-Cu, Au, Ag-Cu, Al, and Al-Cu.

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[C01] «Projection Type Display Device: First Aspect»

[0168] A projection type display device configured of

40 a panel comprising:

a first substrate;

a second substrate; and

a plurality of light emitting elements sandwiched between the first substrate and the second substrate, wherein each light emitting element includes

a first electrode;

an organic layer formed on the first electrode and including a luminescent layer composed of an organic luminescent material; and

a second electrode formed on the organic layer,

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the luminescent layer being configured by layering a plurality of luminescent layers that emit light of the same color.

[C02] «Projection Type Display Device: Second Aspect»

55 [0169] A projection type display device configured of

a panel comprising:

- a first substrate:
- a second substrate; and
- a plurality of light emitting elements sandwiched between the first substrate and the second substrate, wherein
- 5 each light emitting element includes
  - a first electrode:
  - an organic layer formed on the first electrode and including a luminescent layer composed of an organic luminescent material; and
  - a second electrode formed on the organic layer, and
  - each light emitting element further includes a light reflecting layer provided below the first electrode, and light emitted in the luminescent layer is resonated between the light reflecting layer and an interface of the second electrode and the organic layer, and a portion of the light is output from the second electrode.

[C03] «Projection Type Display Device: Third Aspect»

[0170] A projection type display device

configured of a panel comprising:

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arior comprising

a first substrate;

- a second substrate; and
- a plurality of light emitting elements sandwiched between the first substrate and the second substrate, wherein
- each light emitting element is configured of the light emitting element according to any one of [A01] to [B11].

[C04]

[0171] The projection type display device according to any one of [C01] to [C03], that is configured of three panels of

one red light outputting panel that outputs red light,

one green light outputting panel that outputs green light, and one blue light outputting panel that outputs blue light.

[C05]

**[0172]** The projection type display device according to [C04], that is configured of four panels and further includes one blue light outputting panel that outputs blue light.

[C06]

[0173] The projection type display device according to [C05], wherein the four panels are arranged in an array.

[C07]

[0174] The projection type display device according to [C05], wherein the four panels are arranged in a 2  $\times$  2 state.

[C08]

**[0175]** The projection type display device according to any one of [C01] to [C07], further comprising a projection lens system on the light outputting side.

[C09]

[0176] The projection type display device according to any one of [C01] to [C07], comprising:

image synthesizing means for synthesizing images output from a plurality of panels into one image, and a projection lens system on the light outputting side of the image synthesizing means.

[C10]

**[0177]** The projection type display device according to [C09], wherein the image synthesizing means is configured of an unpolarized dichroic prism.

[C11]

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**[0178]** The projection type display device according to [C09], wherein the image synthesizing means is configured of a Philips prism.

[C12]

**[0179]** The projection type display device according to any one of [C08] to [C11], wherein light incident on the projection lens system is parallel light.

[C13]

[0180] The projection type display device according to any one of [C01] to [C12], wherein the panel is curved.

[D01] «Planar Light Emitting Device»

[0181] A planar light emitting device configured of

a panel comprising:

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- a first substrate;
- a second substrate; and
- a plurality of light emitting elements sandwiched between the first substrate and the second substrate, wherein

each light emitting element is configured of the light emitting element according to any one of [A01] to [B11].

[E01] «Display Device (HMD)»

[0182] A display device including

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a frame attached to the observer's head and

an image display device attached to the frame, wherein

the image display device includes

an image forming apparatus provided with the projection type display device according to any one of [C01] to [C13] and an optical device that receives and outputs light output from the image forming apparatus, and the optical device is configured of

a light guide plate that outputs light toward the observer after the light incident from the image forming apparatus propagates inside by total reflection;

a first deflecting means for deflecting the light incident on the light guide plate, so that the light incident on the light guide plate, so that the light incident on the light guide plate is totally reflected inside the light guide plate, and

a second deflecting means that deflects the light propagated inside the light guide plate by total reflection multiple times in order to output the light propagated inside the light guide plate by total reflection from the light guide plate.

[E02] «Three-Dimensional Sensing Device»

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[0183] A three-dimensional sensing device including

an image forming apparatus provided with the projection type display device according to any one of [C01] to [C13] and an image capturing device that captures an image projected by the projection type display device on an object.

[E03] «Wearable Device»

[0184] A wearable device including an image forming apparatus provided with the projection type display device

according to any one of [C01] to [C13].

[Reference Signs List]

# <sup>5</sup> [0185]

1G Green light outputting panel (second panel) 181, 182 Blue light outputting panel (third panel, fourth panel) 10 10, 10' Light emitting element 11 First substrate 20 Transistor 21 Gate electrode 22 Gate insulating layer 23 Channel forming region 24 Source/drain region 25 Element separation region 26 Substrate (interlayer insulating layer) 26A Lower interlayer insulating layer 27 Contact plug 28 Insulating layer 27 Contact plug 28 Insulating layer 31 First electrode 32 Second electrode 32 Second electrode 32 Second electrode 25 33 Organic layer 33A, 33B Luminescent layer 33C Hole injection layer and hole transport layer 33D Intermediate layer (charge generation layer) 33E Electron transport layer and electron injection layer 36 Base layer 37 Light guide path 38 Opening 39 Alight reflecting film 39 Transparent material 41 Second substrate 50 Light reflecting layer 51 Light shielding part or light reflecting portion 40 52 Metal thin-film filter layer 53 Pores provided in the metal thin-film filter layer 54 Light absorption layer (black matrix layer) 56 Lens member (on-chip microlens) 70 Projection lens system 45 71 Image synthesis means 72 Unpolarized dichroic prism 73 Philips prism		1R	Red light outputting panel (first panel)
10, 10' Light emitting element 11 First substrate 20 Transistor 21 Gate electrode 22 Gate insulating layer 23 Channel forming region 24 Source/drain region 25 Element separation region 26 Substrate (interlayer insulating layer) 26A Lower interlayer insulating layer 27 Contact plug 28 Insulating layer 31 First electrode 32 Second electrode 32 Second electrode 32 Second electrode 33 Organic layer 33C Hole injection layer and hole transport layer 33D Intermediate layer (charge generation layer) 33E Electron transport layer and electron injection layer 36 Base layer 37 Light guide path 38 Opening 39 ALight reflecting film 39 Transparent material 41 Second substrate 50 Light reflecting layer 51 Light shielding part or light reflecting portion 40 52 Metal thin-film filter layer 53 Pores provided in the metal thin-film filter layer 54 Light absorption layer (plack matrix layer) 56 Projection lens system 57 Ilmage synthesis means 72 Unpolarized dichroic prism		1G	Green light outputting panel (second panel)
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33A, 33B Luminescent layer 33C Hole injection layer and hole transport layer 33D Intermediate layer (charge generation layer) 33E Electron transport layer and electron injection layer 34 Protective layer (planarizing layer) 35 Sealing resin layer 36 Base layer 37 Light guide path 38 Opening 39A Light reflecting film 39B Transparent material 41 Second substrate 50 Light reflecting layer 51 Light shielding part or light reflecting portion 40 52 Metal thin-film filter layer 53 Pores provided in the metal thin-film filter layer 54 Light absorption layer (black matrix layer) 60 Lens member (on-chip microlens) 70 Projection lens system 45 71 Image synthesis means 72 Unpolarized dichroic prism			
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Light reflecting layer Light shielding part or light reflecting portion  Light shielding part or light reflecting portion  Metal thin-film filter layer Sa Pores provided in the metal thin-film filter layer Light absorption layer (black matrix layer) Lens member (on-chip microlens) Projection lens system  Projection lens system  Image synthesis means Unpolarized dichroic prism			
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Light absorption layer (black matrix layer) Lens member (on-chip microlens) Projection lens system Image synthesis means Unpolarized dichroic prism	40	-	-
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70 Projection lens system 45 71 Image synthesis means 72 Unpolarized dichroic prism			
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72 Unpolarized dichroic prism			-
	45	-	<del>-</del> -
73 Philips prism			
		73	Philips prism

# 50 Claims

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# 1. A light emitting element

a first electrode;

an organic layer formed on the first electrode and including a luminescent layer composed of an organic luminescent material; and

a second electrode formed on the organic layer, wherein

the luminescent layer is configured by layering a plurality of luminescent layers that emit light of the same color.

- 2. The light emitting element according to claim 1, wherein an intermediate layer is provided between the luminescent layer and the luminescent layer.
- 3. The light emitting element according to claim 2, wherein the intermediate layer is configured of at least one kind of material selected from the group consisting of, lithium (Li), calcium (Ca), sodium (Na), cesium (Cs), molybdenum oxide (MoO<sub>3</sub>), vanadium oxide (V<sub>2</sub>O<sub>5</sub>) and tungsten oxide (WO<sub>3</sub>).
  - 4. The light emitting element according to claim 1, wherein the plurality of luminescent layers have the same composition.
- 5. The light emitting element according to claim 1, further comprising a light reflecting layer, wherein light emitted in the luminescent layer is resonated between the light reflecting layer and an interface of the second electrode and the organic layer, and a portion of the light is output from the second electrode.
  - **6.** A light emitting element comprising:

a first electrode;

an organic layer formed on the first electrode and including a luminescent layer composed of an organic luminescent material; and

a second electrode formed on the organic layer, and further comprising a light reflecting layer provided below the first electrode, wherein

light emitted in the luminescent layer is resonated between the light reflecting layer and an interface of the second electrode and the organic layer, and a portion of the light is output from the second electrode.

7. The light emitting element according to claim 6, wherein where an optical distance from a maximum light emission position of the luminescent layer to the luminescent layer is denoted by OL<sub>1</sub>, an optical distance from the maximum light emission position of the luminescent layer to the interface is denoted by OL<sub>2</sub>, and m<sub>1</sub> and m<sub>2</sub> are integers, the following formulas (1-1) and (1-2) are satisfied.

$$0.7\{-\Phi_1/(2\pi) + m_1\} < 2 \times OL_1/\lambda < 1.2\{-\Phi_1/(2\pi) + m_1\}$$
 (1-1)

$$0.7\{-\Phi_2/(2\pi) + m_2\} < 2 \times OL_2/\lambda < 1.2\{-\Phi_2/(2\pi) + m_2\}$$
 (1-2)

35 Here,

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λ: maximum peak wavelength of the spectrum of light generated in the luminescent layer (or the desired wavelength in the light generated in the luminescent layer);

 $\Phi_1$ : phase shift amount (unit: radian) of reflected light generated at the light reflecting layer. However,  $-2\pi < \Phi_1 \le 0$ ; and

 $\Phi_2$ : phase shift amount (unit: radian) of reflected light generated at the interface.

However,  $-2\pi < \Phi_2 \le 0$ .

- **8.** The light emitting element according to claim 7, wherein the value of a full width at half maximum of the light output from the light emitting element is 30 nm or less.
- 9. The light emitting element according to claim 7, wherein where a light intensity at a center line passing through the center of a light emitting unit of the light emitting element is taken as 100%, a directional half-value angle, which is the angle formed by the center line and the direction with respect to the center line that has a 50% light intensity, is 25 degrees or less.
- **10.** The light emitting element according to claim 6, wherein a lens member is arranged on the light outputting side of the organic layer.
- 11. The light emitting element according to claim 6, wherein a metal thin-film filter layer is further formed between the first electrode and the light reflecting layer.
  - 12. The light emitting element according to claim 6, wherein

when the light intensity at the center line passing through the center of the light emitting unit of the light emitting element is denoted by  $I_0$  and the light intensity at the center line passing through the center of the light emitting unit of the light emitting element without a light reflecting layer is denoted by  $I_{conv}$ ,

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 $I_0/I_{conv} > 5$ 

is satisfied.

10 **13.** A projection type display device configured of

a panel comprising:

- a first substrate:
- a second substrate: and
- a plurality of light emitting elements sandwiched between the first substrate and the second substrate, wherein

each light emitting element includes

a first electrode;

an organic layer formed on the first electrode and including a luminescent layer composed of an organic luminescent material; and

a second electrode formed on the organic layer,

the luminescent layer being configured by layering a plurality of luminescent layers that emit light of the same color.

14. A projection type display device configured of

a panel comprising:

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- a first substrate;
- a second substrate; and
- a plurality of light emitting elements sandwiched between the first substrate and the second substrate, wherein

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each light emitting element includes

a first electrode;

an organic layer formed on the first electrode and including a luminescent layer composed of an organic luminescent material; and

a second electrode formed on the organic layer, and

each light emitting element further includes a light reflecting layer provided below the first electrode, and light emitted in the luminescent layer is resonated between the light reflecting layer and an interface of the second electrode and the organic layer, and a portion of the light is output from the second electrode.

45 **15.** A projection type display device

configured of a panel comprising:

- a first substrate;
  - a second substrate; and
  - a plurality of light emitting elements sandwiched between the first substrate and the second substrate, wherein

each light emitting element is configured of the light emitting element according to any one of claims 1 to 12.

16. The projection type display device according to any one of claims 13 to 15, that is configured of three panels of

one red light outputting panel that outputs red light, one green light outputting panel that outputs green light, and one blue light outputting panel that outputs blue light.

- 5 17. The projection type display device according to claim 16, that is configured of four panels and further includes one blue light outputting panel that outputs blue light.
  - 18. The projection type display device according to any one of claims 13 to 15, further comprising a projection lens system on the light outputting side.
  - **19.** The projection type display device according to claim 18, comprising:

image synthesizing means for synthesizing images output from a plurality of panels into one image, and a projection lens system on the light outputting side of the image synthesizing means.

20. A planar light emitting device configured of

a panel comprising:

- a first substrate;
- a second substrate; and
- a plurality of light emitting elements sandwiched between the first substrate and the second substrate, wherein
- 25 each light emitting element is configured of the light emitting element according to any one of claims 1 to 12.

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Fig. 1

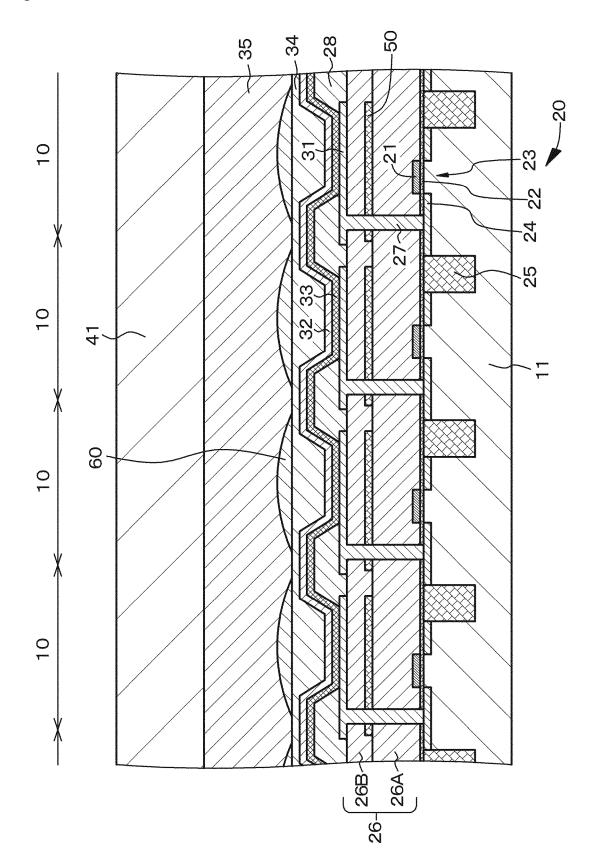


Fig. 2A

1R 1	G 1B	1 1B <sub>2</sub>
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Fig. 2B

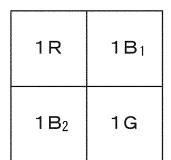


Fig. 3

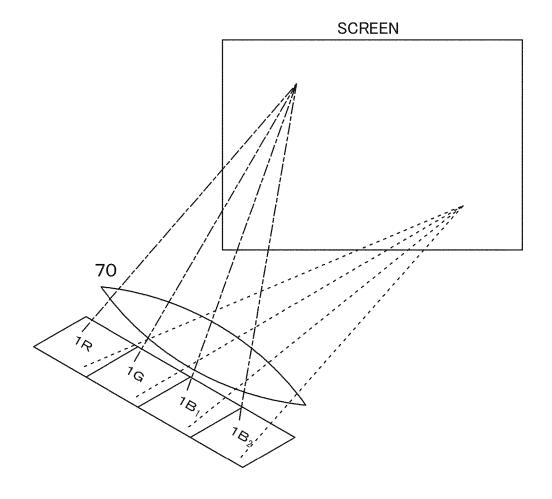


Fig. 4

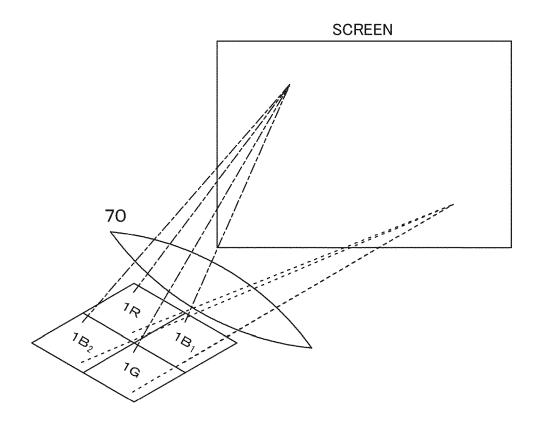


Fig. 5

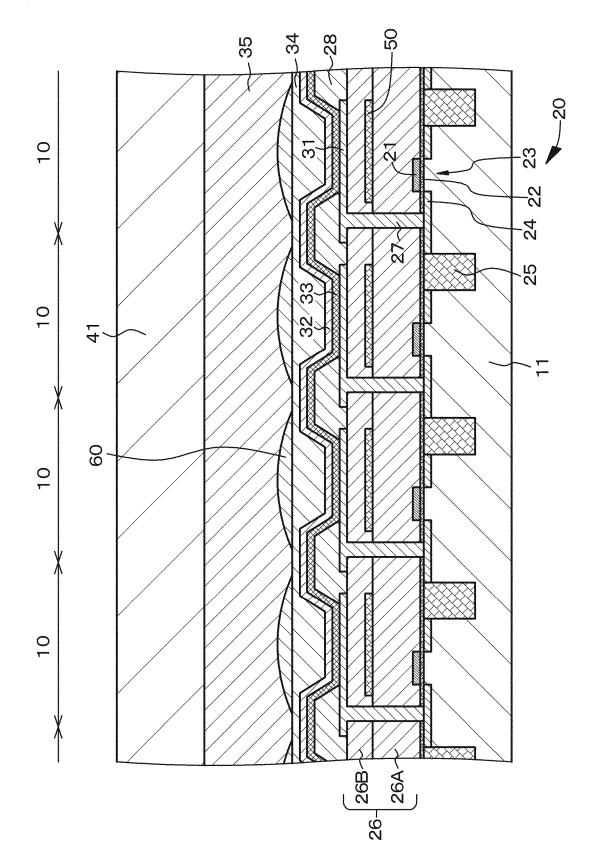


Fig. 6

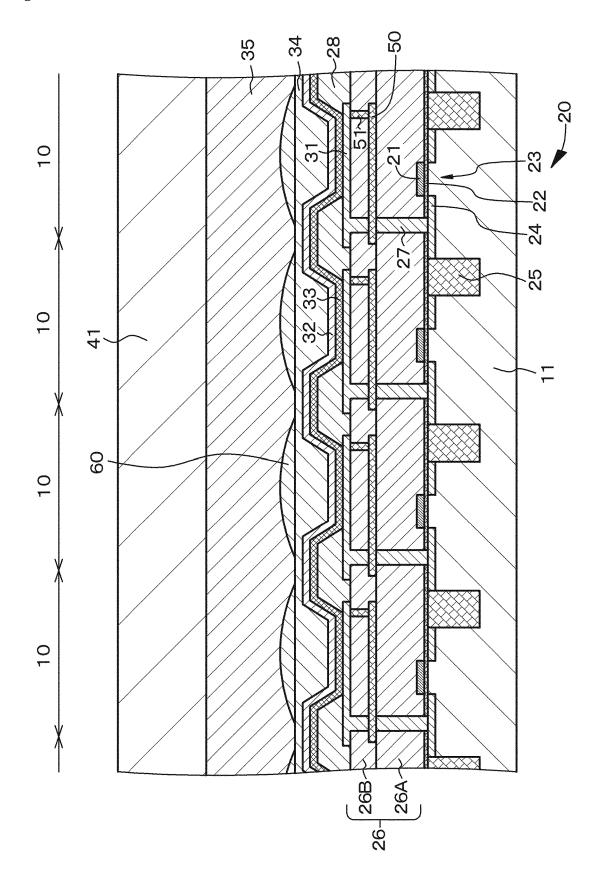


Fig. 7

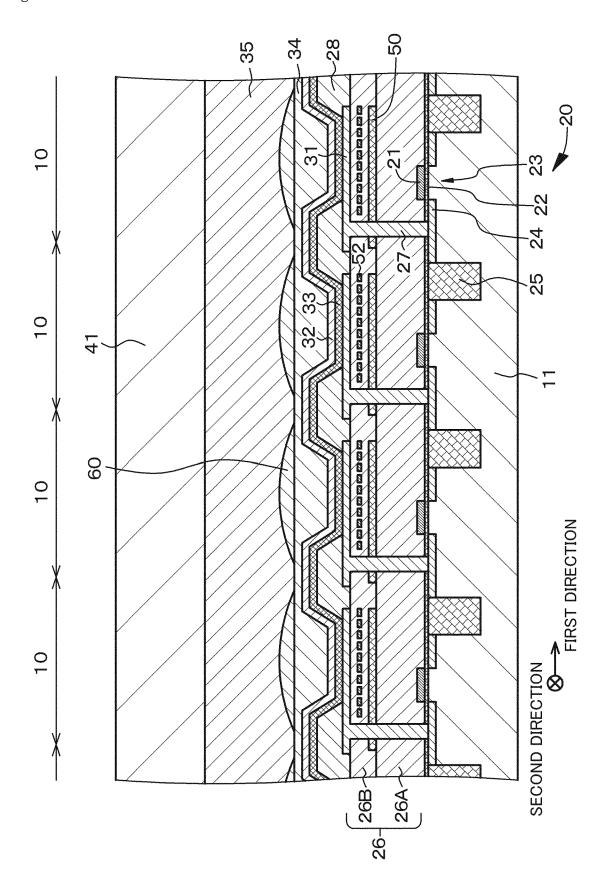


Fig. 8

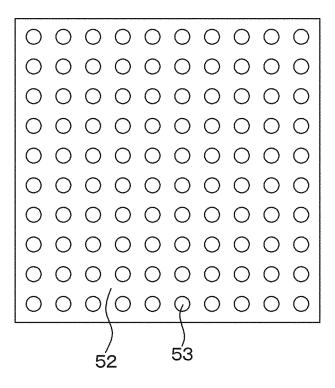


Fig. 9

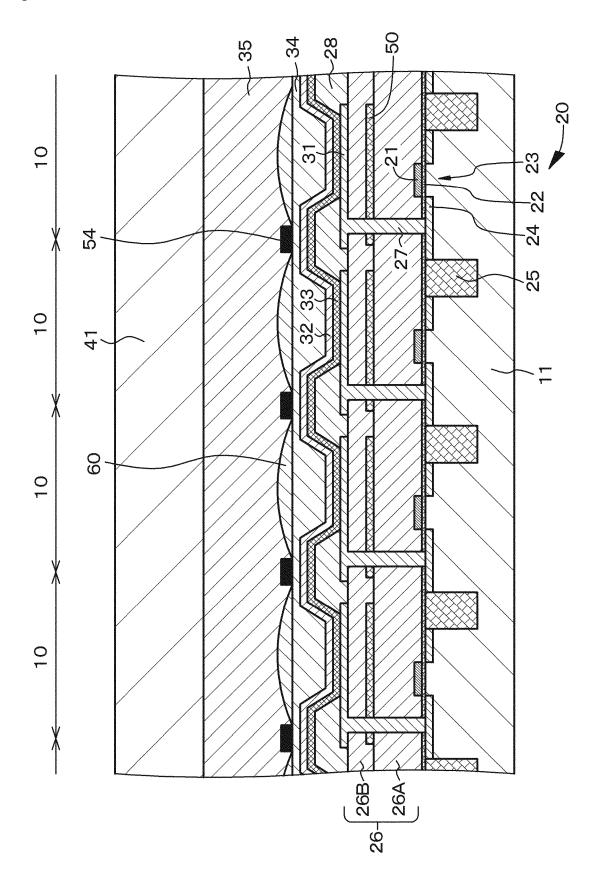


Fig. 10

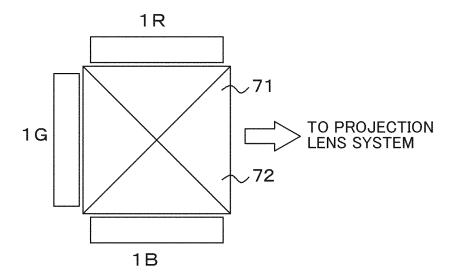


Fig. 11A

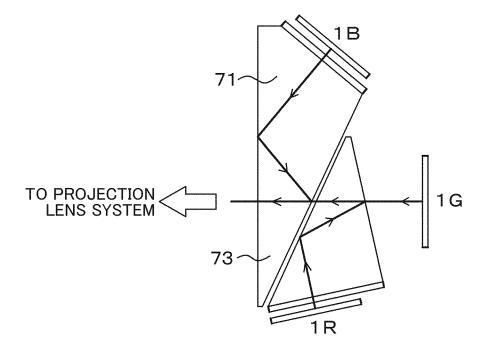


Fig. 11B

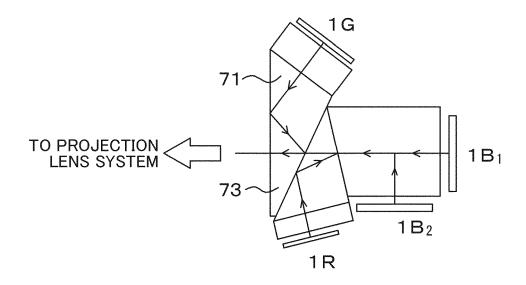


Fig. 12

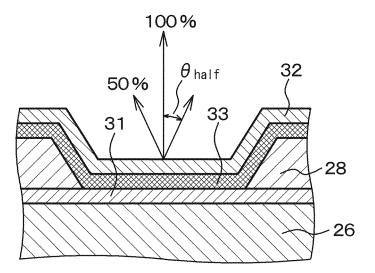


Fig. 13A

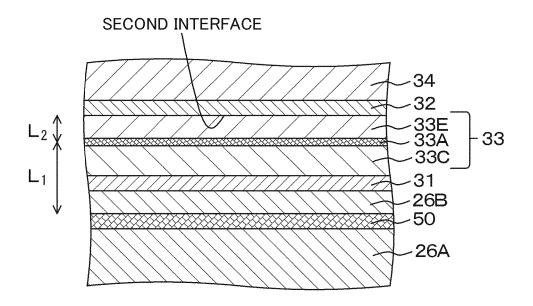


Fig. 13B

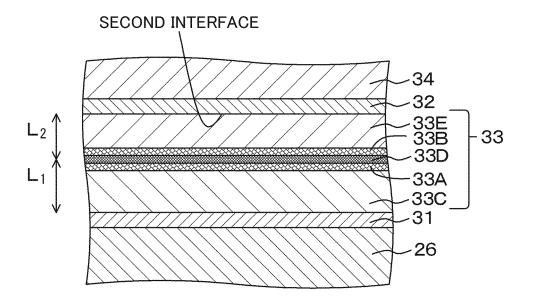


Fig. 14

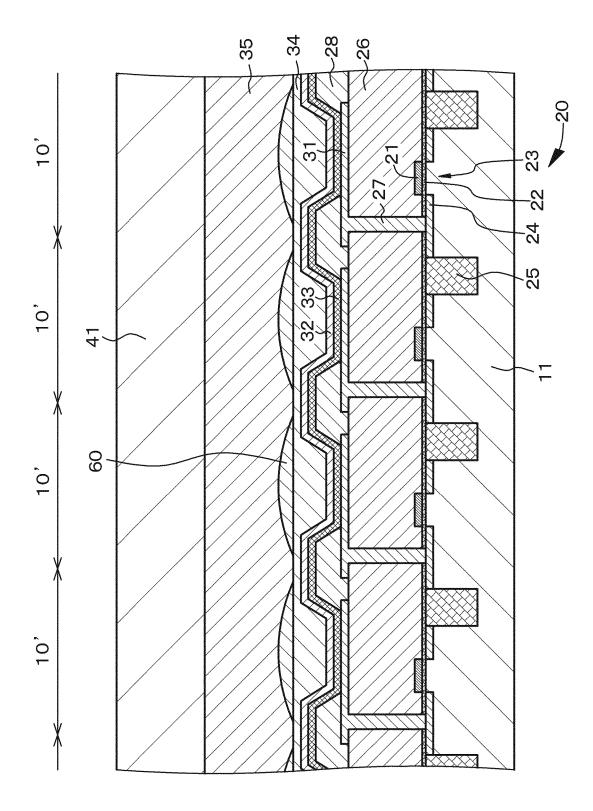


Fig. 15

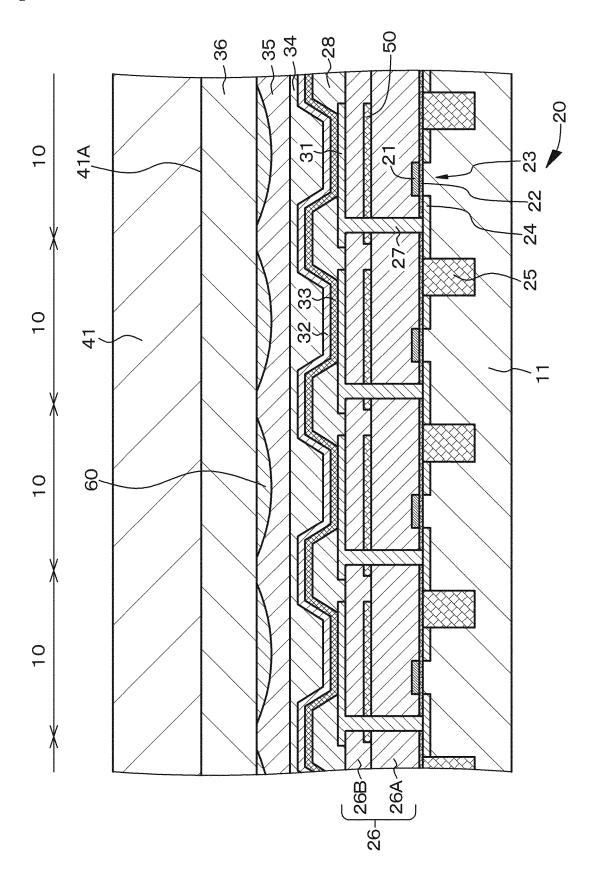


Fig. 16

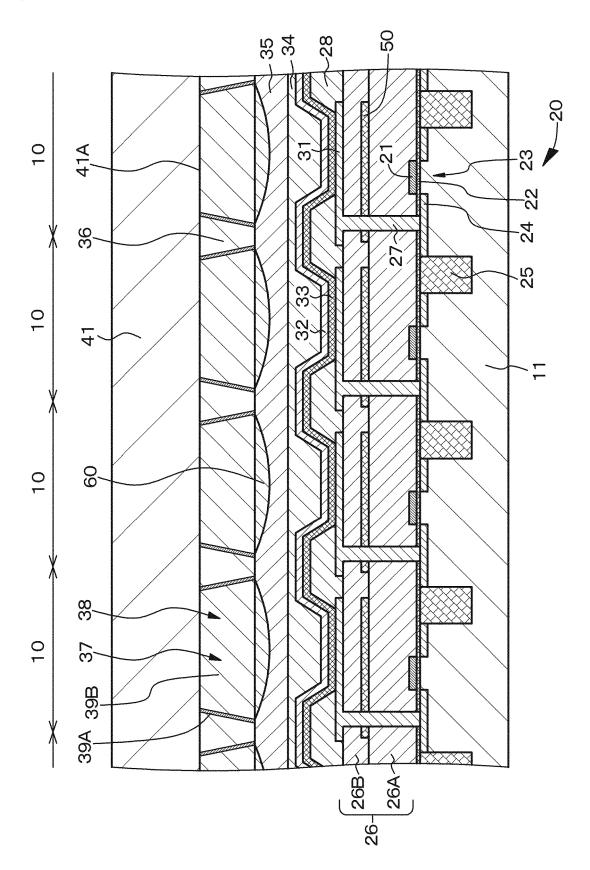


Fig. 17

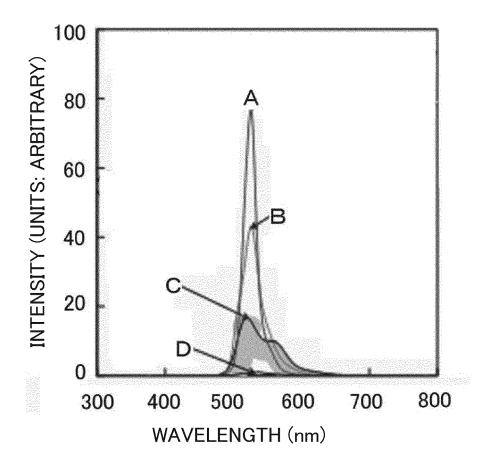


Fig. 18A

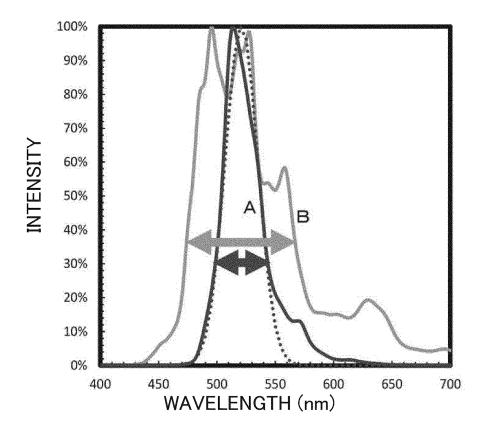


Fig. 18B

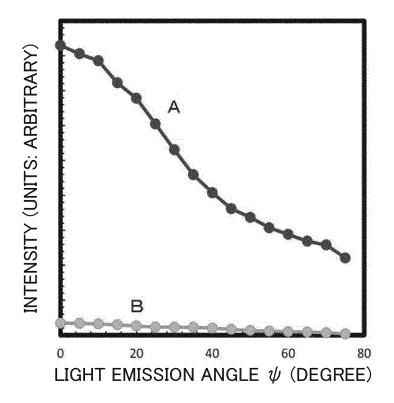


Fig. 19A

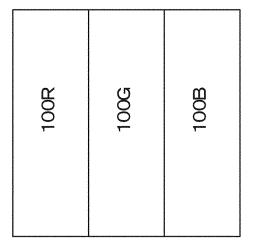
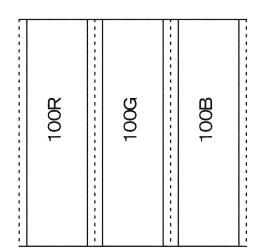


Fig. 19B



#### INTERNATIONAL SEARCH REPORT International application No. PCT/JP2019/043911 A. CLASSIFICATION OF SUBJECT MATTER 5 Int.Cl. G03B21/00(2006.01)i, G03B21/14(2006.01)i, H01L51/50(2006.01)i, H05B33/24(2006.01)i, H05B33/28(2006.01)i, G02B5/22(2006.01)i, G09F9/30(2006.01)i, H01L27/32(2006.01)i FI: H05B33/24, H05B33/14A, H01L27/32, H05B33/28, G02B5/22, G03B21/00D, G03B21/14Z, G09F9/30349D, G09F9/30349Z, G09F9/30365 10 According to International Patent Classification (IPC) or to both national classification and IPC FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) Int.Cl. G03B21/00, G03B21/14, H01L51/50, H05B33/24, H05B33/28, G02B5/22, G09F9/30, H01L27/32 15 Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Published examined utility model applications of Japan 1922-1996 Published unexamined utility model applications of Japan 1971-2020 Registered utility model specifications of Japan 1996-2020 Published registered utility model applications of Japan 1994-2020 Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) 20 C. DOCUMENTS CONSIDERED TO BE RELEVANT Citation of document, with indication, where appropriate, of the relevant passages Relevant to claim No. Category\* JP 2007-265638 A (SANYO ELECTRIC CO., LTD.) 11.10.2007 1-4, 20X 25 (2007-10-11), paragraphs [0002], [0003], [0032], [0042] - [0055]JP 2007-103493 A (FUJIFILM CORPORATION) 19.04.2007 1-4, 20Χ 5, 13, 15-16, (2007-04-19), paragraphs [0431], [0443], Υ [0457] - [0480], fig. 1 18-19 30 JP 2018-081831 A (SEIKO EPSON CORPORATION) 24.05.2018 6-9, 12, 20 Χ (2018-05-24), paragraphs [0039]-[0048], fig. 4 5, 10, 14-16, Υ 18-19 JP 2011-076799 A (FUJIFILM CORPORATION) 14.04.2011 10 Υ 35 (2011-04-14), claim 1 JP 2002-184567 A (CANON INC.) 28.06.2002 (2002-06-28), 10 Υ claims 1-4 40 $\bowtie$ Further documents are listed in the continuation of Box C. See patent family annex. Special categories of cited documents: later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier application or patent but published on or after the international document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive filing date step when the document is taken alone 45 document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "L" document of particular relevance: the claimed invention cannot be considered to involve an inventive step when the document is "O" document referring to an oral disclosure, use, exhibition or other means combined with one or more other such documents, such combination being obvious to a person skilled in the art document published prior to the international filing date but later than the priority date claimed document member of the same patent family 50 Date of the actual completion of the international search Date of mailing of the international search report 23.01.2020 04.02.2020

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3-4-3, Kasumigaseki, Chiyoda-ku,

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Telephone No.

# EP 3 889 682 A1

# INTERNATIONAL SEARCH REPORT

International application No.
PCT/JP2019/043911

			PCT/JP20:	19/043911		
5	C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT					
	Category*	Citation of document, with indication, where appropriate, of the relev	Relevant to claim No.			
10	Y	JP 2004-062113 A (CANON INC.) 26.02.2004 (20 claims 1-5, paragraphs [0016]-[0019], fig	04-02-26), g. 2	13-16, 18-19		
10	A	JP 2012-230151 A (SEIKO EPSON CORPORATION) (2012-11-22), entire text, all drawings	22.11.2012	1-20		
15	A	JP 2004-133111 A (CANON INC.) 30.04.2004 (20 entire text, all drawings	04-04-30),	1-20		
20						
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Form PCT/ISA/210 (second sheet) (January 2015)

# EP 3 889 682 A1

	INTERNATIONAL SEARCH REPORT Information on patent family members			International application No. PCT/JP2019/043911
5	JP 2007-265638 A	11.10.2007	US 2007/0024168 A1 paragraphs [0004], [0207]-[0220] KR 10-2007-0015085 CN 1905205 A TW 200714131 A	[0110],
	JP 2007-103493 A JP 2018-081831 A		(Family: none) US 2018/0138376 A1	
15	01 2010 001001 11	21.00.2010	paragraphs [0063]-[ US 2019/0267517 A1	0085], fig. 4
	JP 2011-076799 A	14.04.2011	(Family: none)	
20	JP 2002-184567 A	28.06.2002	(Family: none)	
	JP 2004-062113 A	26.02.2004	US 2004/0021833 A1 claims 1-5, paragrafig. 2	phs [0070]-[0081],
25	JP 2012-230151 A	22.11.2012	(Family: none)	
	JP 2004-133111 A	30.04.2004	US 2004/0008326 A1 entire text, all dr	awings
30				
35				
40				
45				
50				
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### EP 3 889 682 A1

#### REFERENCES CITED IN THE DESCRIPTION

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#### Patent documents cited in the description

- WO 2001039554 A [0002] [0004]
- JP 2009049135 A **[0002] [0004]**

• JP 2015232599 A [0031]