LT8614 Supply Design Summary Report

Vin: 10.5V (min.), 13.8V (nom.), 18V (max.)

Output Rails : Vout1 = 4.96V / 2A (max.)

Project Name:

Project Date:

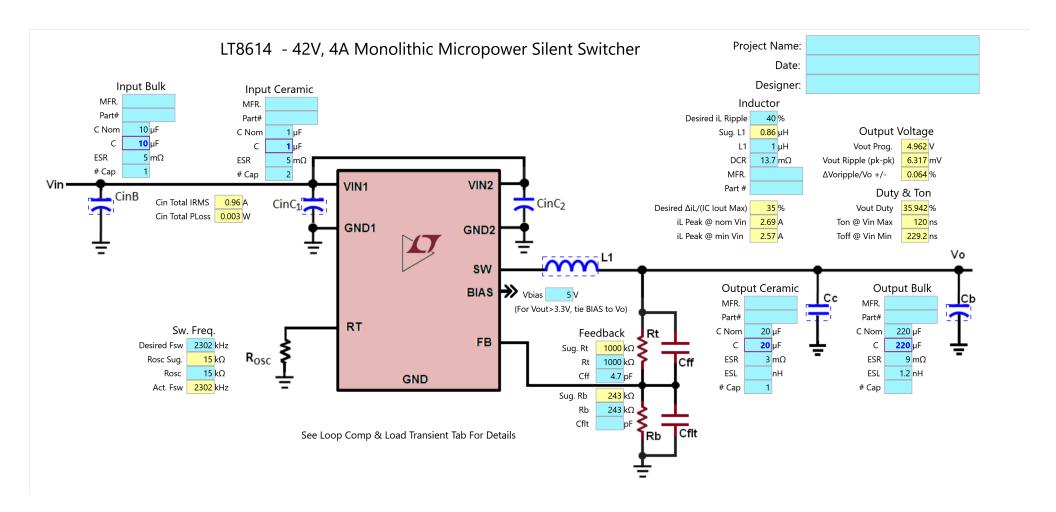
Designer:





LT8614 Solution - Simplified Schematic

Vin: 10.5V (min.), 13.8V (nom.), 18V (max.) Output Rails: Vout1 = 4.96V / 2A (max.)

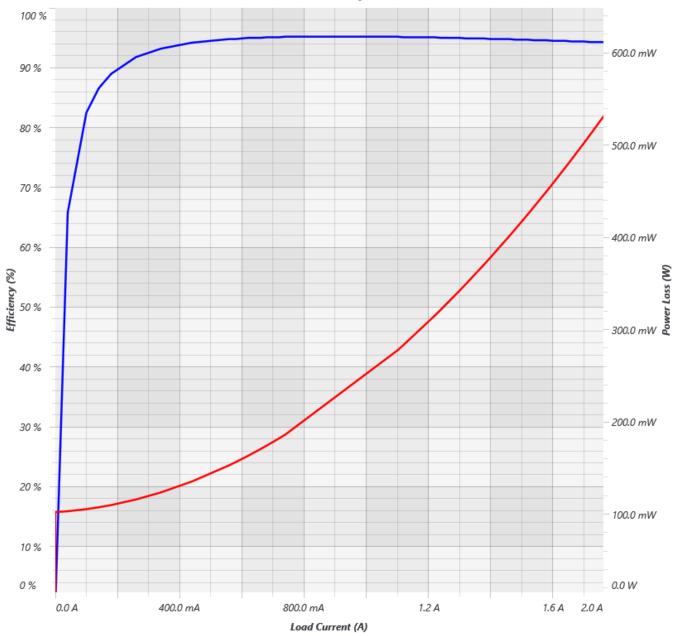


LT8614 Solution - Efficiency & Loss Estimations

Rail # 1 : Vin = 13.8V, Vout1 = 4.96V

* Estimations For CCM Mode Only. Inductor AC Losses Entered by User

Rail #1 (4.96V) Efficiency & Power Loss



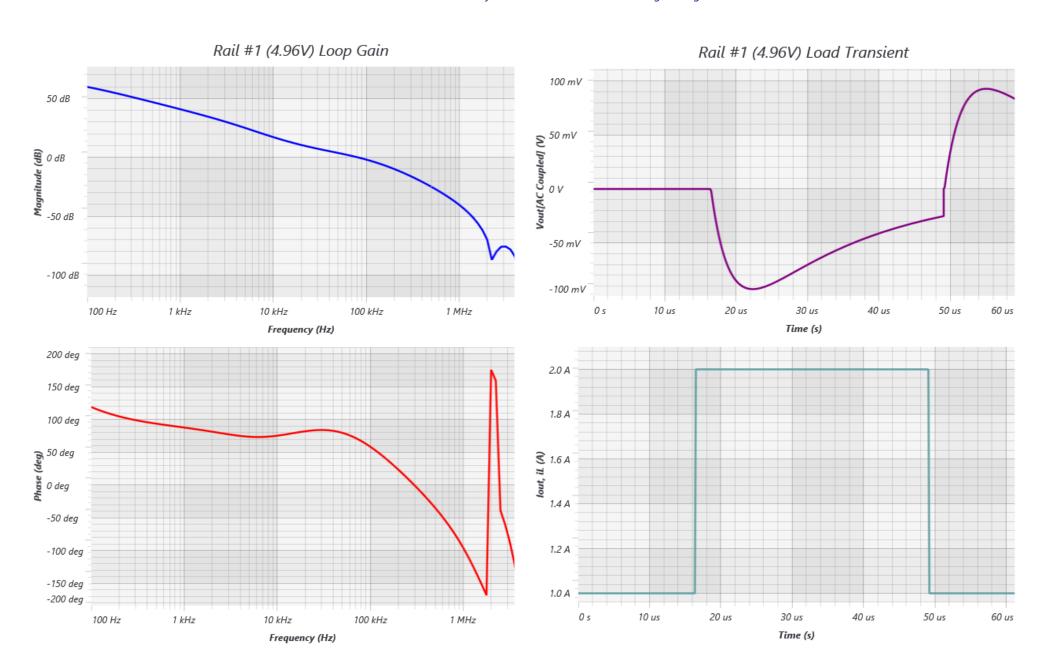
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LT8614 Solution - Loop Gain & Load Transient Estimations

Rail # 1 : Vin = 13.8V, Vout1 = 4.96V, Iout1 = 2A

* Estimations For CCM Mode Only. Estimations Based On Small Signal Avg. Model



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LT8614 Solution - Summary

LT8614 Supply Design Summary Project Info: Design Specifications Steady State: Rail # Vin Min. ΔVο iLpk Vin Nom Vin Max. Fsw Vo ΔVo% lo Max ΔiLp-p ΔiL% Duty Toff min. 10.5 V 18 V 2302 kHz 35.94 % 4.96 V 6.32 mV 0.1 % 2 A 1.38 A 35 % 2.69 A 120 ns 229 ns **Efficiency and Loop:** Loop BW Vo Iomax Eff.@lomax PLoss@lomax Loop PM Step Low Step High Step Slew ΔVo@Step ΔVo@Step % 4.96 V 2 A 93.86 % 0.649 W 79.43 kHz 67.43 deg 1 A 2 A 100 A/µs 92.78 mV +/-1.9 % **Recommendations and Warnings:** Rail #1 Capacitance value is not de-rated. Inadequate information provided to estimate de-rated value. Please check the capacitor's entries in library. Rail #1 Capacitance value is not de-rated. Inadequate information provided to estimate de-rated value. Please check the capacitor's entries in library. Rail #1 Capacitance value is not de-rated. Inadequate information provided to estimate de-rated value. Please check the capacitor's entries in library. Rail #1 Capacitance value is not de-rated. Inadequate information provided to estimate de-rated value. Please check the capacitor's entries in library. **Power Components** Export BOM **Power Components Bill Of Materials:** Ref. Des. Value Quantity Description Mfr. Name Mfr. Part # Pkg. (Imperial) L(mm) W(mm) H(mm) User Note U1 LINEAR TECH LT8614 0.75 Lo1 1µH IND 0 Cinb1 10μF CAP Cinc1 Cinc2 1μF CAP ~ O 220µF CAP Coc1 20µF CAP **Power Components Footprint:** 6 # Components Component Clearance (d Max. Height 0.75 mm

Part # 2

Width

* Notes :

1. The calculated power component area is only the simple sum of component footprint areas with given clearance, assuming all power components are on the same side of PCB. It is NOT the final PCB size with layout design.

Part #1

d/2

d

2. Component count should change with the number of paralleled phases.

1.5

24.8

0.038

mm mm^2

in^2

mm^2

in^2

Component Clearance (d)

* Power Components Area (Excludes ICs)

* Power Components Area (Includes ICs)

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