## Visualizing code structure in LLVM



**Dmitry Mikushin** 

dmitry.mikushin@usi.ch

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## Abstract

This presentation overviews LLVM's capabilities for creating human-readable code structure visualizations and related dependence analysis passes.

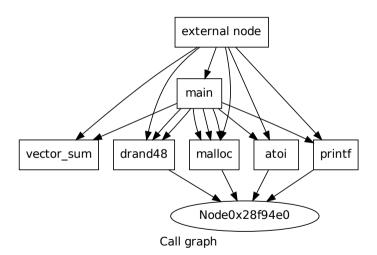
```
void vector_sum(int length, float* in1, float* in2, float* out)
   for (int i = 0: i < length: i++)</pre>
        out[i] = in1[i] + in2[i];
#include <malloc.h>
#include <stdio.h>
#include <stdlib b>
int main(int argc, char* argv[])
   if (argc != 2) return 1;
   int length = atoi(argv[1]);
   float* in1 = (float*)malloc(length * sizeof(float)):
   float* in2 = (float*)malloc(length * sizeof(float));
   float* out = (float*)malloc(length * sizeof(float));
   for (int i = 0: i < length: i++)</pre>
        in1[i] = drand48();
        in2[i] = drand48():
   vector sum(length, in1, in2, out):
   printf("%f\n", out[length - 1]);
   return 0:
```

## LLVM IR for the function of interest (vector sum)

```
$ clang -emit-llvm example.c -01 -S -o - | less
define void @vector sum(i32 %length, float* nocapture readonly %in1, float* nocapture readonly %in2, float* nocapture %out) #0 {
entry:
 %cmp9 = icmp sgt i32 %length, 0
 br i1 %cmp9, label %for.body, label %for.end
for.body:
                                                  ; preds = %entry, %for.body
  %indvars.iv = phi i64 [ %indvars.iv.next, %for.body ], [ 0, %entry ]
  %arrayidx = getelementptr inbounds float* %in1, i64 %indvars.iv
  %0 = load float* %arrayidx, align 4, !tbaa !1
  %arravidx2 = getelementptr inbounds float* %in2, i64 %indvars.iv
  %1 = load float* %arravidx2, align 4, !tbaa !1
  %add = fadd float %0. %1
  %arravidx4 = getelementptr inbounds float* %out, i64 %indvars.iv
  store float %add, float* %arravidx4, align 4, !tbaa !1
  %indvars.iv.next = add nuw nsw i64 %indvars.iv. 1
  %Iftr widely = trunc i64 %indvars iv next to i32
  %exitcond = icmp eq i32 %lftr.wideiv. %length
  br i1 %exitcond, label %for.end, label %for.body
for end:
                                                  : preds = %for.body, %entry
  ret void
```

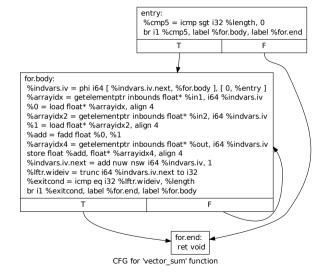
Two forms of graphs available in LLVM right from the command line:

- 1 Call graph
- 2 CFG (basic blocks)
- 3 CFG & DFG (instructions)



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Black edges - dataflow, red edges - control flow

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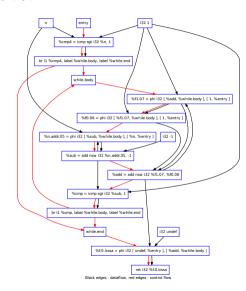
#### Spells:

- 1 Call graph built-in LLVM pass + dot
  - \$ clang -emit-llvm -S -c example.c -o | opt -01 | opt -dot-callgraph -o /dev/null && dot -Tpdf callgraph.dot -o callgraph.pdf
- 2 CFG (basic blocks) built-in LLVM pass + dot
  - $\$\ clang\ -emit-llvm\ -S\ -c\ example.c\ -o\ -\ |\ opt\ -dot\ -cfg\ -o\ /dev/null\ \&\&\ dot\ -Tpdf\ cfg.vector\_sum.dot\ -o\ cfg.vector\_sum.pdf\ -o\ (fg.vector\_sum.dot\ -o\ ($
- 3 CFG & DFG (instructions) llvmpy script by Paul Sokolovsky + dot https://github.com/pfalcon/graph-llvm-ir
  - \$ ./graph-llvm-ir ./example.ll && dot -Tpdf vector sum.dot -o vector sum.pdf

## CFG & DFG of Zeller's Ex. 7.1

```
/* fibo.c -- Fibonacci C program to be debugged */
#include <stdio.h>
int fib(int n)
   int f. f0 = 1. f1 = 1:
   while (n > 1)
       n = n - 1;
       f = f0 + f1:
       f0 = f1;
       f1 = f:
   return f:
int main()
   int n = 9:
   while (n > 0)
       printf("fib(%d)=%dN", n, fib(n));
       n = n - 1:
   return 0:
```

Note uninitialized int. f becomes undef  $\rightarrow$ 



## Dependence Analysis passes

Two LLVM passes focused on dependencies:

- Memory Dependence Analysis
- 2 Dependence Analysis

Both leverage Alias Analysis to reduce  $O(n^2)$  strength.

# Memory Dependence Analysis

- Fully functional
- Analyses dependencies between memory references:
  - Clobber instruction that clobbers the memory, e.g. a may-aliased store
  - **Def** instruction defines/produces the desired memory location
  - NonLocal outside of the current basic block (need to check predecessors)
  - NonFuncLocal outside of the current function
- No visual graph output
- Printed output is not very clear (see next slide)

# Memory Dependence Analysis - Test

```
define void @vector_sum(i32 %length, float* nocapture readonly ←
      %in1, float* nocapture readonly %in2, float* nocapture ←
      %out) #0 {
entry:
 %cmp9 = icmp sgt i32 %length, 0
 br i1 %cmp9, label %for.body, label %for.end
for.body:
                             ; preds = %entry, %for.body
  %indvars.iv = phi i64 [ %indvars.iv.next, %for.body ], [ 0, ←
        %entry ]
  %arravidx = getelementptr inbounds float* %in1, i64 ←
        %indvars iv
  %0 = load float* %arravidx, align 4, !tbaa !1
  %arravidx2 = getelementptr inbounds float* %in2, i64 ←
        %indvars iv
  %1 = load float* %arravidx2, align 4, !tbaa !1
  %add = fadd float %0. %1
  %arravidx4 = getelementptr inbounds float* %out, i64 ←
        %indvars.iv
  store float %add, float* %arravidx4, align 4, !tbaa !1
  %indvars.iv.next = add nuw nsw i64 %indvars.iv. 1
  %Iftr.wideiv = trunc i64 %indvars.iv.next to i32
  %exitcond = icmp eq i32 %lftr.wideiv, %length
  br i1 %exitcond, label %for.end, label %for.body
for end:
                             ; preds = %for.body, %entry
 ret woid
```

#### Memory Dependency Analysis printed output:

```
$ clang -emit-llvm example.c -01 -S -o - | opt -memdep -print-
    memdeps -o /dev/null
Function vector_sum memory dependencies:

Unknown in block %for.body
Unknown in block %entry
%0 = load float* %arrayidx, align 4, !tbaa !1

Unknown in block %for.body
Unknown in block %for.body
Unknown in block %entry
%1 = load float* %arrayidx2, align 4, !tbaa !1

Def from: %1 = load float* %arrayidx2, align 4, !tbaa !1

store float %add, float* %arrayidx4, align 4, !tbaa !1
```

Why store depends only on one load out of two?..

## Dependence Analysis

- Work in progress
- Implemented by Preston Briggs following "Practical Dependence Testing" by Goff, Kennedy, Tseng - PLDI 1991
- Comprehensive dependence analysis:
  - Output write after write
  - Flow (true) read after write
  - Anti write after read
  - ...
- No visual graph output

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Conclusion

#### So far:

- General dependence analysis is work in progress in LLVM
- No graph visualizations based on dependence analysis are implemented