

# Overview of quantum error correction

with a view on machine learning opportunities

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A MESSAGE FROM OUR CEO

- Noise severely
  - Quantum error
  - Factoring, c
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- ## Our progress toward quantum error correction

ions

Feb 22, 2023 · 3 min read



**Sundar Pichai**  
CEO of Google and Alphabet

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## **PsiQuantum to Build World's First Utility-Scale, Fault-Tolerant Quantum Computer in Australia**

- Facto ***The Australian and Queensland Governments Will Invest \$940M AUD (\$620M USD) into PsiQuantum***

April 29, 2024 04:00 PM Eastern Daylight Time

- But s
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- BRISBANE, Australia--(BUSINESS WIRE)--PsiQuantum today announced it will build the world's first utility-scale quantum computer at a strategically located site near Brisbane Airport in Brisbane, Australia. The Australian Commonwealth and Queensland Governments will invest \$940M AUD (\$620M USD) into PsiQuantum through a financial package, comprised of equity, grants, and loans. PsiQuantum is on an aggressive plan to have the site operational by the end of 2027. A fault-tolerant quantum computer will be able to solve commercially useful problems across industries built upon chemistry, math, and physics; thereby transforming critical industries – including renewable energy, minerals and metals, healthcare and transportation – that will propel the global economy for decades to come.

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- Surprising connections to other fields in computer science & physics
  - Topological phases of matter, black holes, quantum PCP conjecture, ...

# Talk outline

- Quantum error correction 101
- The decoding problem
- Finding fault-tolerant logical gates

# Quantum error correction 101

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- Pauli matrices  $X = \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix}$ ,  $Y = \begin{bmatrix} 0 & -i \\ i & 0 \end{bmatrix}$ , and  $Z = \begin{bmatrix} 1 & 0 \\ 0 & -1 \end{bmatrix}$

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  - Bit-flip  $|0\rangle \xrightarrow{X} |1\rangle$ , phase-flip  $|0\rangle + |1\rangle \xrightarrow{Z} |0\rangle - |1\rangle$

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- Quantum error-correcting code: subspace  $\mathcal{C} \subseteq \mathcal{H}$

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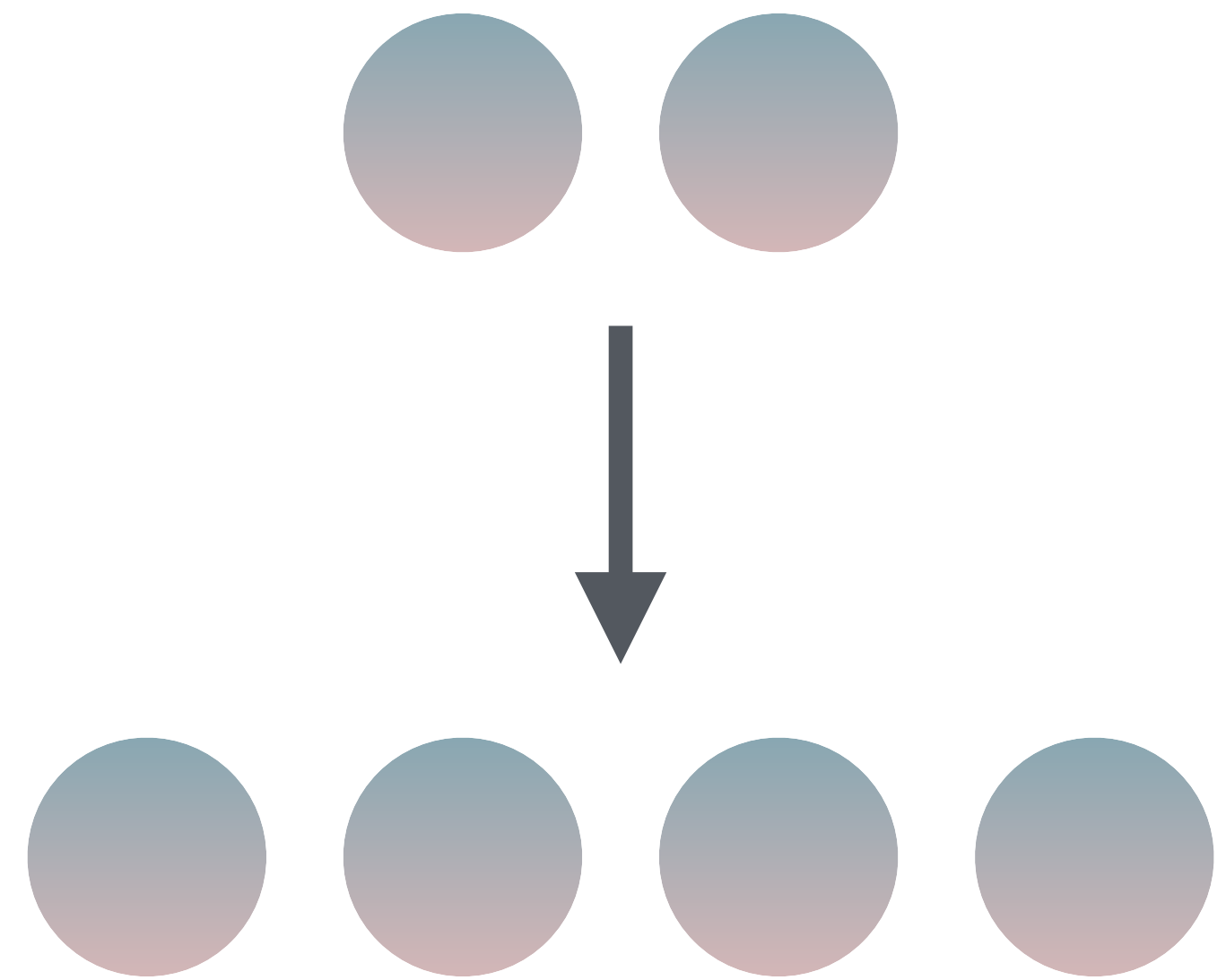
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- Code parameters  $[[n, k, d]]$

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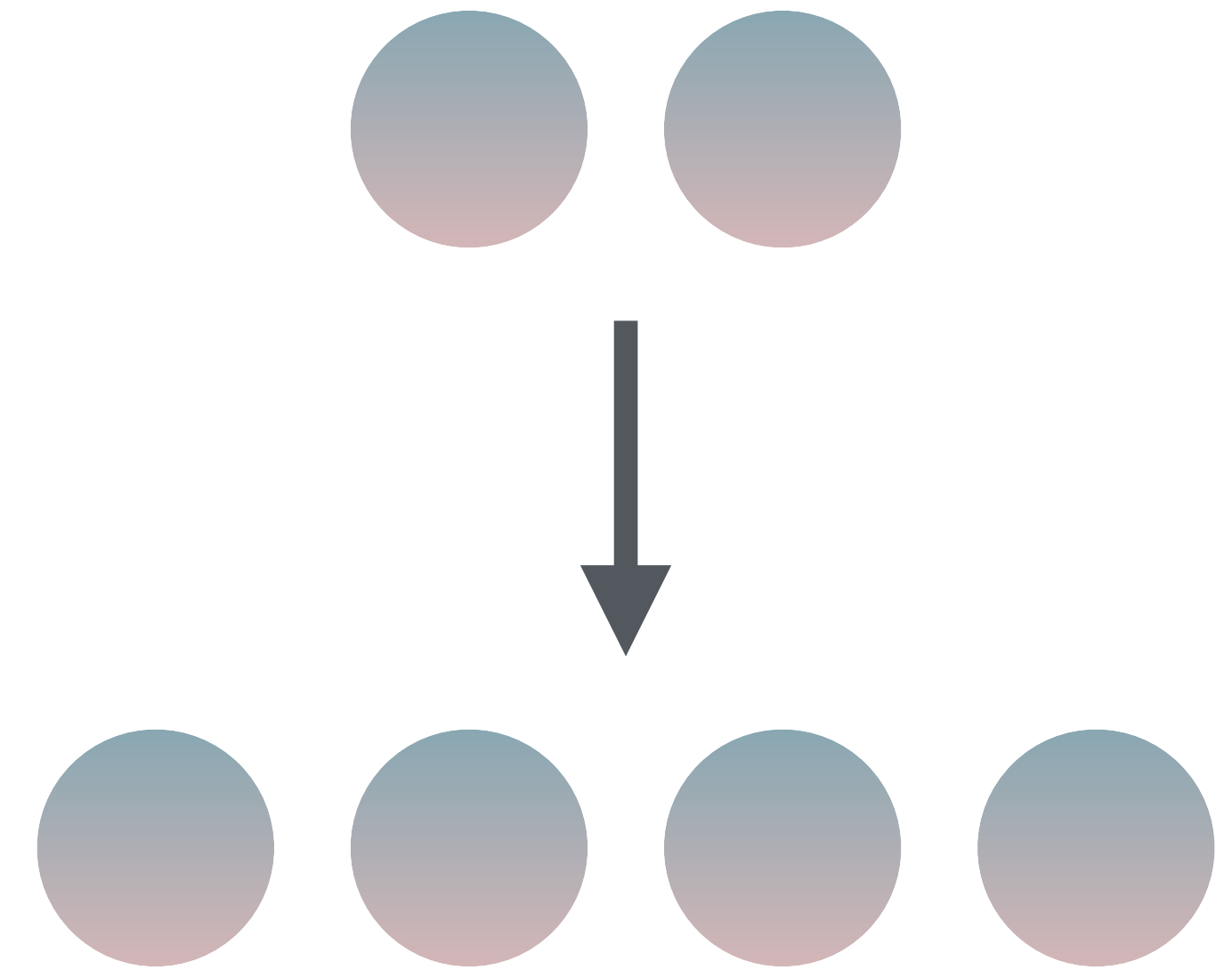
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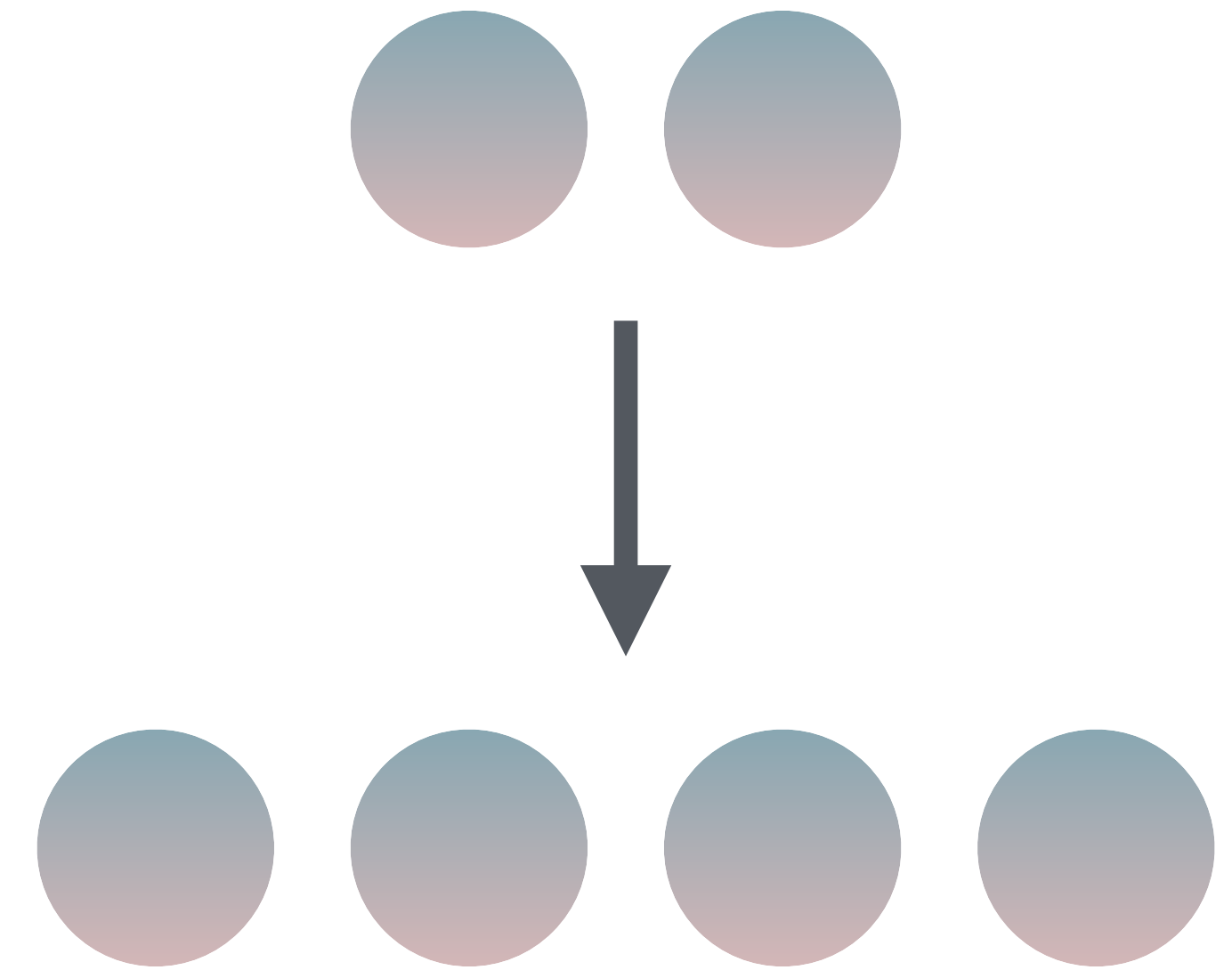
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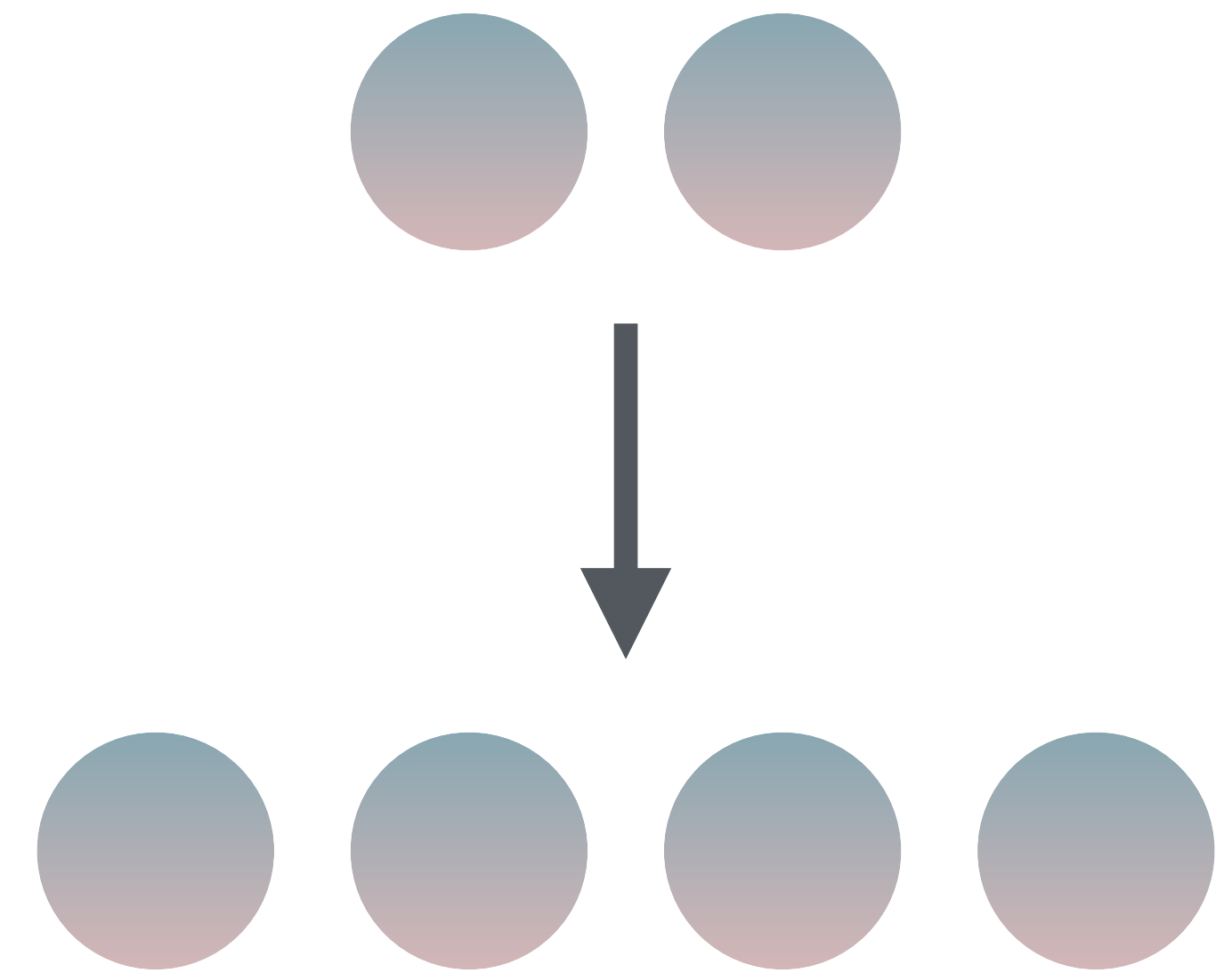
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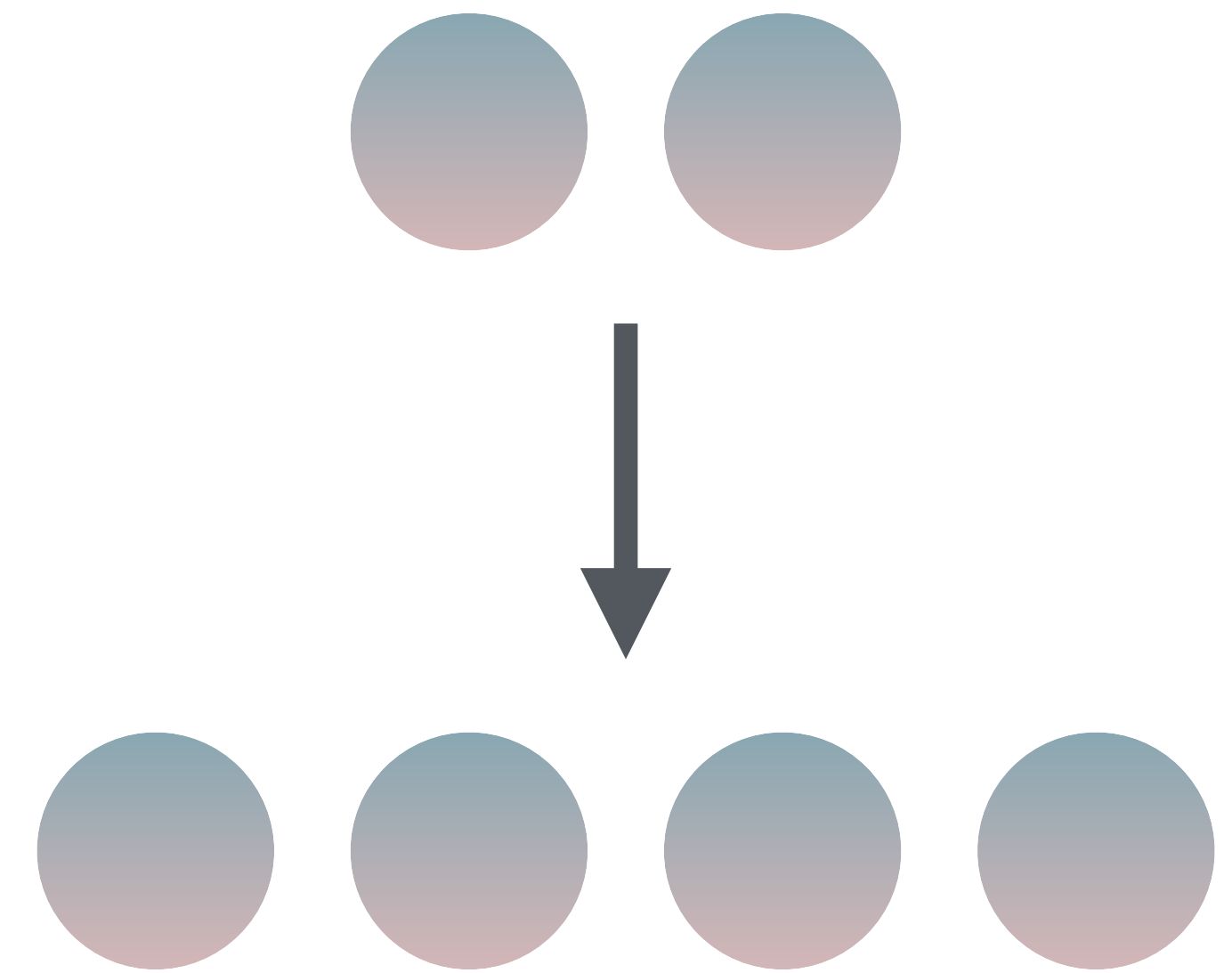




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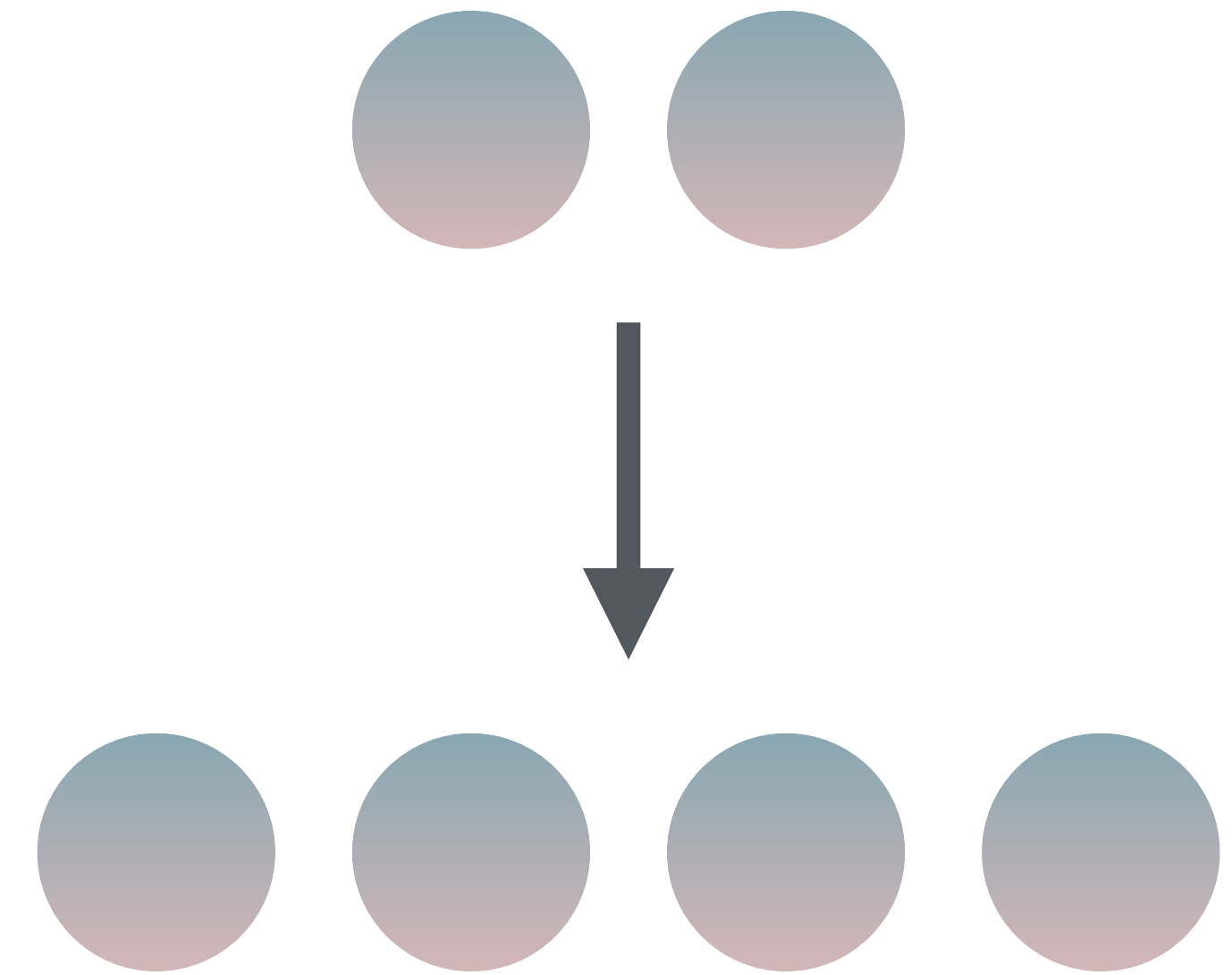
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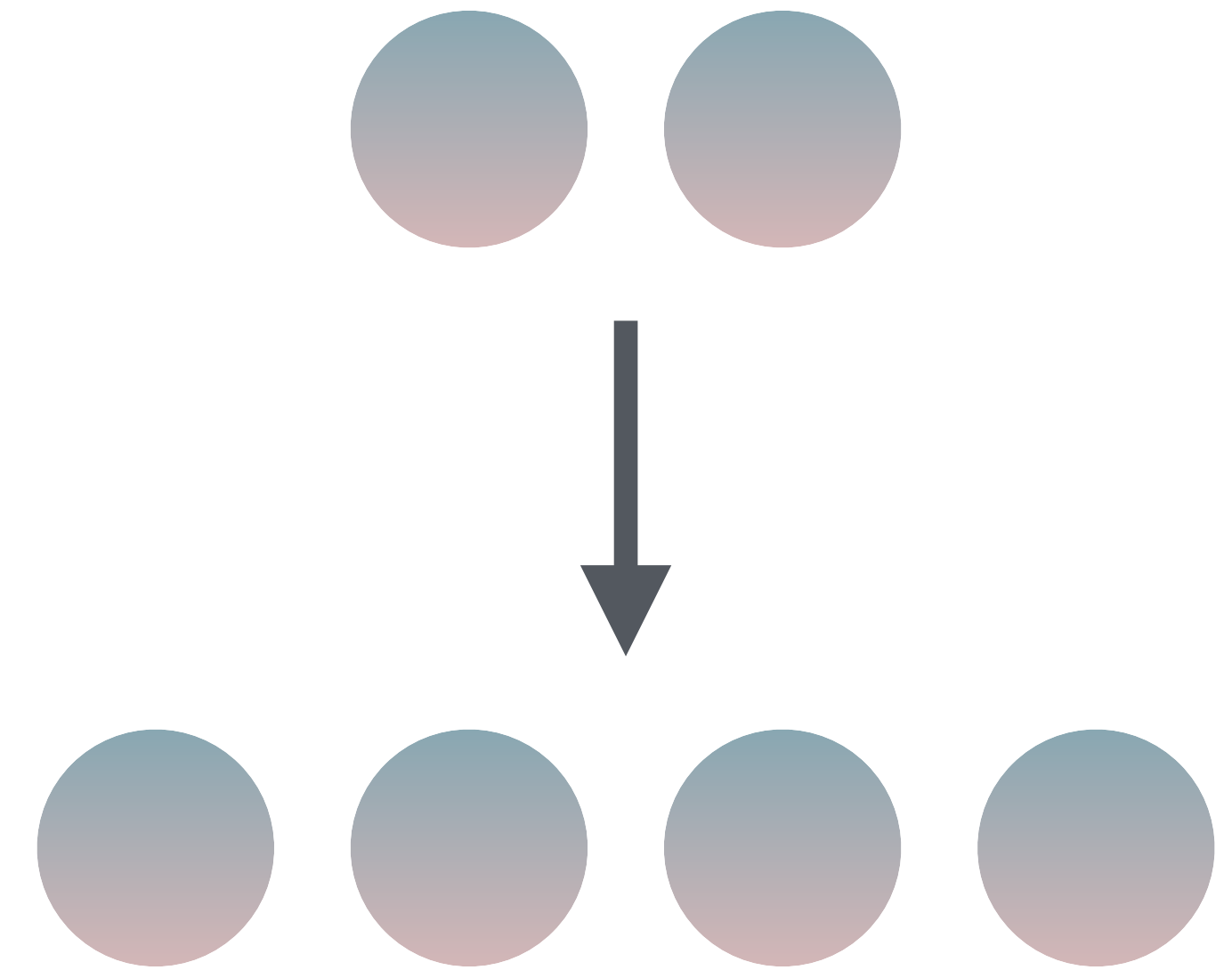
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- $|\bar{00}\rangle = \frac{1}{\sqrt{2}} (|0000\rangle + |1111\rangle), \quad |\bar{10}\rangle = \frac{1}{\sqrt{2}} (|1100\rangle + |0011\rangle), \quad \dots$



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- Clifford + one non-Clifford = **universal** quantum computation

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  - $\bar{H}_1\bar{H}_2\overline{\text{SWAP}}$



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- **Depolarising** channel  $\mathcal{D}(\rho) = (1 - p)\rho + \frac{p}{3} (X\rho X^\dagger + Y\rho Y^\dagger + Z\rho Z^\dagger)$

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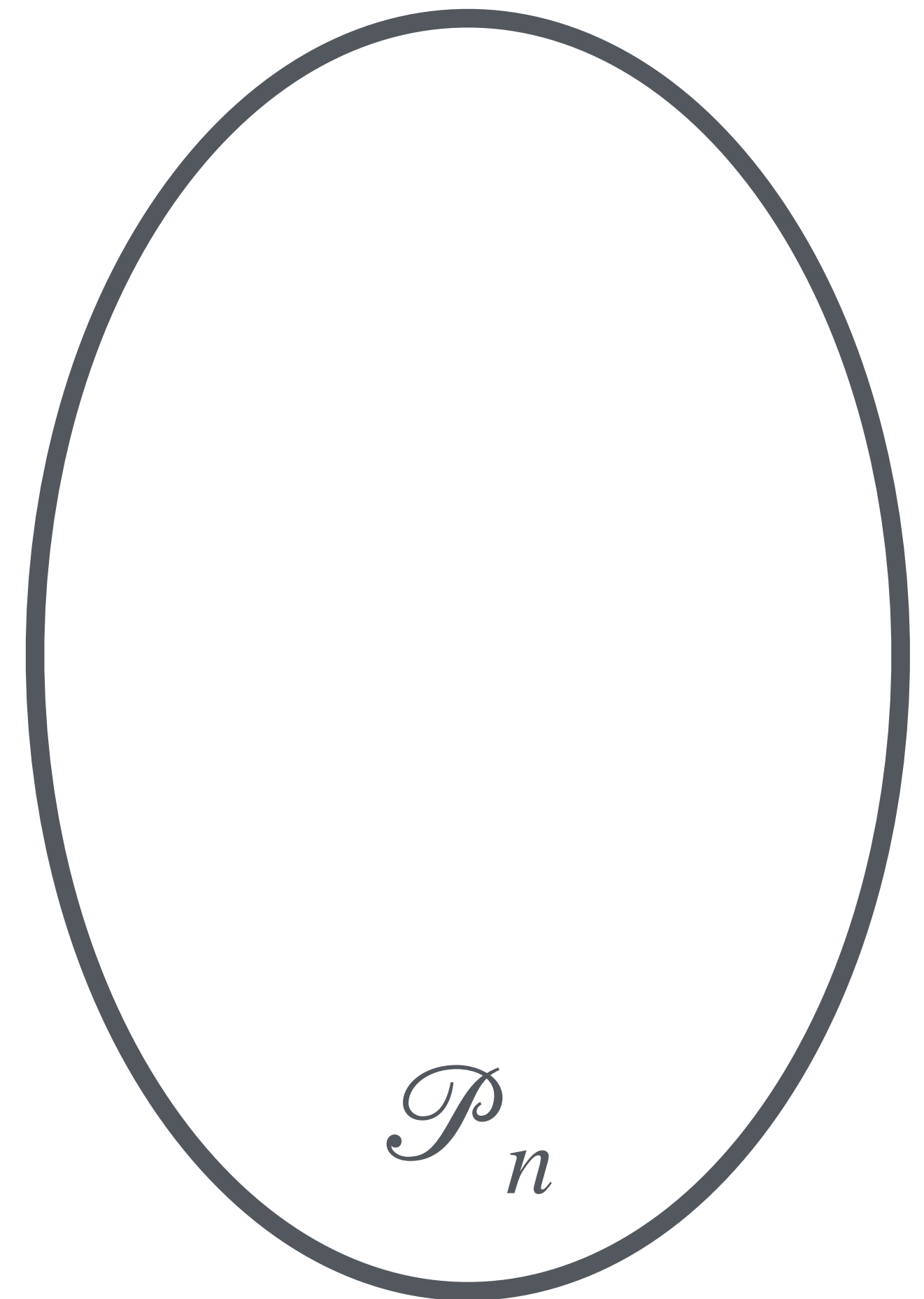
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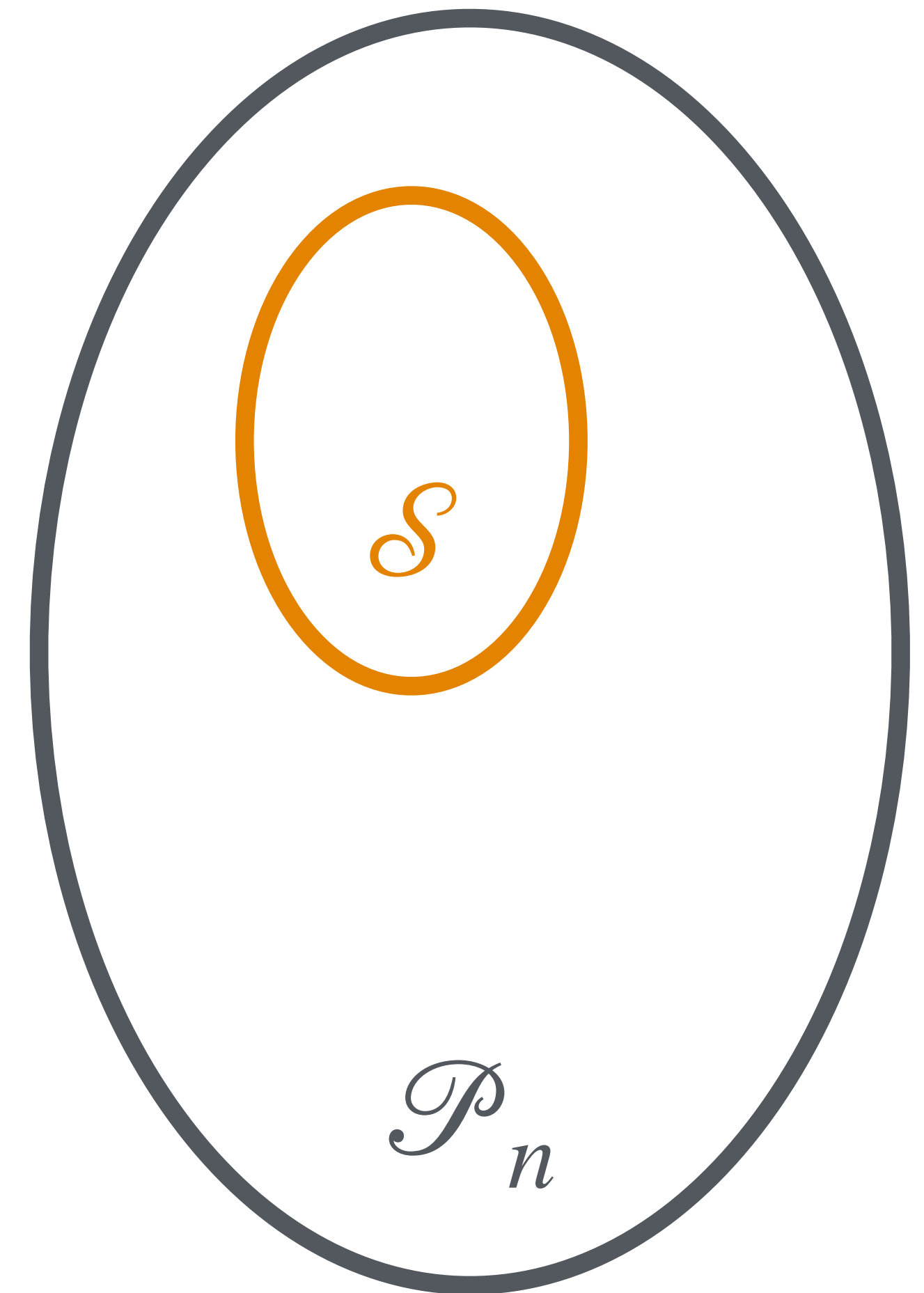
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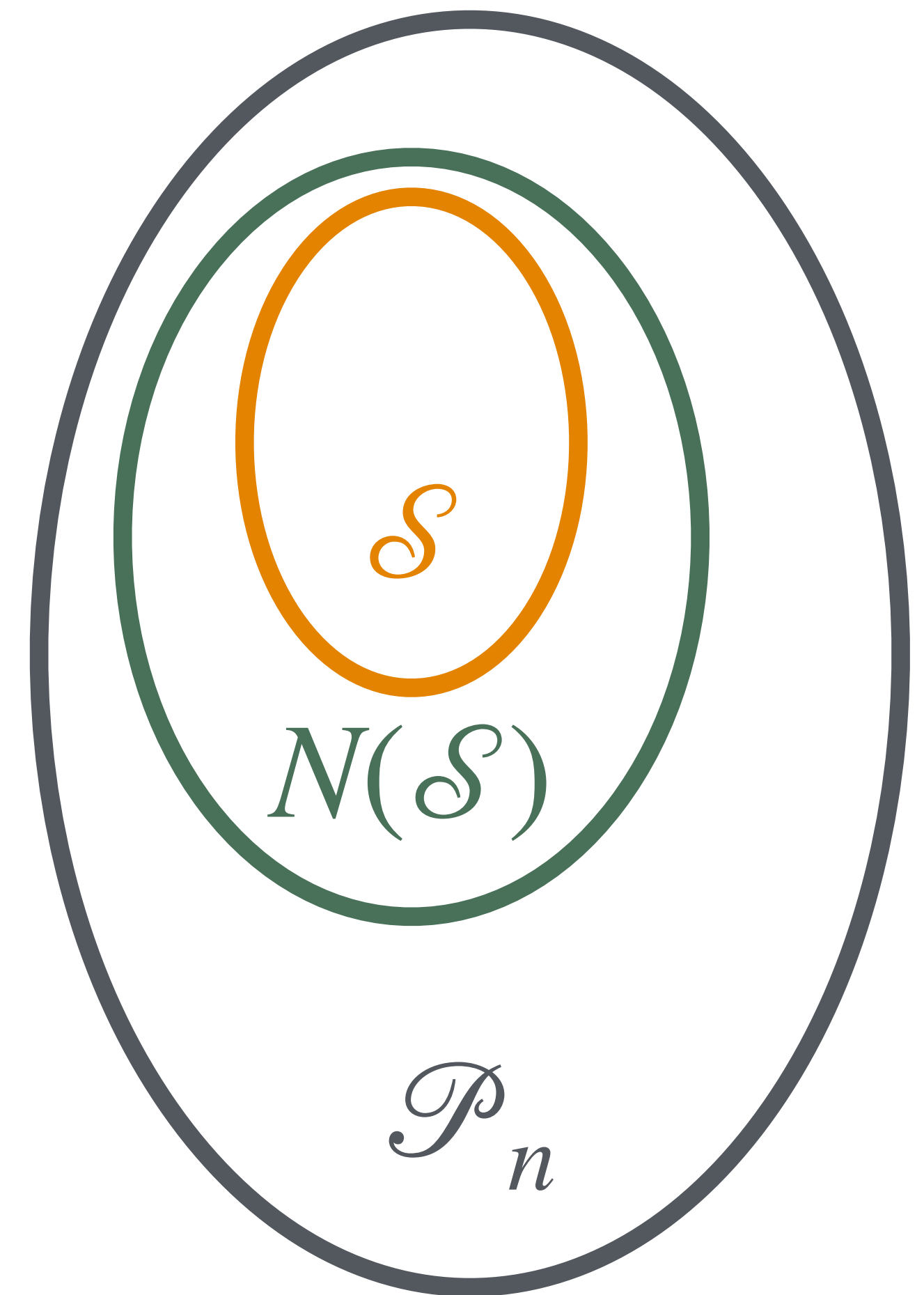
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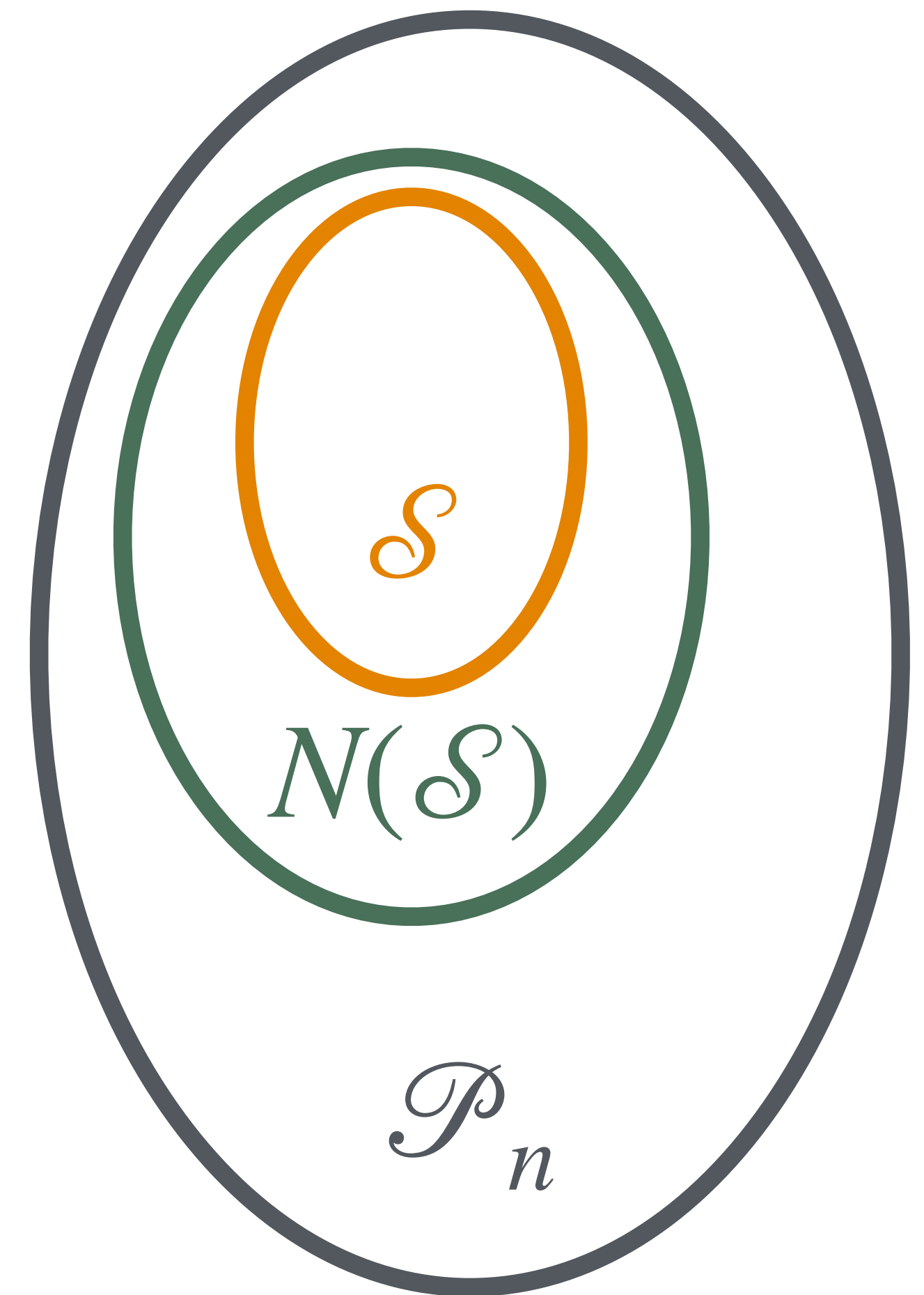
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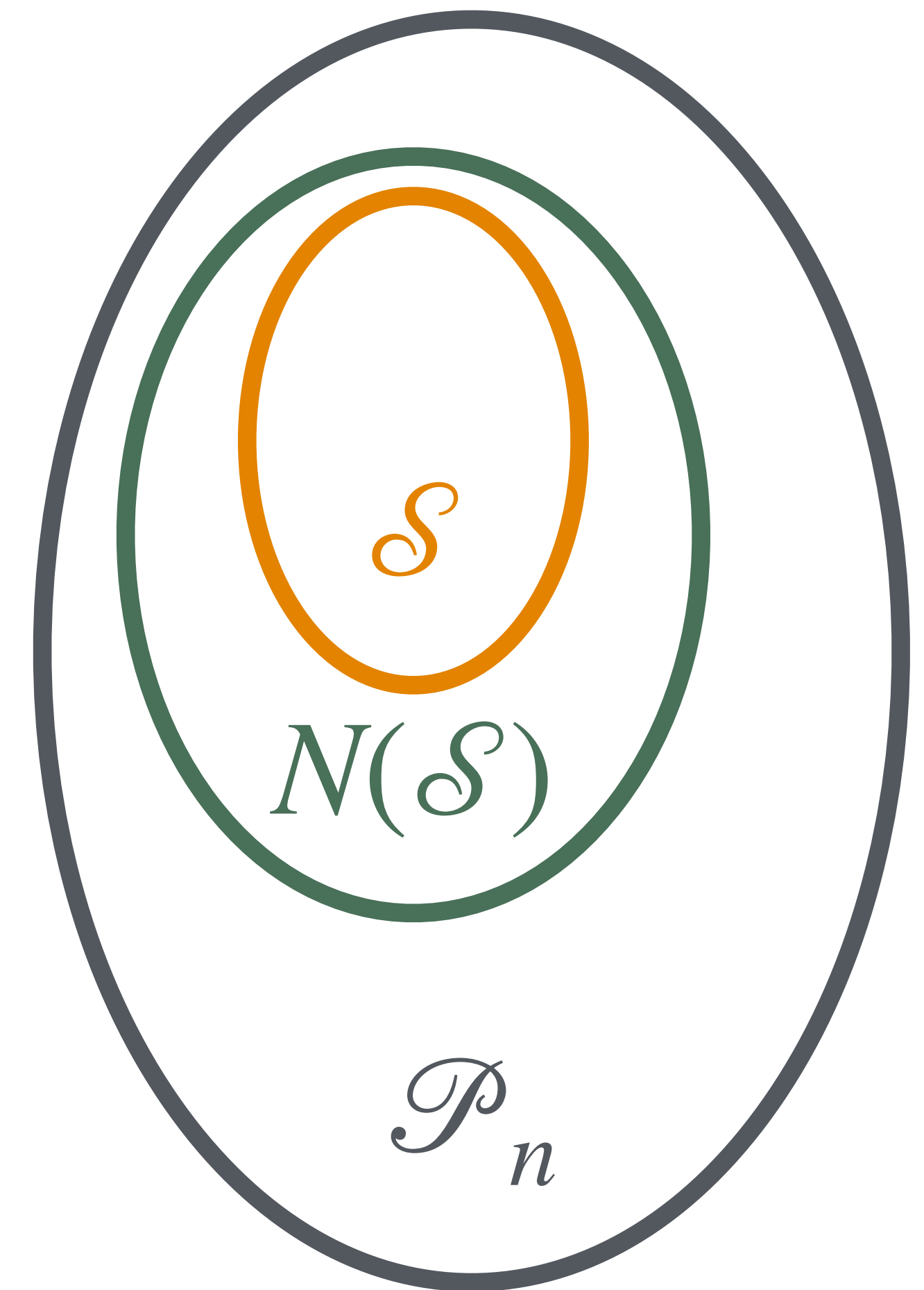
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- $S(E | \bar{\psi}) = -ES | \bar{\psi} \rangle = -E | \bar{\psi} \rangle$



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  - $-1$  outcomes give us **information** about the error

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  - **Output:** recovery operator  $R \in \mathcal{P}_n$  such that  $\sigma(R) = \sigma(E)$

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  - **Input:** error syndrome  $\sigma(E) \in \{-1, 1\}^m$
  - **Output:** recovery operator  $R \in \mathcal{P}_n$  such that  $\sigma(R) = \sigma(E)$
- **Success** if  $RE \in \mathcal{S}$

# The decoding problem

Example 3: Toric code decoding [Kit97] [DKL+02]

- See the hands-on session later today!
- <https://github.com/MikeVasmer/future-horizons-qec-hands-on>



# The decoding problem

Machine learning solutions

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## Machine learning solutions

- [TM17] Neural network decoder for topological codes

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- ... and many more!

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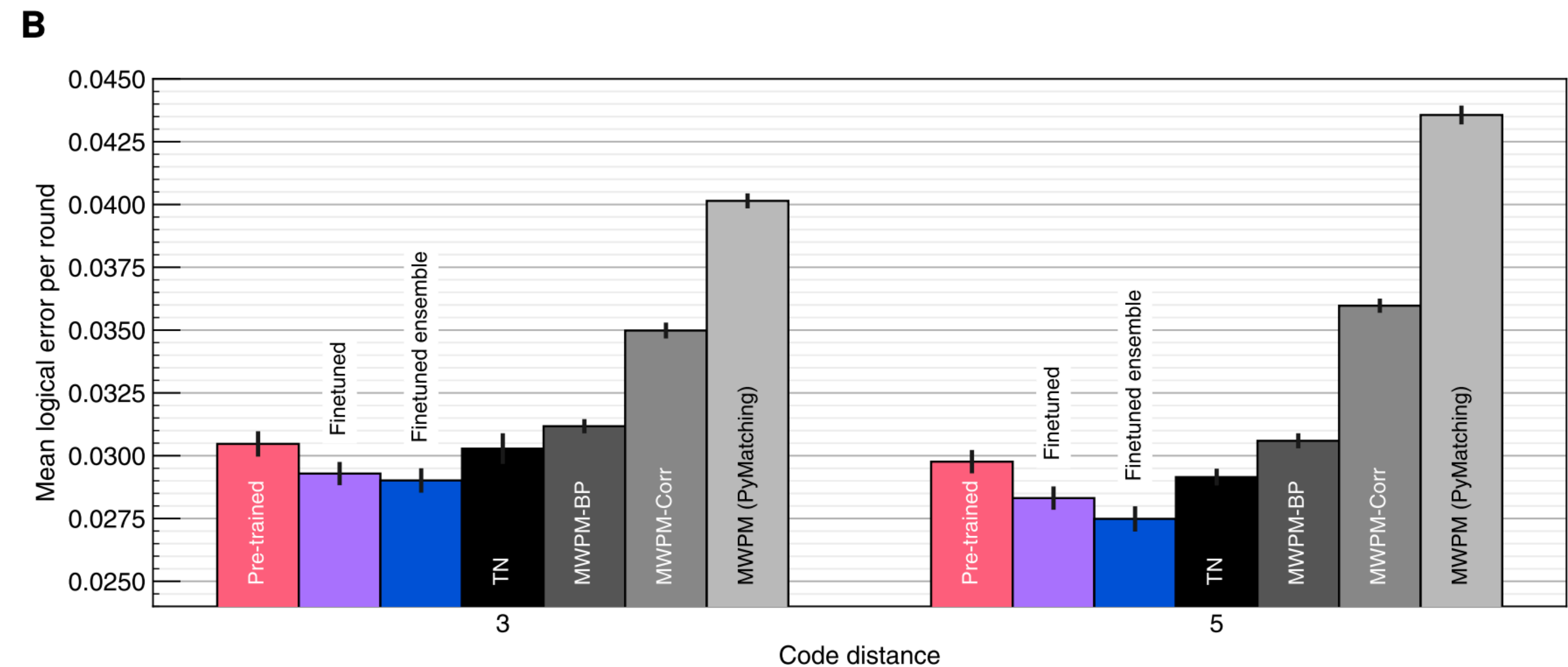


Figure from [BSH+23]

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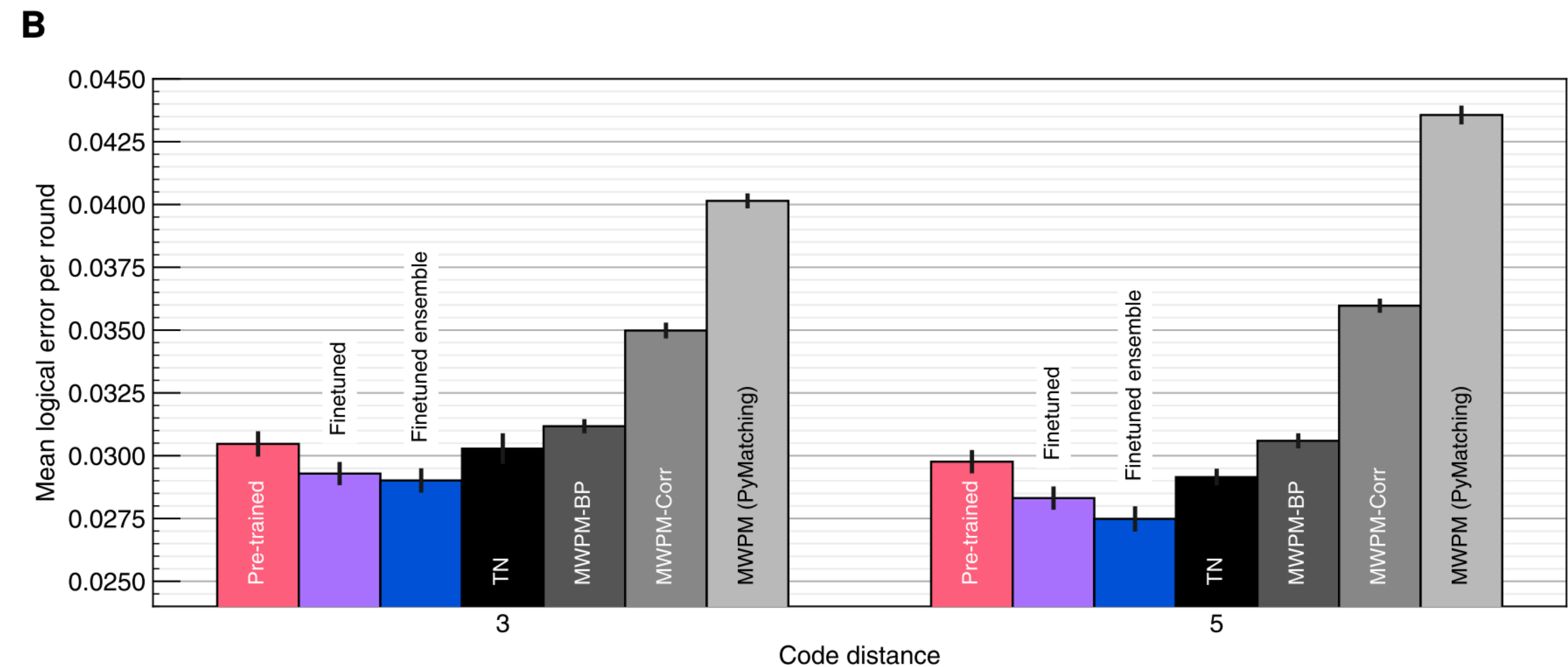


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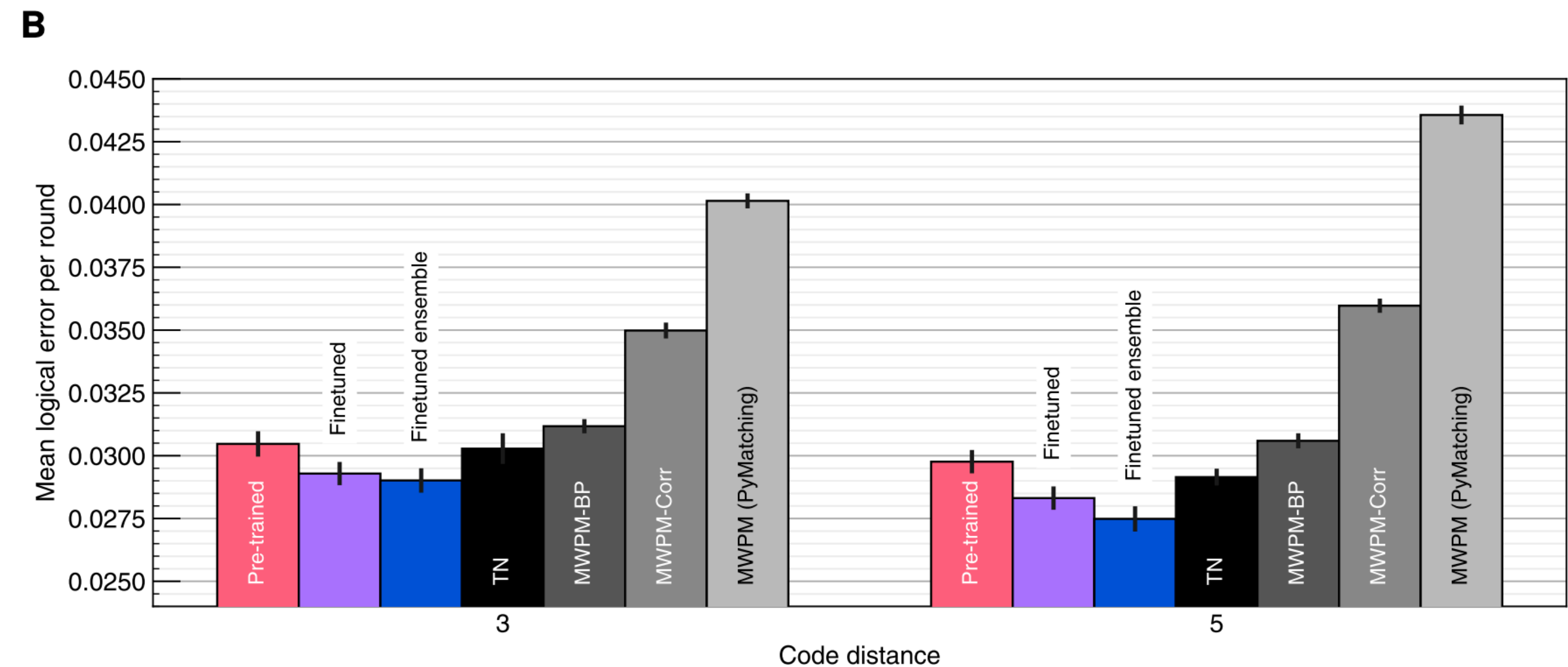


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# The decoding problem

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- **Near-term** (memory experiments)
  - All decoding done offline
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- **Long-term** (quantum computation)
  - Real-time decoding
  - **Speed** most important ( $\mu\text{s}$  timescales)

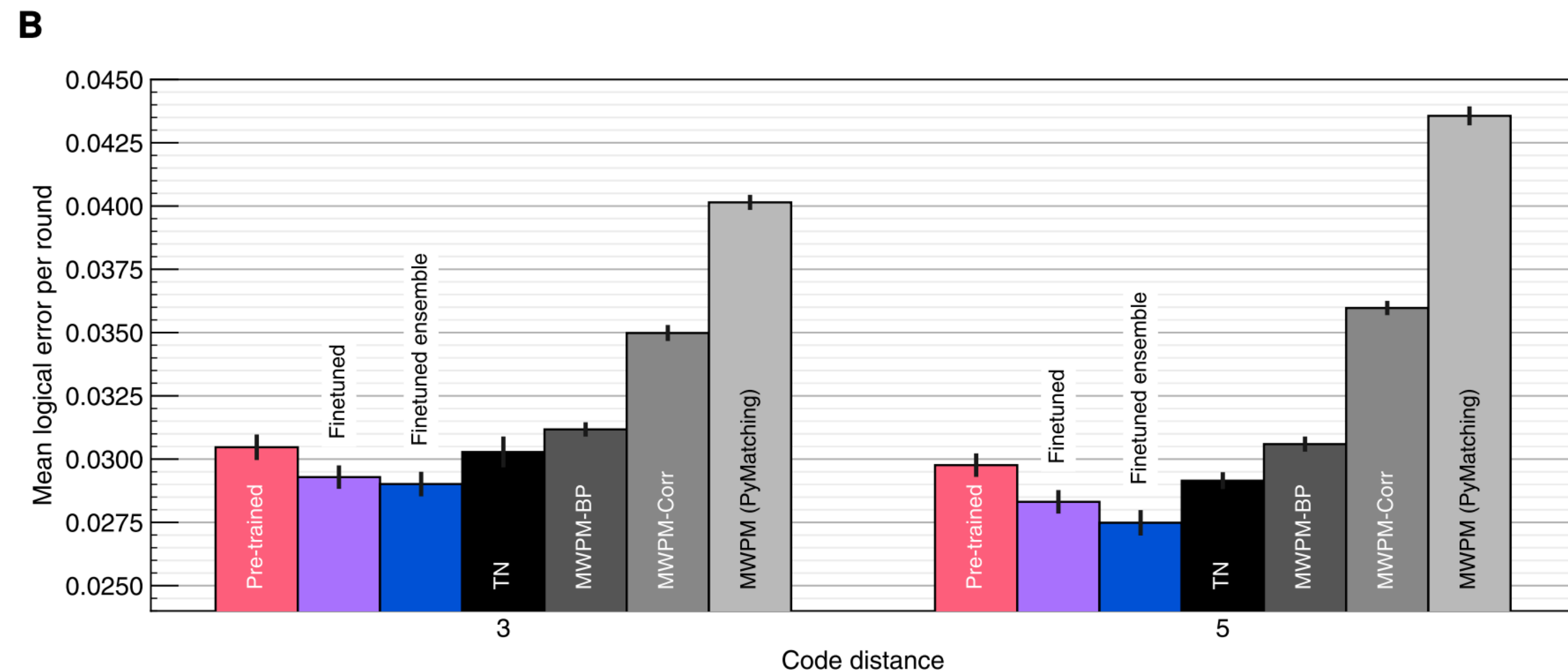


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# Finding fault-tolerant logical gates

# Fault-tolerant logical gates

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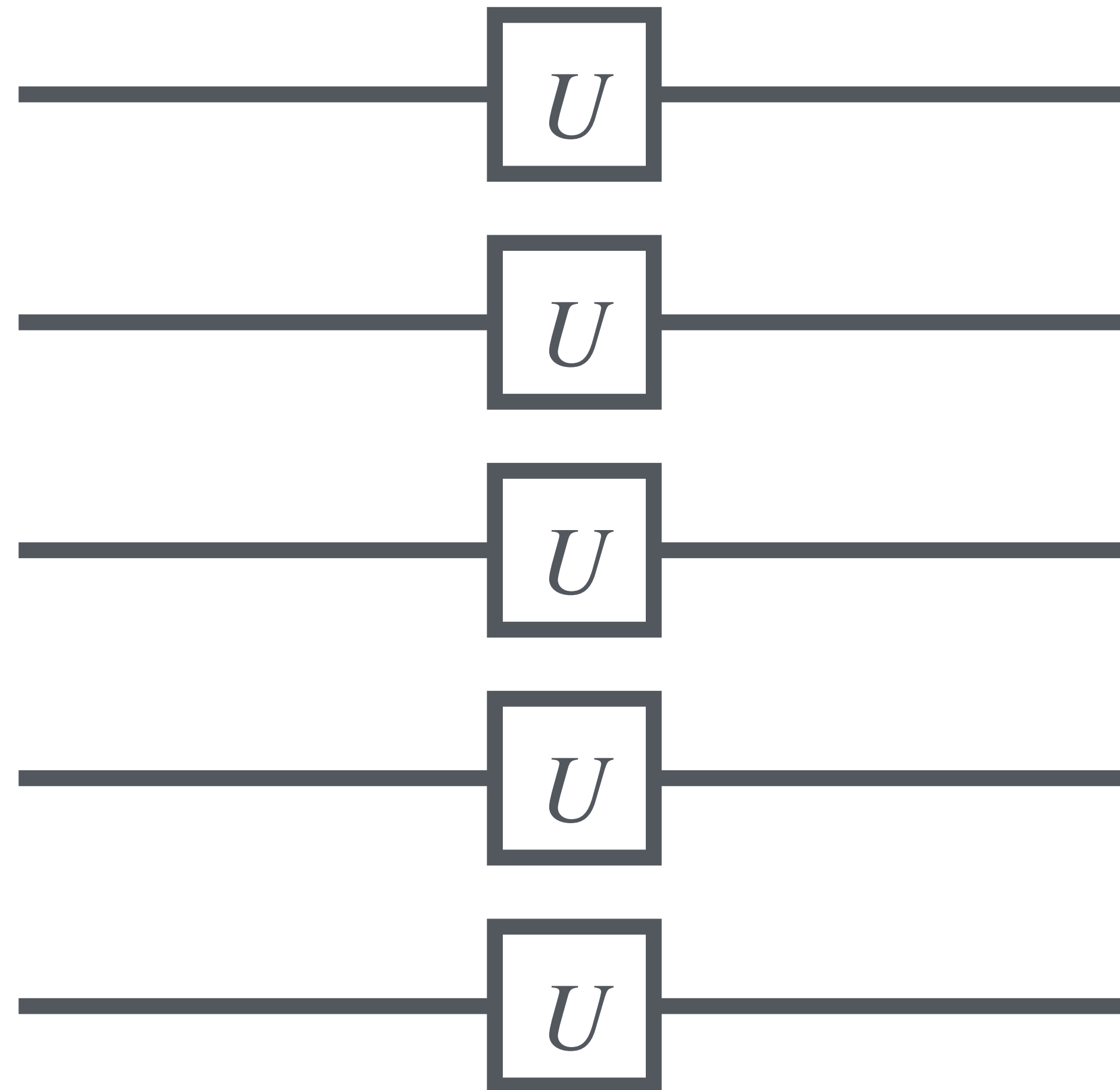
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- A fault-tolerant logical gate should
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  - Be **robust** to  $t$  errors occurring **during** the gate

# Fault-tolerant logical gates

## Example 4: Transversal gates

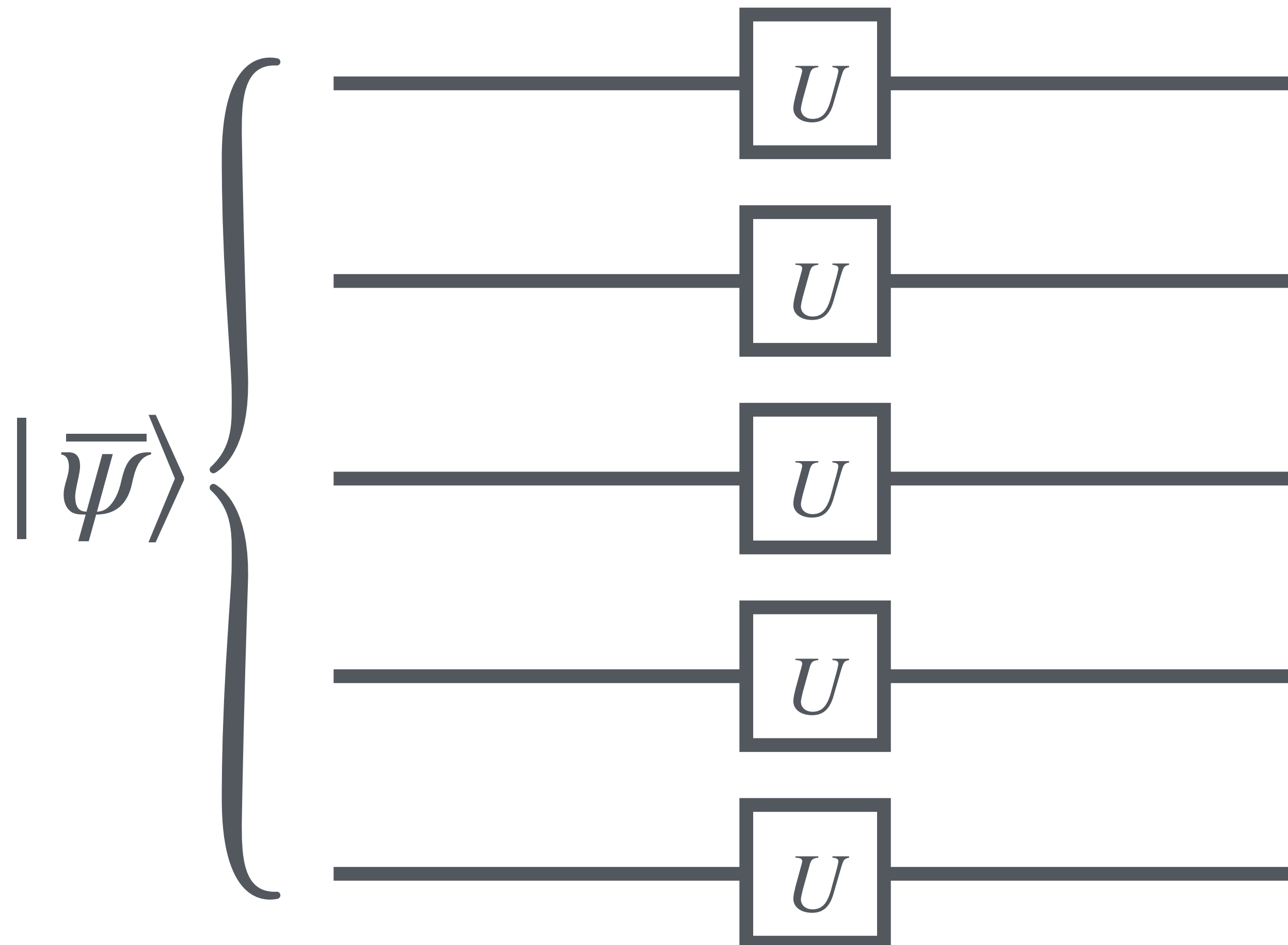
# Fault-tolerant logical gates

## Example 4: Transversal gates



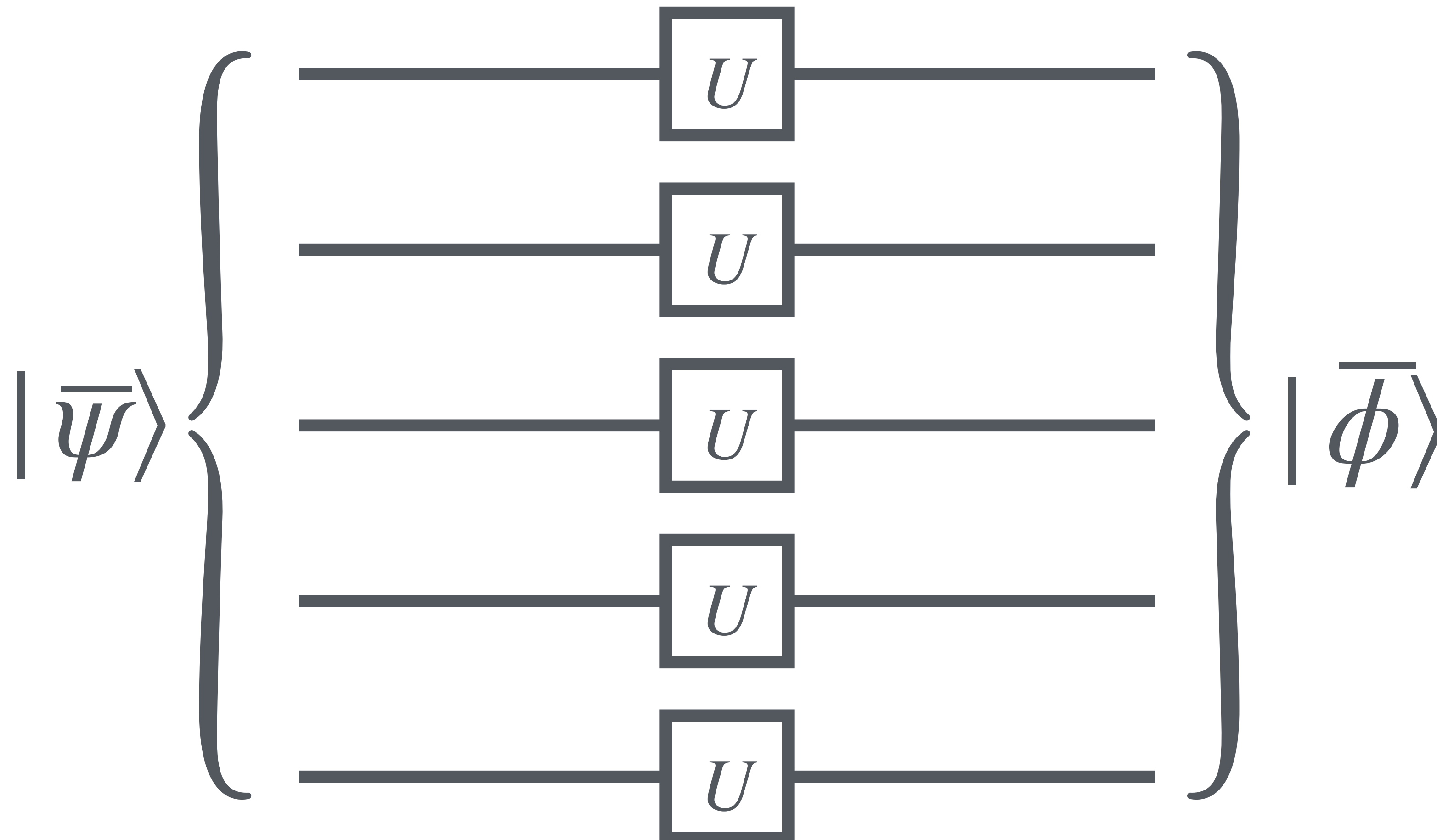
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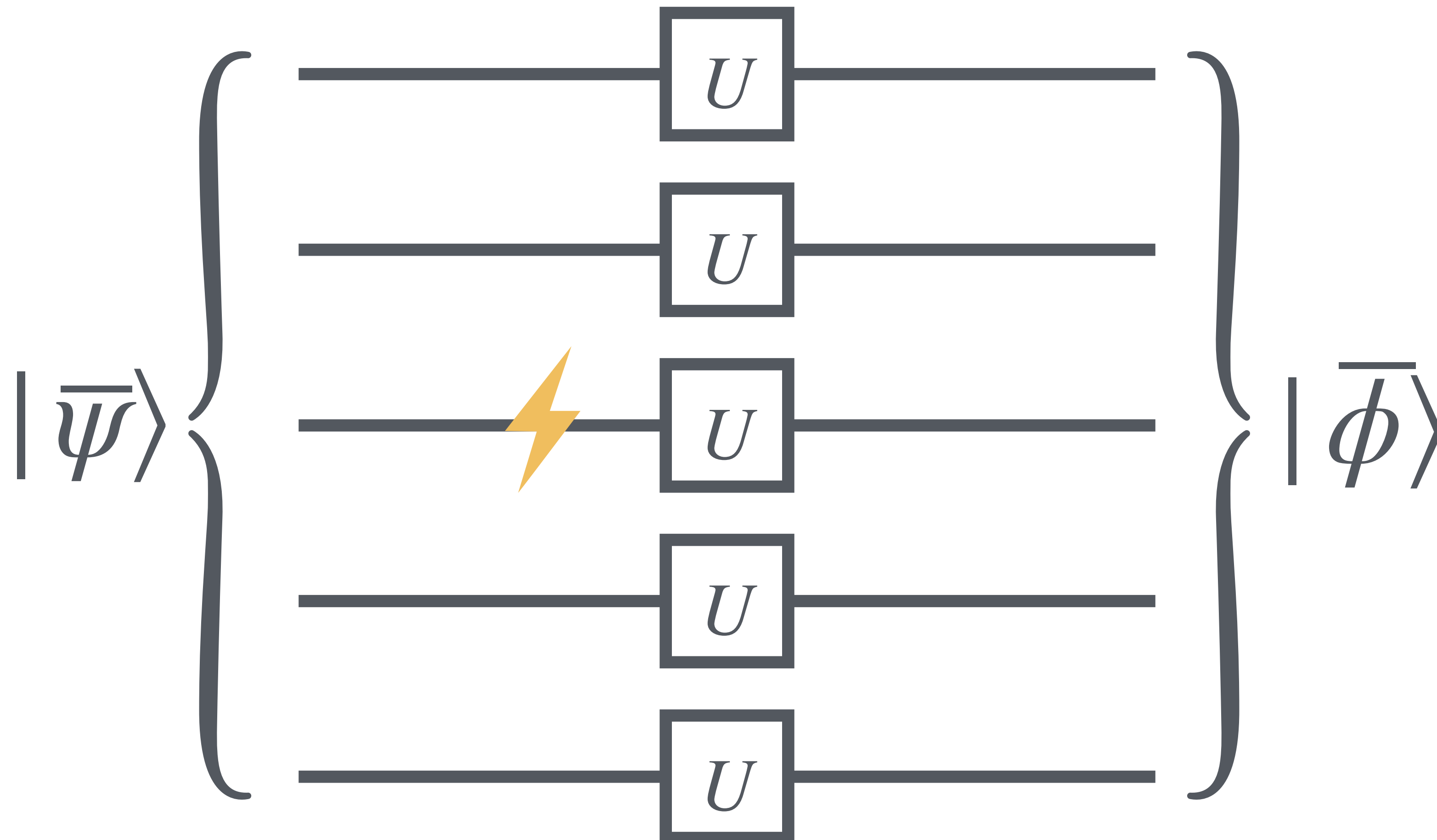
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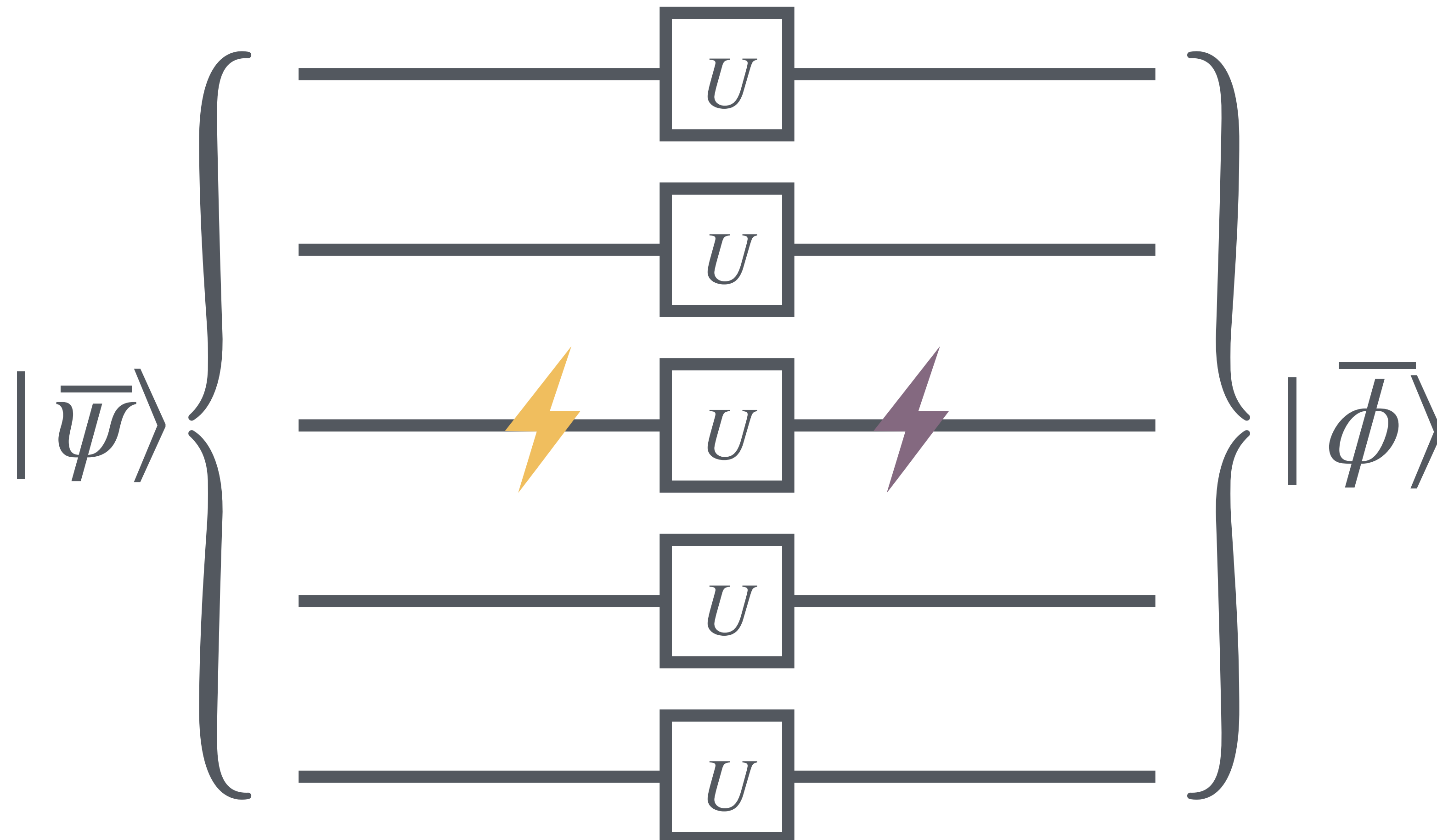
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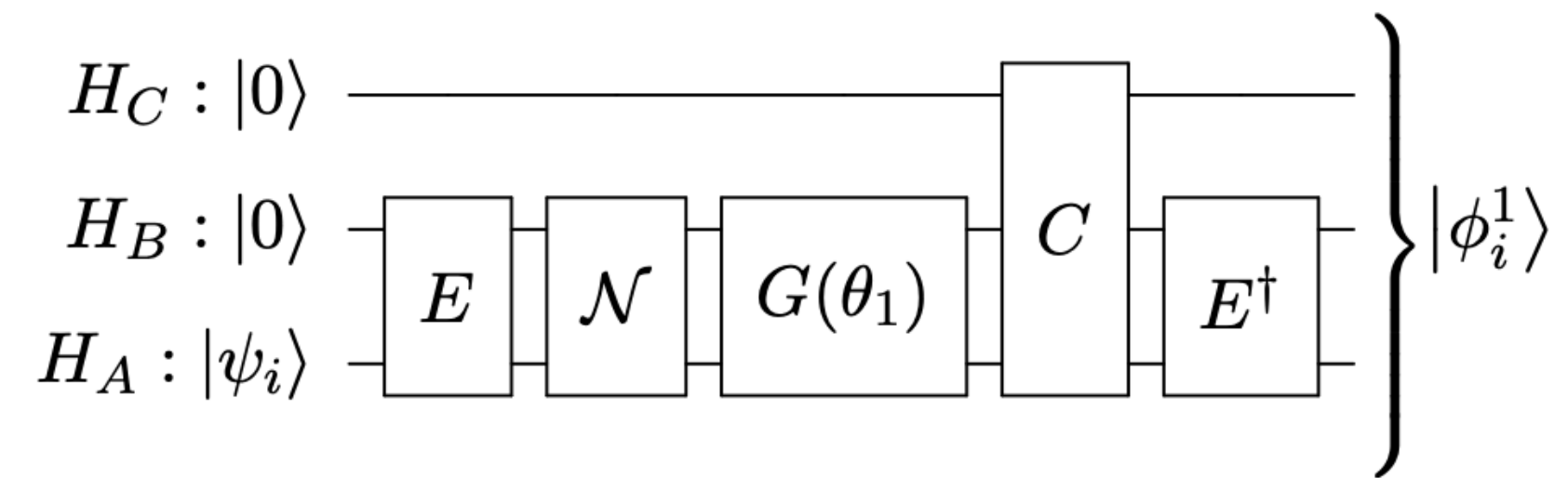
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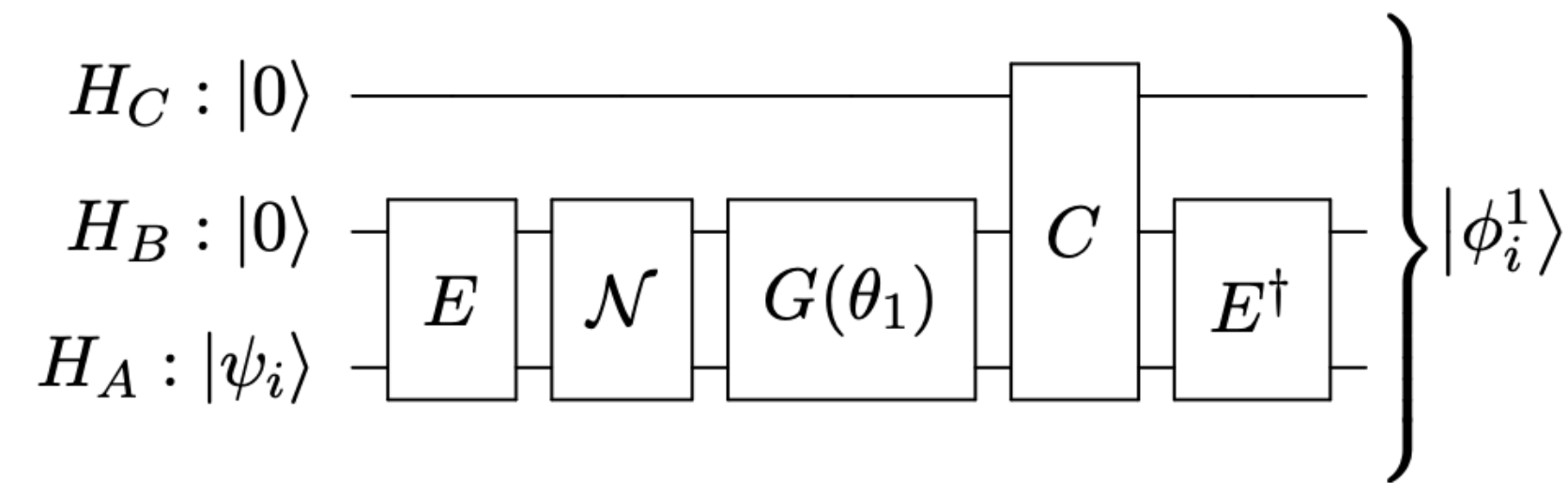
- Finding fault-tolerant logical gates
  - **Key problem** in quantum error correction theory
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- Can we **automate** the discovery of logical gates? Yes! [C**V**B+21]

# Automated discovery of logical gates

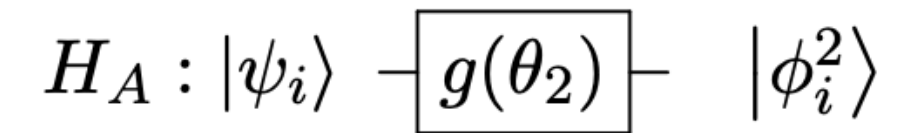


(a) Trial logical circuit

# Automated discovery of logical gates

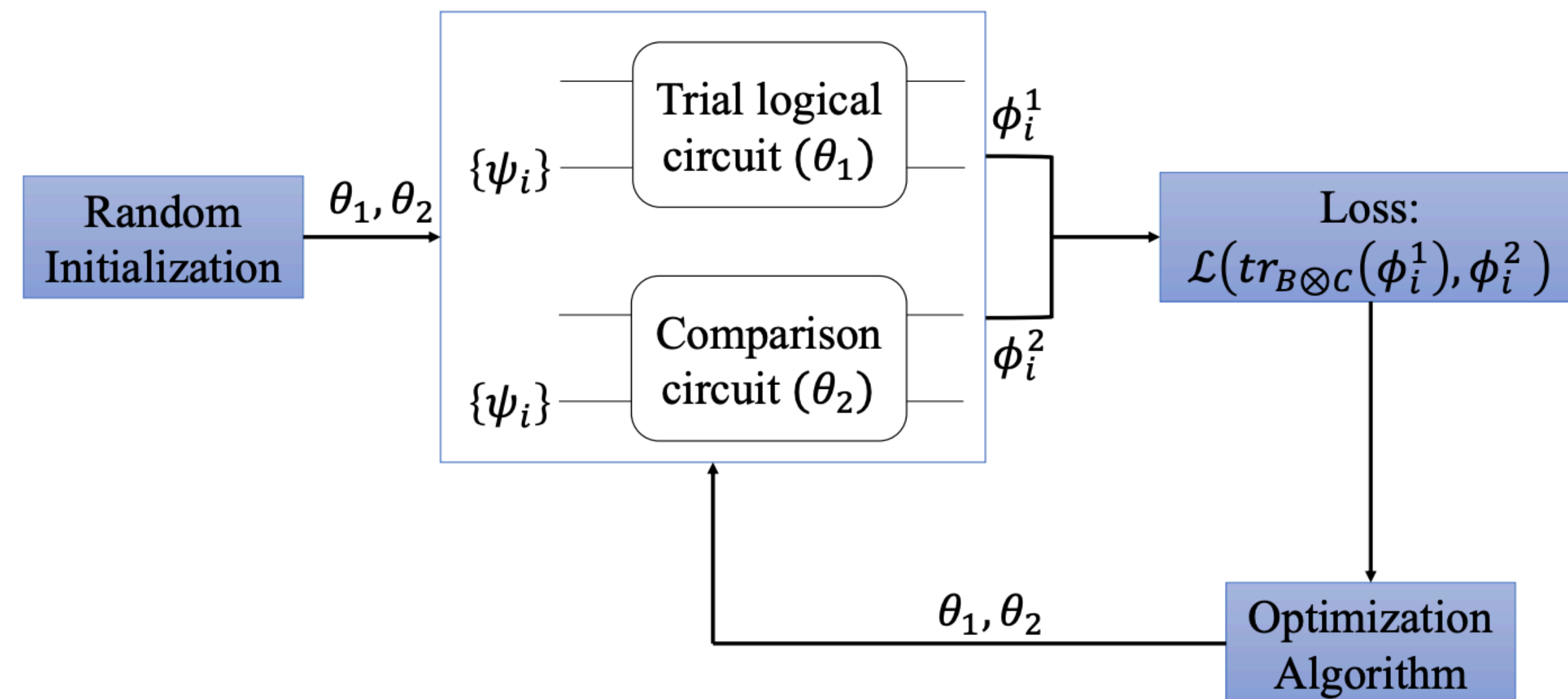
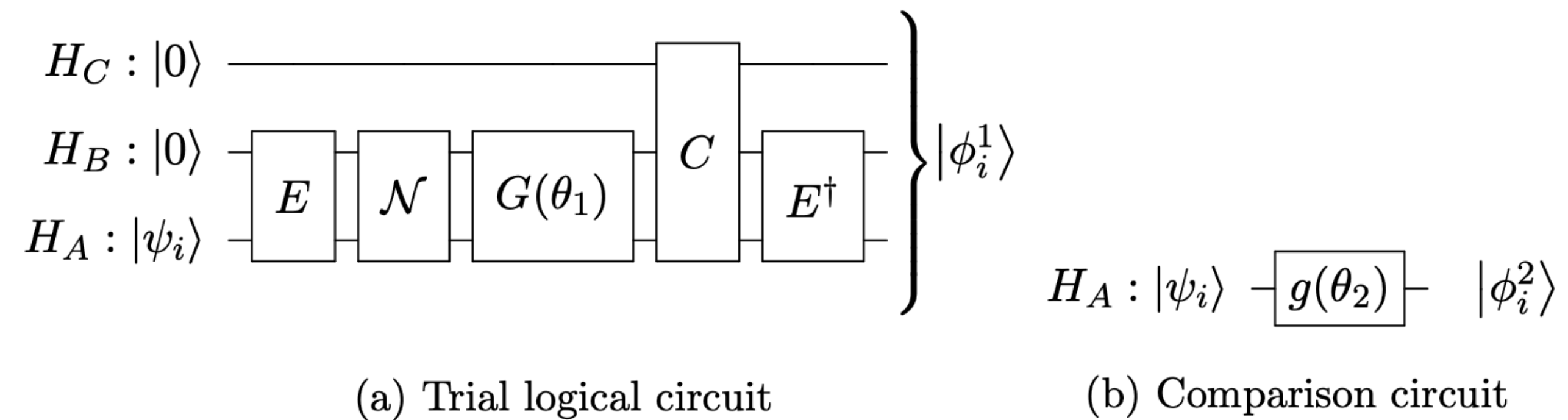


(a) Trial logical circuit



(b) Comparison circuit

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  - Many **limitations** on transversal gates e.g. [EK09] [BK13]

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  - Realistic error model almost always have **measurement errors**
  - **Constraints** for different **qubit technologies** can be very different

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# Thanks

- [mikevasmer.github.io](https://mikevasmer.github.io)
- Research Scientist @ Perimeter Institute // Senior Quantum Architecture Scientist @ Xanadu
- Thanks to the [\*\*Perimeter Institute Quantum Intelligence Lab \(PIQuIL\)\*\*](#) for early feedback on this talk!