

Lecture VI

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The threshold theorem :

Proof and assumptions

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Recall: an error model

is local stochastic if

for any set of faults

$$R, \Pr[S \subseteq R]$$

$$= \sum_{R|S \subseteq R} \Pr[R] \leq \rho^{|S|}$$

where $0 < \rho < 1$

$$\Pr[S \subseteq R] = \sum_{R|S \subseteq R} \Pr[R]$$

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iid $\Pr[R] = p^{|R|} (1-p)^{N-|R|}$

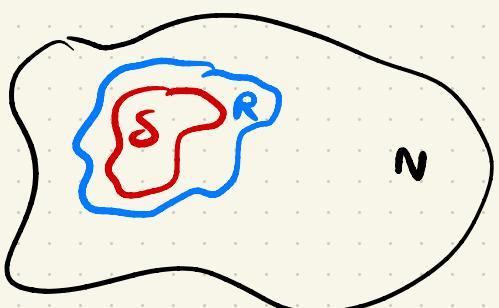
N total # of locations

$$\sum_{R|S \subseteq R} \Pr[R] =$$

$$p^{|S|} \sum_{R|S \subseteq R} p^{|R|-|S|} (1-p)^{(N-|S|)-(|R|-|S|)}$$

$$= p^{|S|} \sum_T p^{|T|} (1-p)^{M-|T|}$$

= 1



$$M = N - |S|$$

T is any

subset of the M locations

Recall : level reduction

Given a circuit C

we can construct a FT

circuit, which when subjected

to local stochastic noise w/

error rate p , is equivalent

to C subjected to local

stochastic noise w/ error

$$\text{rate } p' = \binom{A}{t+1} p^{t+1}$$

locations in largest ex Rec

If $\left(\frac{A}{t+1}\right) p^{t+1} < p$ then

our FT circuit is more reliable than C.

We can repeat this process to further reduce the error rate.

Def: Code concatenation

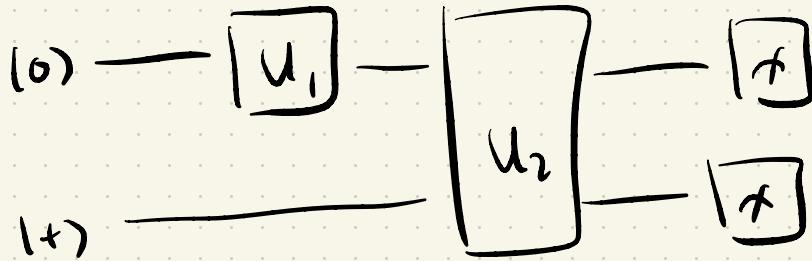
Given an $[[n, k, d]]$ code we take each physical

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qubit of the code and encode it again using the same code, giving an $[[n^2, 1, d^2]]$ code.

Repeating this L times gives a $[[n^L, 1, d^L]]$ code.

In a similar way we can define a concatenated FT simulation of a circuit.

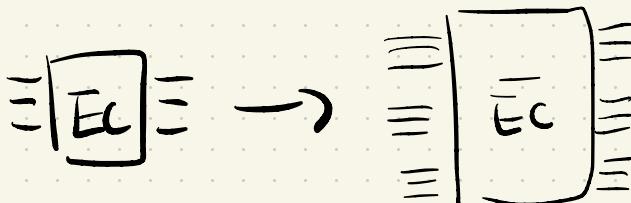


$$\begin{aligned} |\bar{0}\rangle &\equiv \boxed{\text{Ec}} \equiv \boxed{\bar{u}_1} \equiv \boxed{\text{Ec}} \equiv \boxed{\bar{u}_2} \equiv \boxed{\text{Ec}} \equiv \boxed{x} \\ |(\bar{+})\rangle &\equiv \boxed{\text{Ec}} \equiv \boxed{\bar{1}} \equiv \boxed{\text{Ec}} \equiv \boxed{\bar{u}_2} \equiv \boxed{\text{Ec}} \equiv \boxed{x} \end{aligned}$$

$\equiv \boxed{\text{Ec}} \equiv$ is a circuit

We encode this again in

the same code



Thm: $\exists P_T$ such that

if a system is subjected to
local stochastic noise w/
error prob. $p < P_T$, then for
any $\epsilon > 0$ & any circuit C
with T locations, there exists
a FT circuit with output
distribution within statistical
distance ϵ of the output
distribution of C (executed perfectly).

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The FT protocol uses resources
 (time, qubits, gates) that
 are a factor $\text{polylog}(\frac{T}{\epsilon})$
 greater than those of C.

Proof: Idea is to use a
 concatenated FT sim. w/
 L levels.

1st level of concatenation

$$P^{(1)} \leq \binom{A}{t+1} P^{t+1} \quad t = \left\lfloor \frac{d-1}{2} \right\rfloor$$

Define $P_T = 1 / \left(\frac{A}{t+1} \right)^{1/t}$

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$$P^{(1)} \leq P_T \left(\frac{P}{P_T} \right)^{t+1}$$

$$\frac{P^{(1)}}{P_T} = \left(\frac{P}{P_T} \right)^{t+1}$$

2nd level of concatenation

$$\frac{P^{(2)}}{P_T} \leq \left(\frac{P^{(1)}}{P_T} \right)^{t+1}$$

$$= \left(\left(\frac{P}{P_T} \right)^{t+1} \frac{P_T}{P_T} \right)^{t+1}$$

$$= \left(\frac{P}{P_T} \right)^{(t+1)^2}$$

L' 'th level of concatenation

$$\frac{P^{(L)}}{P_T} \leq \left(\frac{P}{P_T}\right)^{(t+1)^L}$$

If $P < P_T$ we can make
 the error arbitrarily small
 by choosing L large enough

We choose

$$L = \lceil \log_{t+1} \log_{P/P_T} (\epsilon / P_T) \rceil$$

$$\begin{aligned}
 L &= \lceil \log_{t+1} \log_{p/p_T} (\varepsilon / p_T T) \rceil \quad (12) \\
 &= \left\lceil \frac{\log_2 \left(\frac{\log_2 (T p_T / \varepsilon)}{\log (p_T / p)} \right)}{\log_2 (t+1)} \right\rceil \\
 &= \mathcal{O}(\log \log(T/\varepsilon))
 \end{aligned}$$

This gives

$$p^{(L)} \leq \varepsilon / T$$

\Rightarrow Prob of having a single logical fault is $\leq \varepsilon$
 (T locations in circuit) \square

This gives a lower bound on the error threshold p_T , but

What is p_T in practice?

(Billion dollar question!)

Example : Concatenated

Steane code

$$d=3 \quad t=1$$

$$P_T = 1/\binom{A}{2}$$

One can calculate e.g. 14

$$A = 679$$

$$\Rightarrow \binom{A}{2} = 230,181$$

$$p_T = 4.3 \times 10^{-6}$$

Highest proven threshold
value is for Knill's
scheme where $p_T > 10^{-3}$

In practice people often estimate the threshold using simulations.

(possible due to Gottesman Knill theorem)

For Knill's scheme

$$P_T \sim 3\%$$

For surface code

$$P_T \sim 7\%$$

In practice the polylog overhead can hide large constant factors.

e.g. surface code

$\sim 10^3$ physical qubits

needed per logical qubit!

But using certain special codes (low-density parity-check codes w/ additional properties) one can show that

FT q. comp. is possible
w/ constant overhead!

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Reducing the overhead for
practical FT schemes is
a v. important research
problem!

Assumptions behind

the threshold th

- ① Same error rates
for all locations

Not necessary

We can repeat our proof
but now p_T not a
number but a surface.

② Local error model

Necessary

Small-scale correlation
is included in local stochastic
error model

But long range correlation
will kill the threshold

This is a real problem

e.g. cosmic rays in
superconducting circuits

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long range gates

Not necessary

In concatenated codes we need we naively need long range connections between qubits.

We can avoid this by using SWAP gates or topological codes

④ Stochastic errors

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Not necessary (not fully proven)

There exists a threshold
than for coherent errors
but with a reduced
threshold (sim. for
non-Markovian errors).

But it's not clear if
this is a real effect
or an artefact of the

proof technique.

Coherent errors ℓ

non-Markovian errors

are difficult to simulate,

so we don't have much

numerical evidence one way

or another.

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