

Name:

CPU and von Neumann Architecture

A		CPU structure		B	Key vocab	
1		CU	4 _____ with the ALU, immediate access store and main memory to	9	The way the components of a computer are arranged.	
			5 _____ the functions of the CPU.	10	System architecture where the data is stored in the same place as the instructions	
2			A collection of 6 _____ with specific roles in the CPU	11	The cycle followed by the von Neumann architecture	
3		ALU	Takes two 7 _____ from the Accumulator and an	C CPU hardware		
			8 _____ from the CIR and returns a single result to the Accumulator	12	A connector which transfers data between components. Three types are data, address and control	
				13	Fast, expensive memory which is loaded from RAM and called by the CPU	
				Clock generator	A circuit which produces a	
					14 _____ wave, which is the	
				Core	15 _____ frequency a CPU can perform instructions	
					A processing unit which can run	
				16 _____ with others.		
				17 _____ Two cores		
				Multi-core 18 _____		
				Register	A section of	
					19 _____ memory	

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1		CU	4 _____ with the ALU, immediate access store and main memory to 5 _____ the functions of the CPU.	9		The way the components of a computer are arranged.
2			A collection of 6 _____ with specific roles in the CPU	10		System architecture where the data is stored in the same place as the instructions
3		ALU	Takes two 7 _____ from the Accumulator and an 8 _____ from the CIR and returns a single result to the Accumulator	11		The cycle followed by the von Neumann architecture
D			CPU vocab	C		CPU hardware
20			Set of instructions required to make the computer start	12		A connector which transfers data between components. Three types are data, address and control
21			The frequency which the CPU runs at, and the number of instructions which can be processed per second (measured in 22 _____)	13		Fast, expensive memory which is loaded from RAM and called by the CPU
23			Run the CPU at a higher clock speed than its default	Clock generator		A circuit which produces a 14 _____ wave, which is the 15 _____ frequency a CPU can perform instructions
				Core		A processing unit which can run 16 _____ with others.
				17		Two cores
				Multi-core		18
				Register		A section of 19 _____